

Linearization

$$V_c = v_{cin} (i_{in} - i_s) + v_{cin}$$

$$\Rightarrow \Delta V_c = v_{cin} (\Delta i_{in} - \Delta i_s) + \Delta v_{cin}$$

$$\Rightarrow V_{co} = v_{cin} (i_{no} - i_{so}) + v_{cno}$$

$$p v_{cin} = \frac{1}{c_m} (i_{in} - i_s)$$

$$\Rightarrow p \Delta v_{cin} = \frac{1}{c_m} (\Delta i_{in} - \Delta i_s)$$

$$0 = \frac{1}{c_m} (i_{no} - i_{so})$$

Average Value, Small Signal Equivalent Circuit

$$I_s = \frac{P}{V_c}$$

$$I_{s0} = \frac{P}{V_{c0}}$$

$$\Delta I_s = \left. \frac{\partial I_s}{\partial V_c} \right|_{V_{c0}} \Delta V_c = - \frac{P}{V_{c0}^2} \Delta V_c$$

$$\Delta I_s = \frac{\Delta V_c}{R} \quad R = - \frac{V_{c0}^2}{P}$$

Average Value, Small Signal Equivalent Circuit

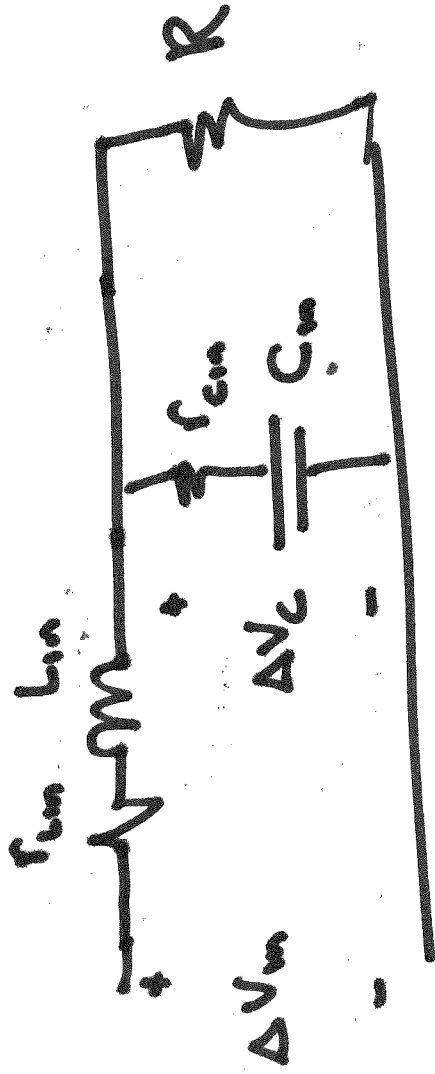
$$\frac{V_{co} - V_{mo}}{r_{Lin}} + \frac{\beta P}{V_{co}} = 0$$

$$V_{co} (V_{co} - V_{mo}) + \beta r_{Lin} = 0$$

$$V_{co}^2 - V_{co} V_{mo} + \beta r_{Lin} = 0$$

$$V_{co} = \frac{V_{mo} \pm \sqrt{V_{mo}^2 - 4 \beta r_{Lin}}}{2}$$

Characteristic Polynomial



$$\Delta V_c (Cp) - \Delta V_{in} \text{ stuff} = 0$$

$$\frac{\Delta V_c - \Delta V_{in}}{r_{Lm} + L_{mS}} + \frac{\Delta V_c C_{mS}}{r_{Cm} + C_{mS}} + \frac{\Delta V_c}{R} = 0$$

$$r_{Cm} C_{mS} + 1$$

$$(\Delta V_c - \Delta V_{in})(r_{Cm} C_{mS} + 1)R + \Delta V_c C_{mS}(r_{Lm} + L_{mS})R$$

$$+ \frac{\Delta V_c}{R}(r_{Lm} + L_{mS})(r_{Cm} C_{mS} + 1) = 0$$

$$CP: [C_{in} L_{in} R + L_{in} C_{in} R_{in}] S^2$$

$$+ [R_{in} C_{in} R + C_{in} R_{in} R] S + L_{in} + R_{in} C_{in} R_{in} S$$

$$+ [R + R_{in}]$$

$$S = \frac{-b \pm \sqrt{b^2 - 4ac}}{2a}$$

2a

Special Case ($r_{Cin} = 0$)

$$\text{C.P.: } [C_{in} L_{in} R] s^2 + [C_{in} r_{in} R + L_{in}] s + [R + r_{in}]$$

$$\text{C.P.: } [C_{in} L_{in}] s^2 + [C_{in} r_{in} + \frac{L_{in}}{R}] s + [1 + r_{in} \frac{R}{L_{in}}]$$

If $a > 0$ then a necessary & sufficient condition for the roots to be in the LHP

$$1) \quad b > 0 \quad c > 0$$

$$V_{in} = \frac{1}{2}(V_1 - V_2)$$

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Special Case ($r_{cin} = 0$)

$$1 + \frac{r_{lin}}{R} > 0$$

$$1 - \frac{r_{lin}}{V_{CO}^2 P} > 0$$

$$1 > \frac{r_{lin} P}{V_{CO}^2}$$

$$P < \frac{V_{CO}^2}{r_{lin}}$$

$$C_{in} r_{lin} + \frac{L_{in}}{R} > 0$$

$$C_{in} r_{lin} - \frac{L_{in}}{V_{CO}^2 P} > 0$$

$$C_{in} r_{lin} > \frac{L_{in} P}{V_{CO}^2}$$

$$P < \frac{C_{in} r_{lin} V_{CO}^2}{L_{in}}$$

C is so odd

L is evil

AVM of Hysteresis Control

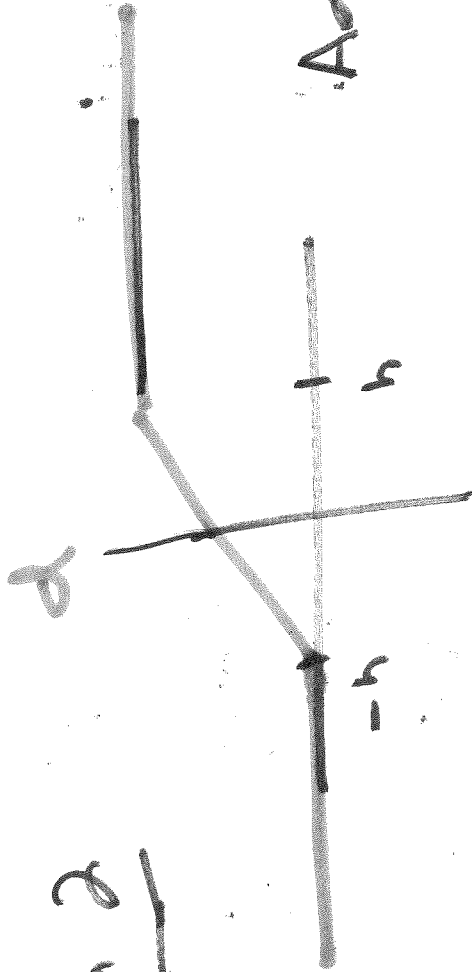
- Consider the following:

Approach 1

Assume $i_e = i_e^*$

Disadvantage: Loss of current tracking

Approach 2



Advantage: Simple

- Voltage limits are recognized

$i_e^* \neq i_e$

Disadvantage: Stiffness

**ECE61016 Power Electronics
Converters and Systems**

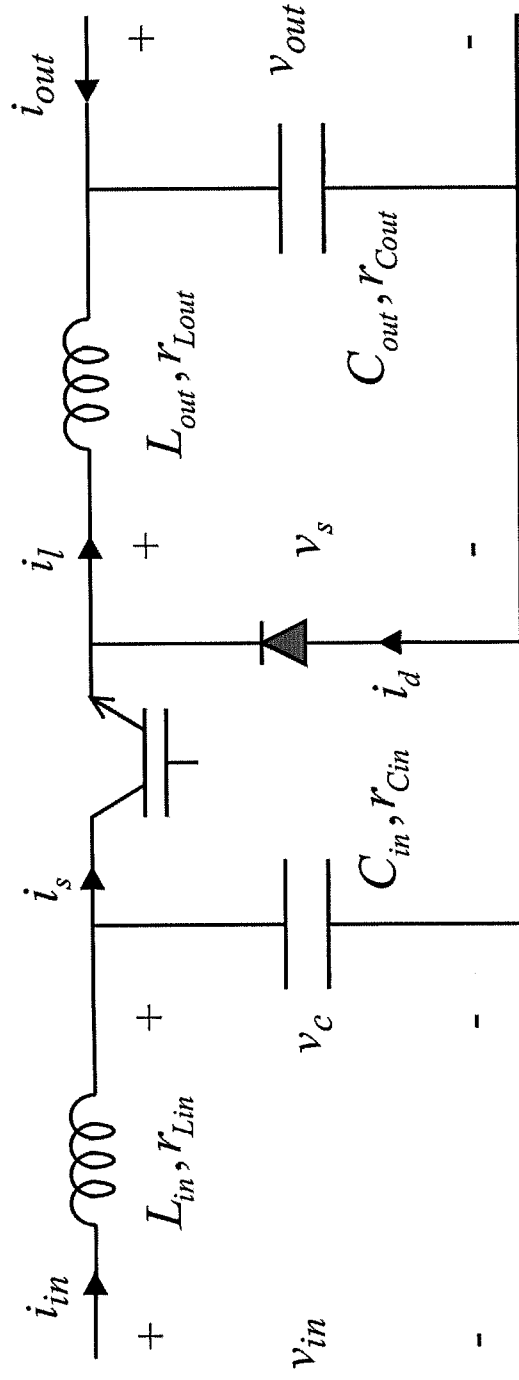
**Lecture Set 3E:
Control of a Buck Converter (Example)**

S.D. Sudhoff

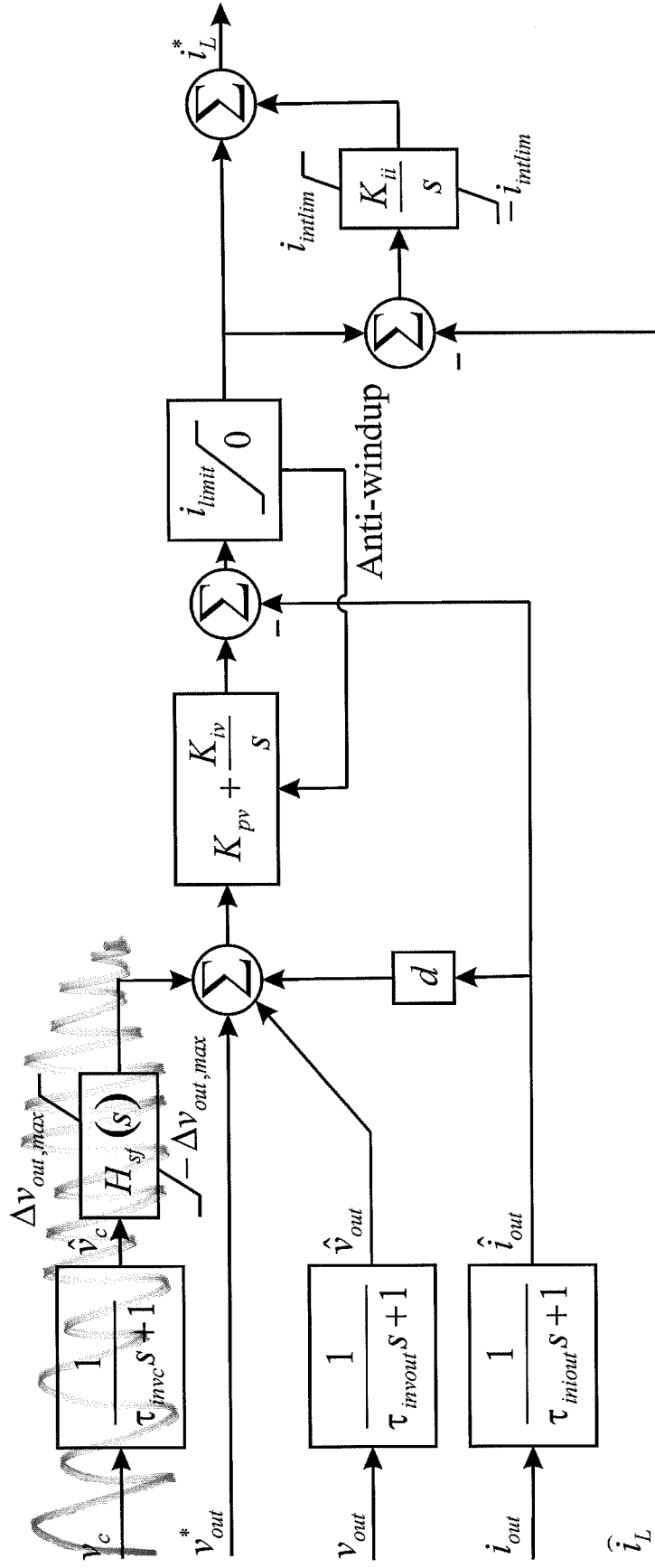
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Buck Converter Control

- Topology



Buck Converter Control



Specifications and Parameters

- Input voltage: 500 V
- Output voltage: 425 V
- Resistive load: steps from 500 W to 5 kW at $t=0.1$ s
- Input inductor: 0.5 mH, 100 m Ω
- Input capacitor: 450 μ F, 130 m Ω
- Output inductor: 3.0 mH, 120 m Ω
- Output capacitor: 450 μ F, 130 m Ω
- Reverse biased resistance: 1 k Ω
- Hysteresis level: 0.5 A
- Droop: 0

Switching Frequency

$$t_{on} = \frac{\Delta V_{out}}{V_m - V_{fsw} - r_{Lout} I_L - V_{out}}$$

$$t_{off} = \frac{\Delta V_{out}}{V_{fd} + r_{Lout} I_L + V_{out}}$$

$$f_{sw} = \frac{1}{t_{on} + t_{off}}$$

Light load: $f_{sw} = 20.7 \text{ KHz}$

Heavy load: $f_{sw} = 20.6 \text{ KHz}$

$f_{sw} = 20.8 \text{ KHz}$

Input and Output Filter

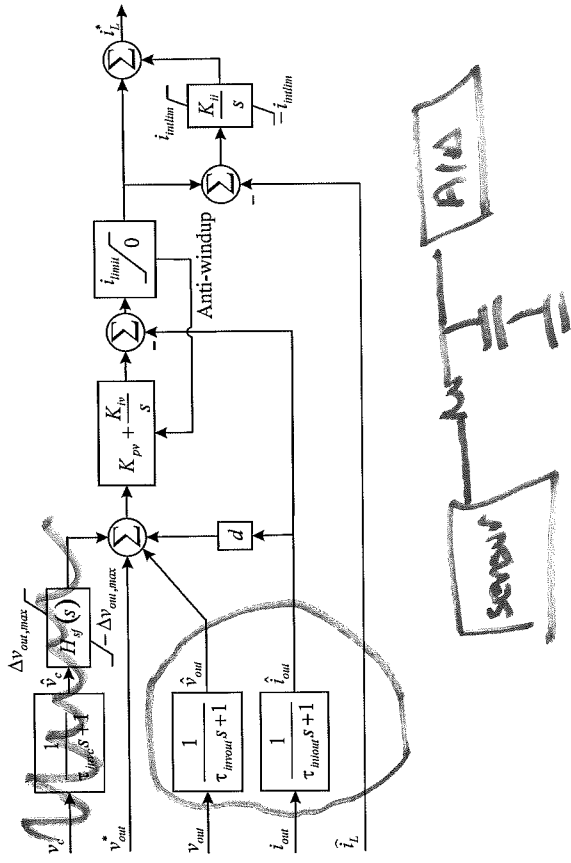
$$H(s) = \frac{1}{\tau s + 1}$$

$$\gamma_{low} = 10$$

$$\gamma_{2\pi f_{bw}} = 10$$

$$\gamma = 76.6 \text{ ms}$$

$$\tau_{inveut} = \tau_{inveut}$$



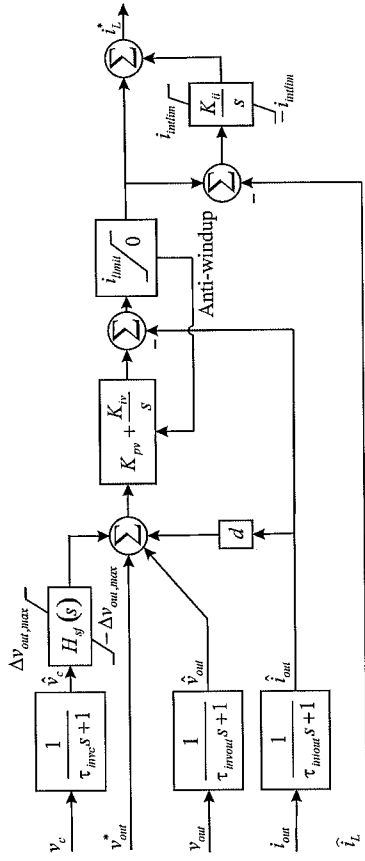
Voltage Control Gains

D.C.P.: $(s-s_1)(s-s_2)$

~~$-\frac{2\pi \text{kr}}{10}$~~

$-\frac{2\pi \text{kr}}{100}$

$s_1 = s_2 = -1306$



Analysis of Voltage Control Loop

- Thus, in order to get poles at $s = s_1, s_2$
- Set gains to

$$K_{iv} = s_1 s_2 C_{out} (1 + r_{out} K_{pv}) = 900$$

$$K_{pv} = - \frac{C_{out}}{1} + \frac{C_{out} r_{out}}{s_1 + s_2 + s_1 s_2 r_{out} C_{out}} = 1.33$$

⇒ check against ~~the~~ ^{out} loop time

Anti-Windup

Recall

$$U = \frac{K_v s + K_i}{s + K_i K_i} e + i_{limit}$$

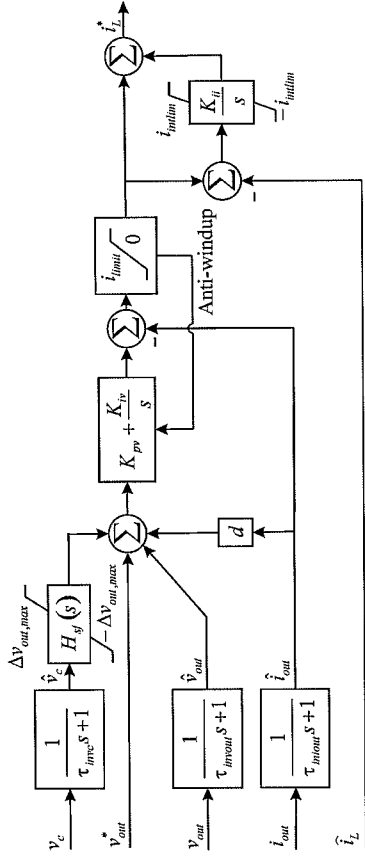
$$U = \frac{e'}{s + K_v K_i}$$

$$\dot{U} + K_v K_i U = e'$$

$$x = a x + \text{stuff}$$

numerical stability with Forward Euler

$$h < -\frac{a}{a}$$



$$h < -\frac{a}{-K_v K_i}$$

$$K_v K_i < \frac{2}{h}$$

$$K_i < \frac{2}{h K_v}$$

$$K_i = \frac{1}{h K_v} = 11.1$$

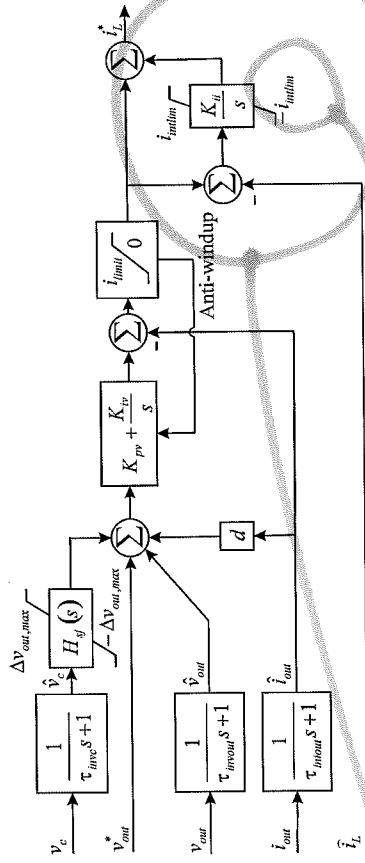
numerical stability with Forward Euler

Current Loop

$$G_c = \frac{1}{\tau_{inv} s + 1} + \frac{s \Delta}{s + K_{ii}}$$

$$s = -\frac{2\pi \cdot 600}{100}$$

$$K_{ii} = 1306$$



$h(1.25) = 0.625 \text{ A}$
 \rightarrow hysteresis level h
 (0.5 A)

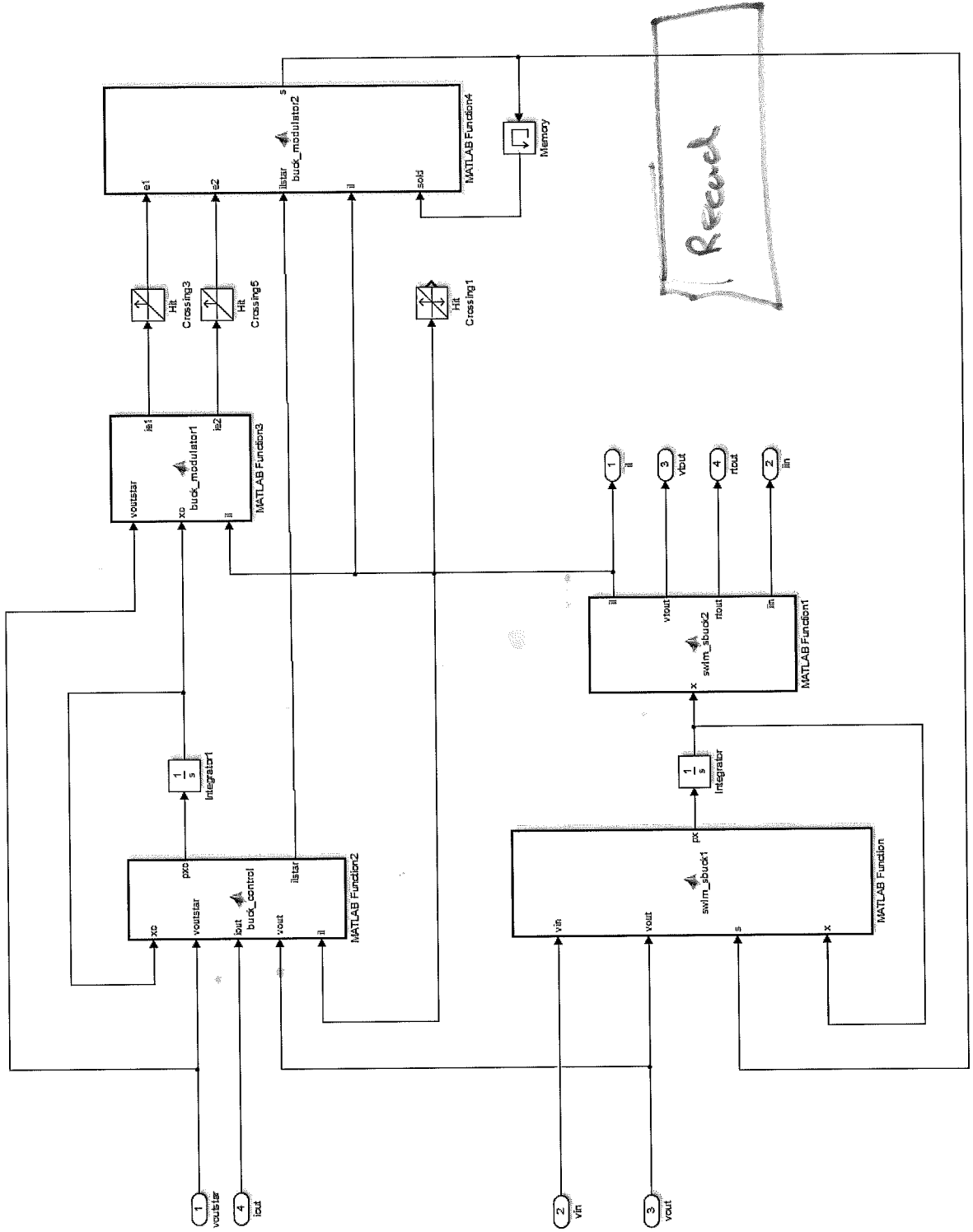
Stop in the middle.
 Check you hit the limit.

Comments on Average Value Model

- Discontinuous mode with hysteresis control



Comments on Waveform Level Model



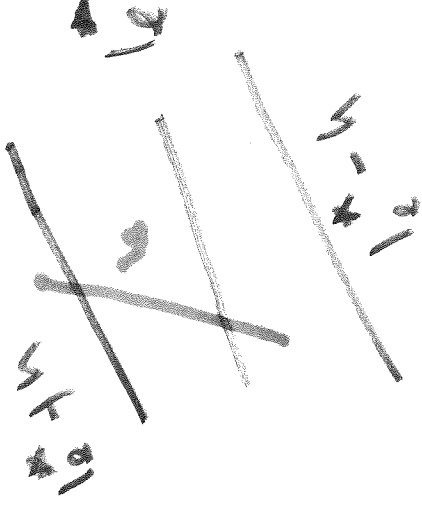
Comments on Waveform Level Model

- buck_modulator 1

...

```
ie1=il-(ilstar+p.h);
```

```
ie2=(ilstar-p.h)-il;
```



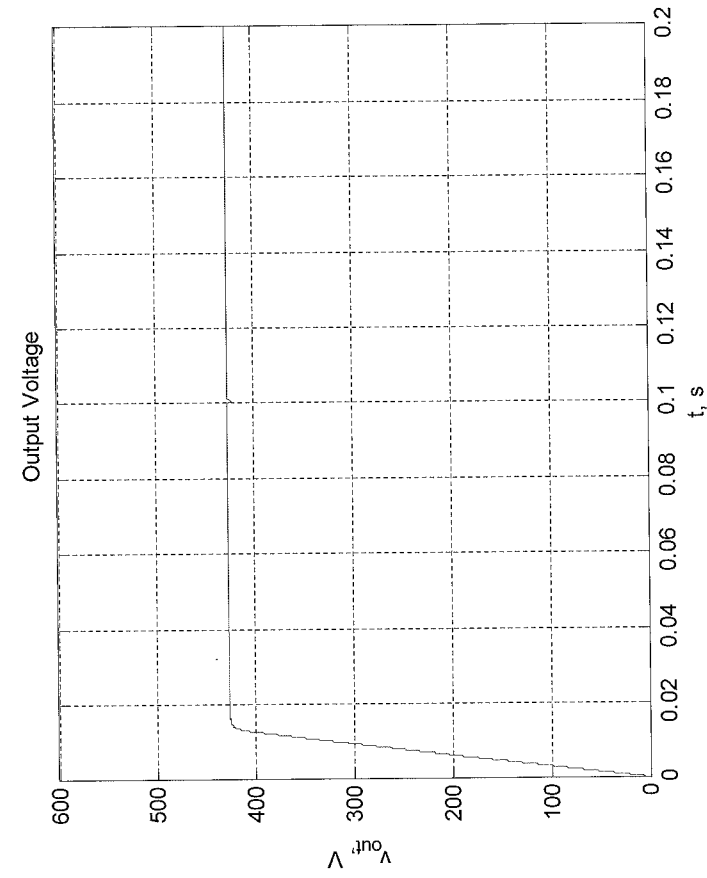
Comments on Waveform Level Model

- buck_modulator 2

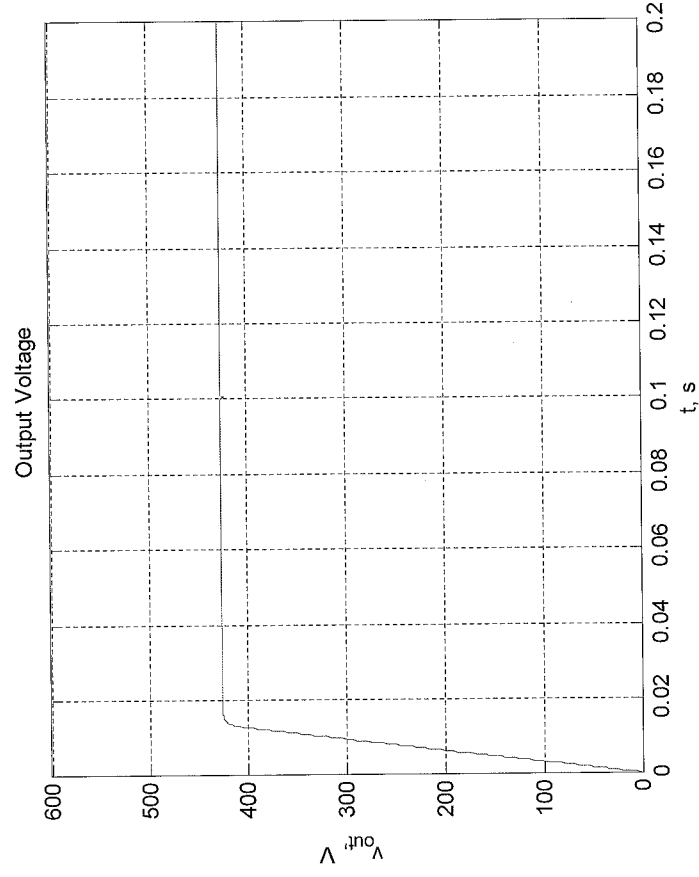
```
% compute current errors relative to hysteresis bands
ie1=il-(ilstar+1.5*P.h);
ie2=(ilstar-1.5*P.h)-il;

% determine the switch state
if (e1||(ie1>0))
    s=0.0;
else
    if (e2||(ie2>0))
        s=1.0;
    else
        s=sold;
    end
end
end
```

Simulation – Output Voltage

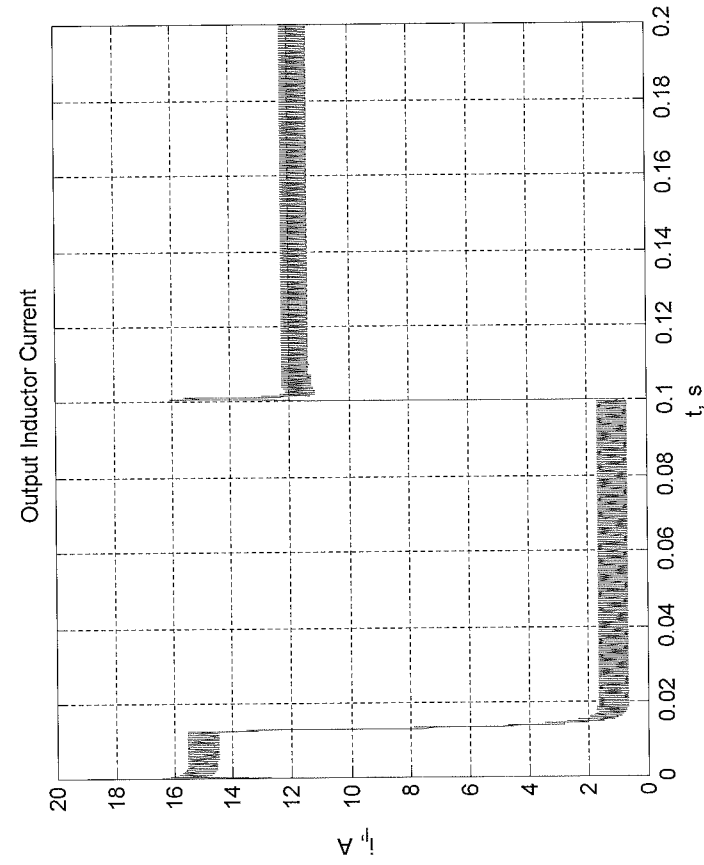


Waveform Level Model

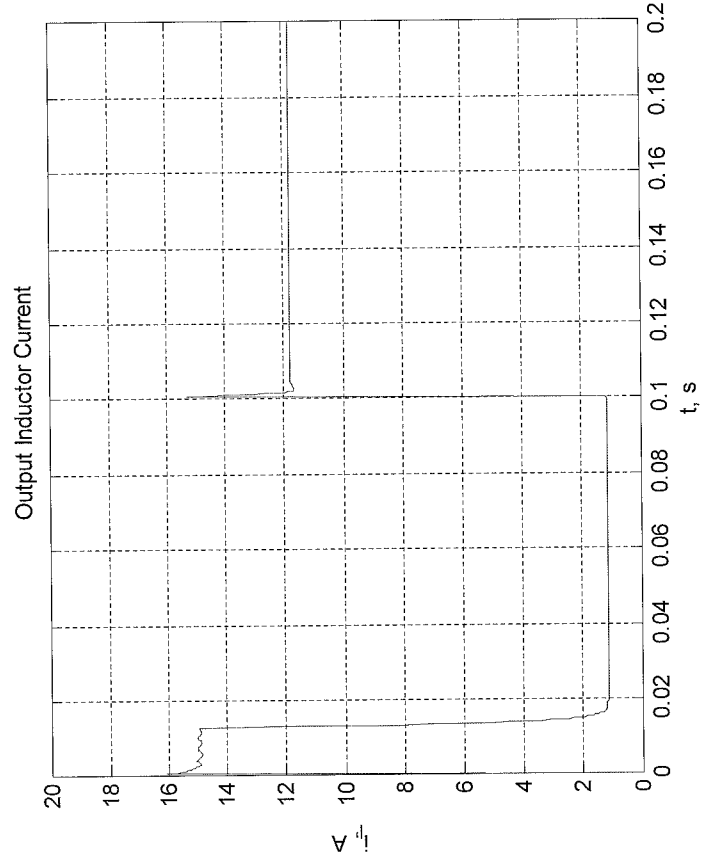


Average Value Model

Simulation – Inductor Current

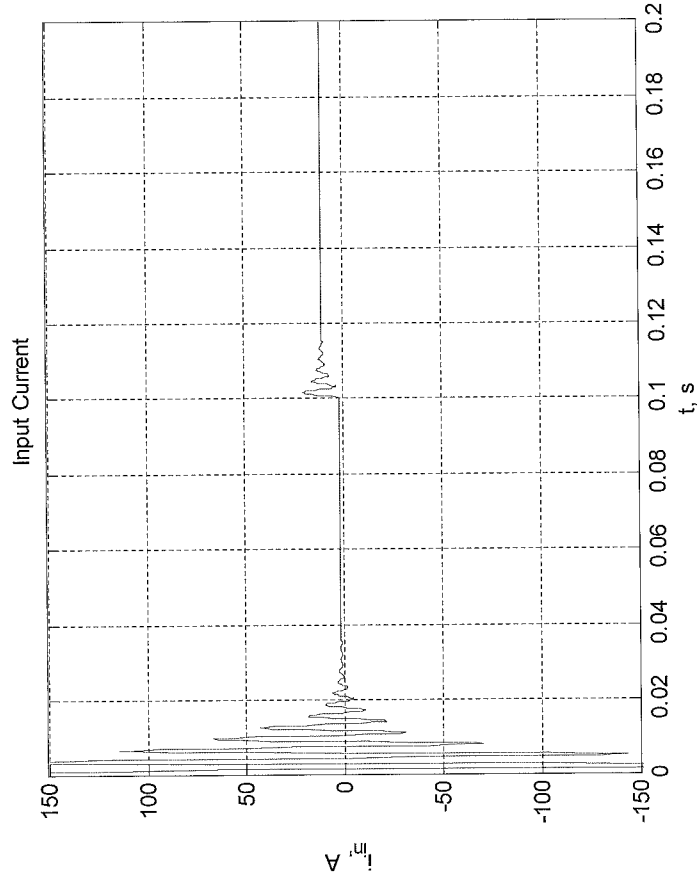
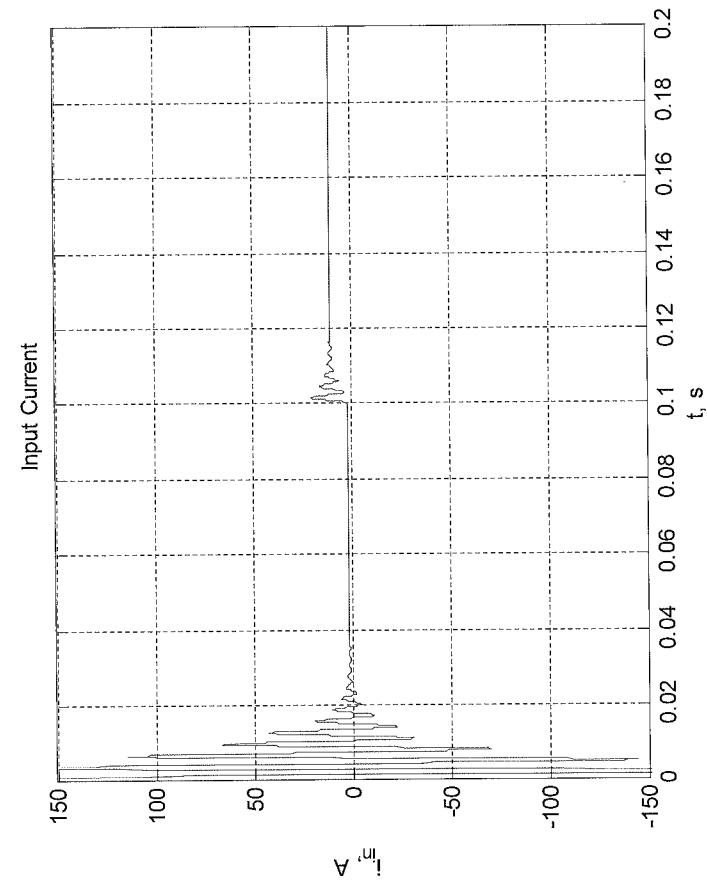


Waveform Level Model



Average Value Model

Simulation – Input Current



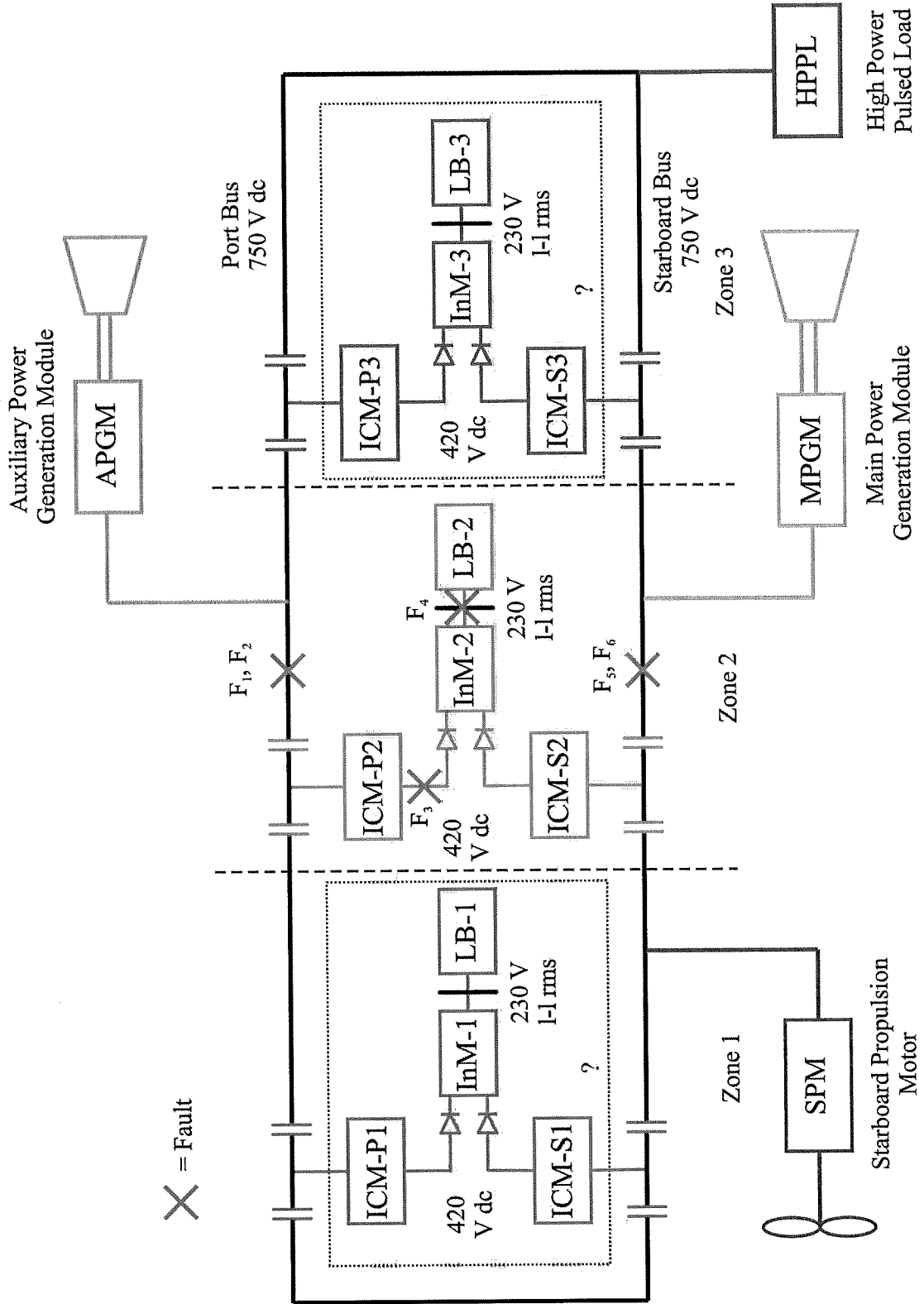
**ECE61016 Power Electronics
Converters and Systems**

**DC/AC Conversion
Lecture Set 4**

S.D. Sudhoff

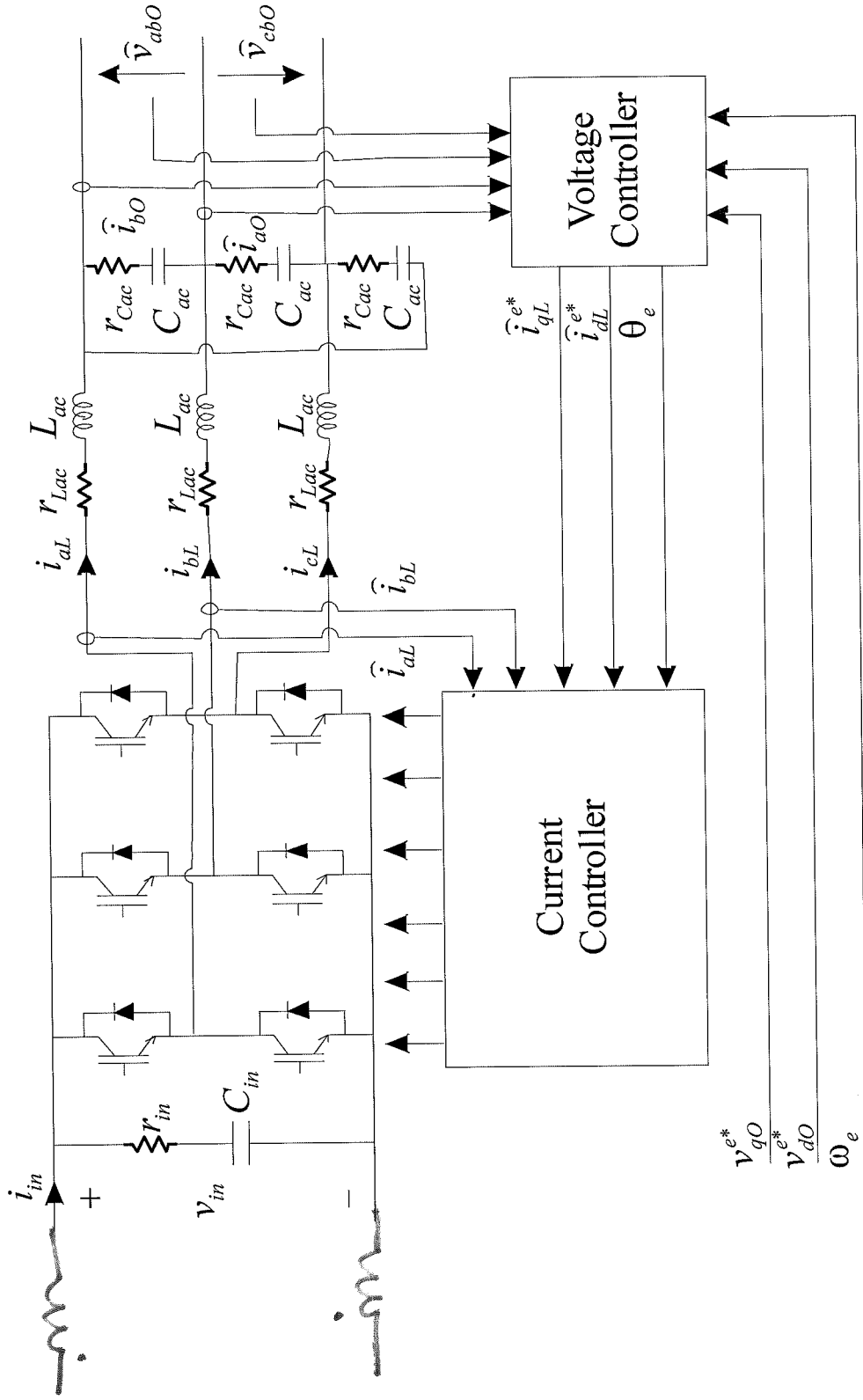
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Context

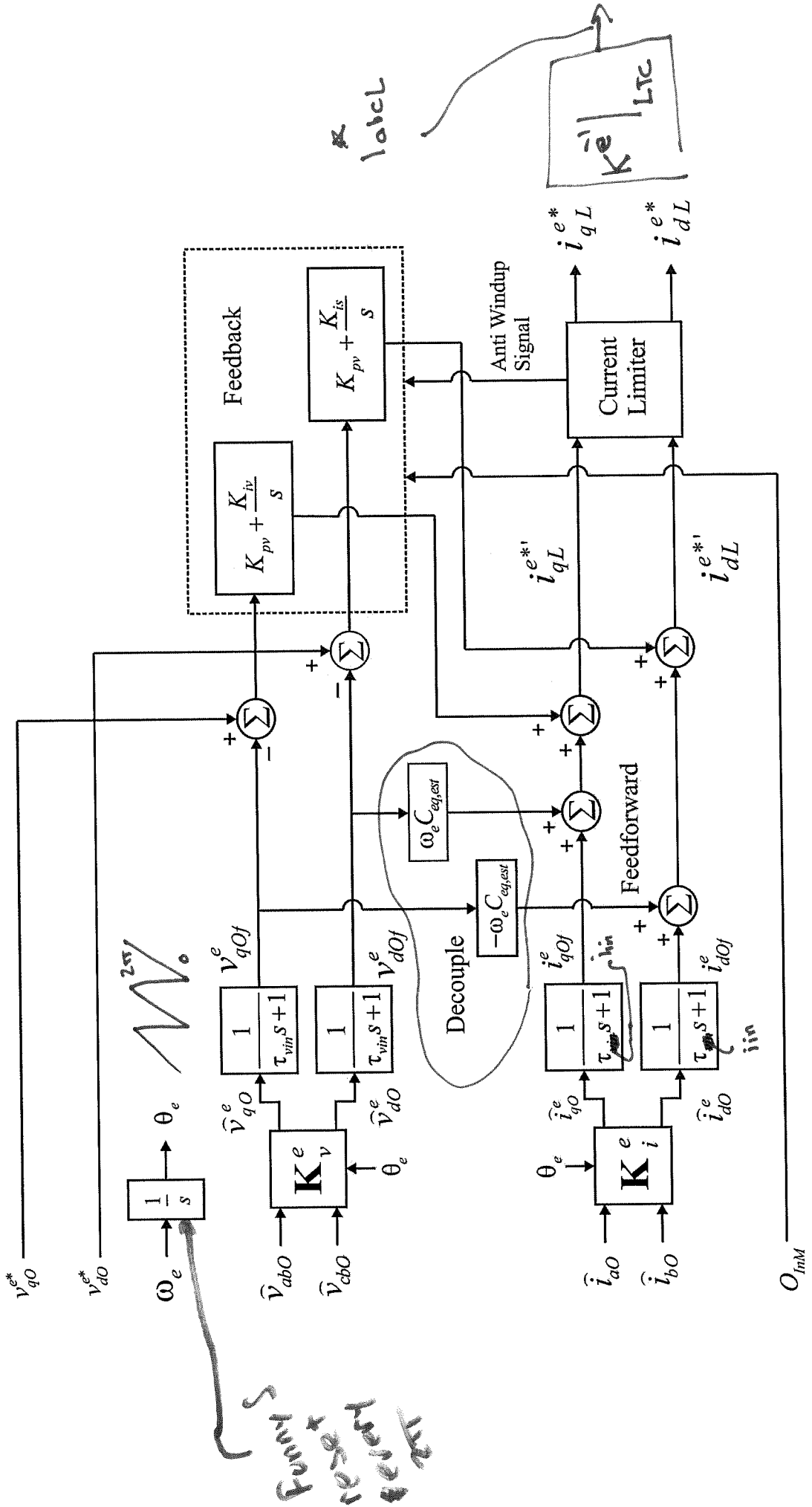


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Inverter Module (InM) Overview



Voltage Control



Voltage Control

- Why QD Variables? \rightarrow ~~Everything is dc~~ \rightarrow Partly decouple system

- Why the Synchronous Reference Frame?

\rightarrow control

\rightarrow Everything is dc

For modeling \rightarrow Stationary ref. frame

Comment 1: The Voltage Transformation

$$\mathbf{K}_v^e = \frac{2}{3} \begin{bmatrix} \cos(\theta_e) & \cos(\theta_e + 2\pi/3) \\ \sin(\theta_e) & \sin(\theta_e + 2\pi/3) \end{bmatrix}$$

$$V_{qdo}^e = K_v^e \begin{bmatrix} \widehat{V}_{abo} \\ \widehat{V}_{cbo} \end{bmatrix} \quad \text{measured}$$

$$C = \cos(\theta_e)$$

$$C^- = \cos(\theta_e - 2\pi/3)$$

etc.

Ideally

$$V_{qdo}^e = K_s^e V_{dc0}$$

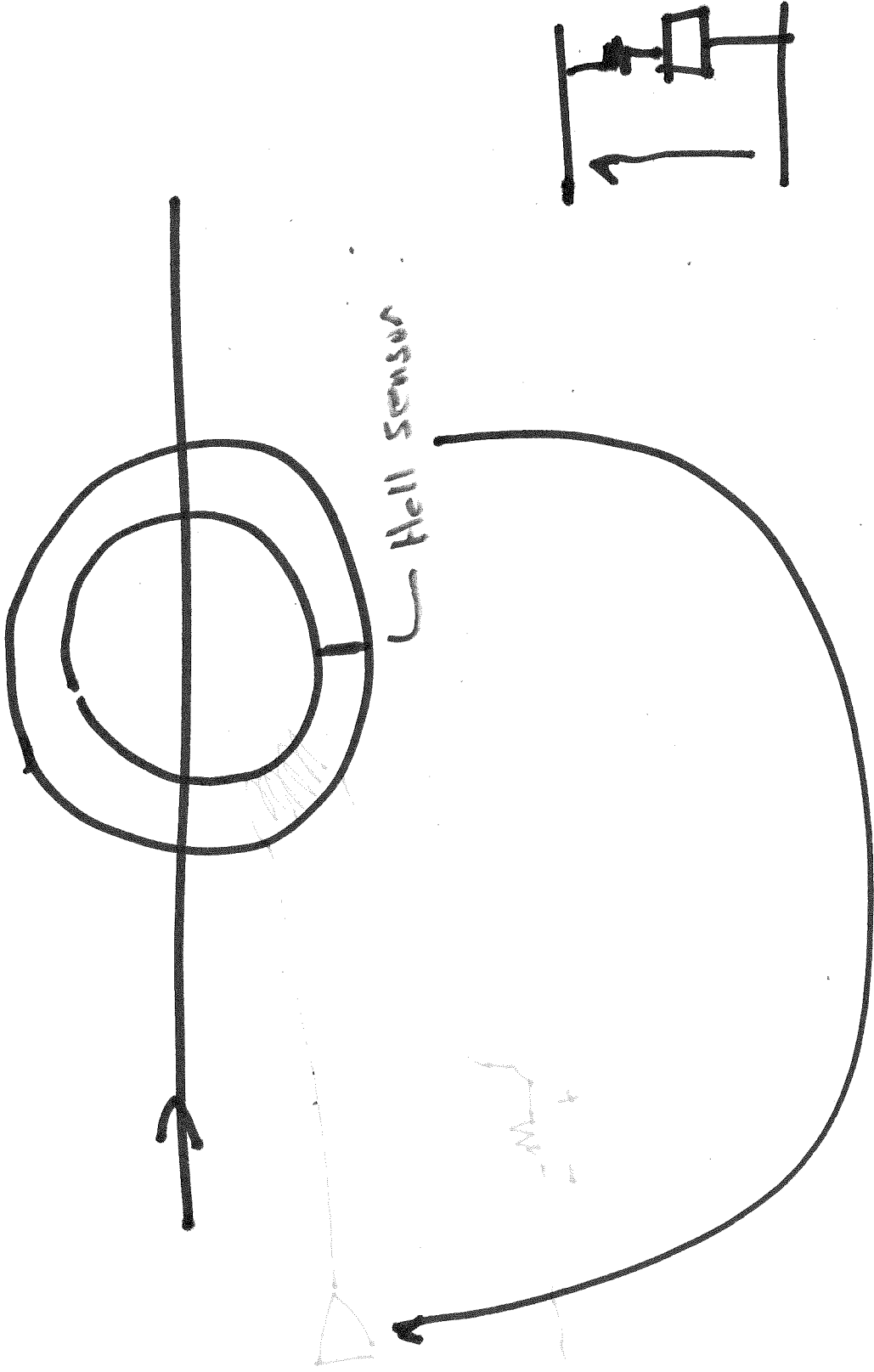
$$= \frac{2}{3} \begin{bmatrix} C & C^- & C^+ \\ S & S^- & S^+ \\ \frac{1}{2} & \frac{1}{2} & \frac{1}{2} \end{bmatrix} \begin{bmatrix} V_{ao} \\ V_{bo} \\ V_{co} \end{bmatrix} \begin{bmatrix} -V_{bo} \\ -V_{co} \\ -V_{ao} \end{bmatrix}$$

$$C + C^- + C^+ = 0$$

$$V_{qdo}^e = \frac{2}{3} \begin{bmatrix} C \\ S \end{bmatrix} \begin{bmatrix} V_{abo} \\ V_{cbo} \end{bmatrix} K_v^e$$

Current Measurement

Voltage Transformation Cont.



Comment 2: The Current Transformation

$$K_i^e = \frac{2}{\sqrt{3}} \begin{bmatrix} \cos(\theta_e - \pi/6) & \sin(\theta_e) \\ \sin(\theta_e - \pi/6) & -\cos(\theta_e) \end{bmatrix}$$

$$i_{gdo} = K_s^e i_{abc}$$

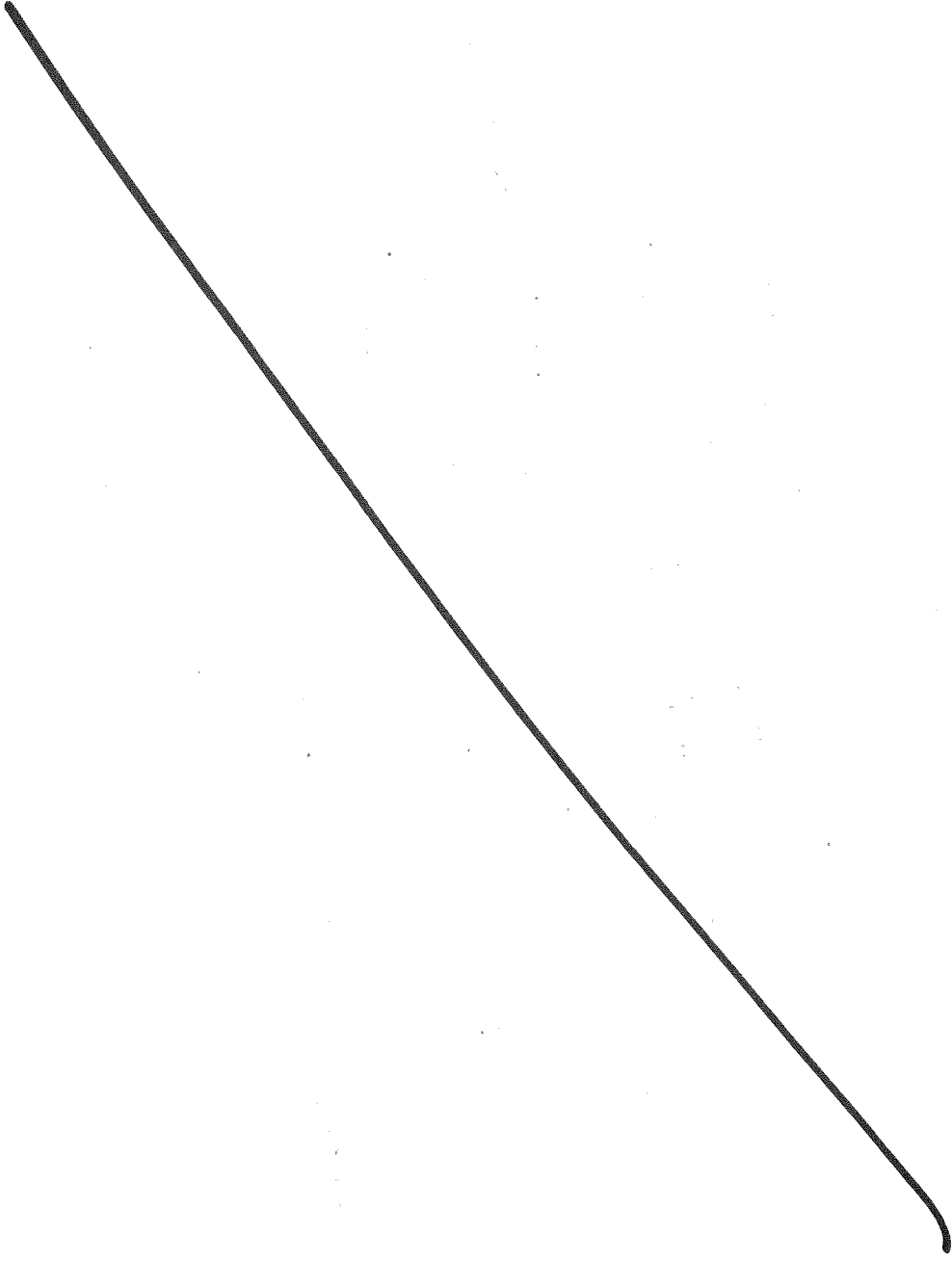
$$= \frac{2}{\sqrt{3}} \begin{bmatrix} c & c^- & c^+ \\ s & s^- & s^+ \\ \frac{1}{2} & \frac{1}{2} & \frac{1}{2} \end{bmatrix} \begin{bmatrix} i_{a0} \\ i_{b0} \\ -i_{a0} - i_{b0} \end{bmatrix}$$

$$= \frac{2}{\sqrt{3}} \begin{bmatrix} c - c^+ & c^- - c^+ \\ s - s^+ & s^- - s^+ \end{bmatrix} \begin{bmatrix} i_{a0} \\ i_{b0} \end{bmatrix}$$

K_i^e

After we do the trig

Current Transformation Cont.



Current Control

Normally

$$B \text{ label} = K_s^{-1} \begin{bmatrix} 1 & 0 & 0 \\ 0 & 1 & 0 \\ 0 & 0 & 1 \end{bmatrix}$$

$$= \begin{bmatrix} v^- & v^+ & v^+ \\ s^- & s^- & s^+ \\ 1 & 1 & 1 \end{bmatrix} \begin{bmatrix} 1 \\ 0 \\ 0 \end{bmatrix}$$

$$= \begin{bmatrix} v^- & v^+ & v^+ \\ s^- & s^- & s^+ \\ 1 & 1 & 1 \end{bmatrix} \begin{bmatrix} 1 \\ 0 \\ 0 \end{bmatrix}$$

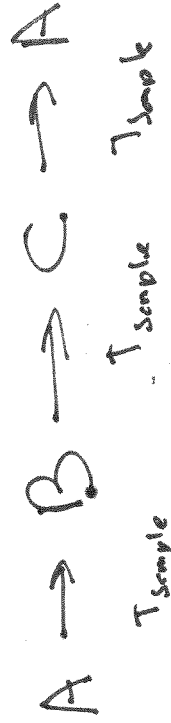
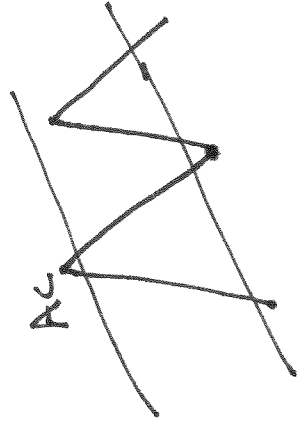
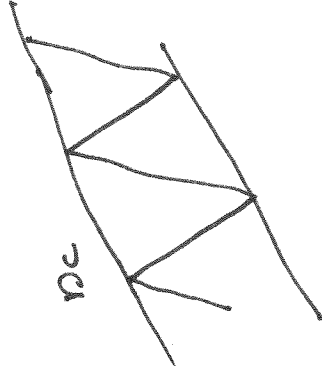
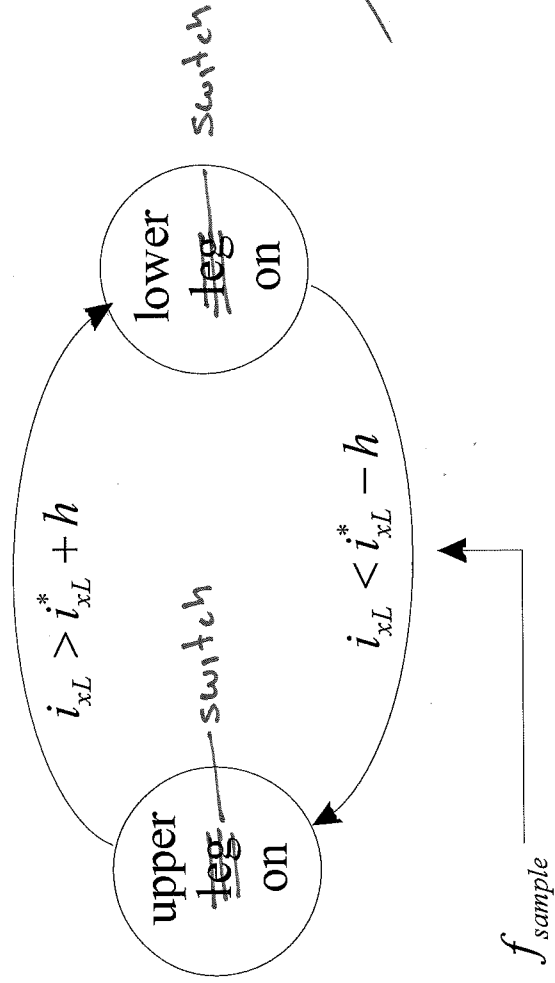
K_s^{-1} LTC

Left two columns

HD Modulator

Hysteretic - Delta Modulator

$X \in [a', b', c']$



Fully Controlled 3-Phase Bridge Converter

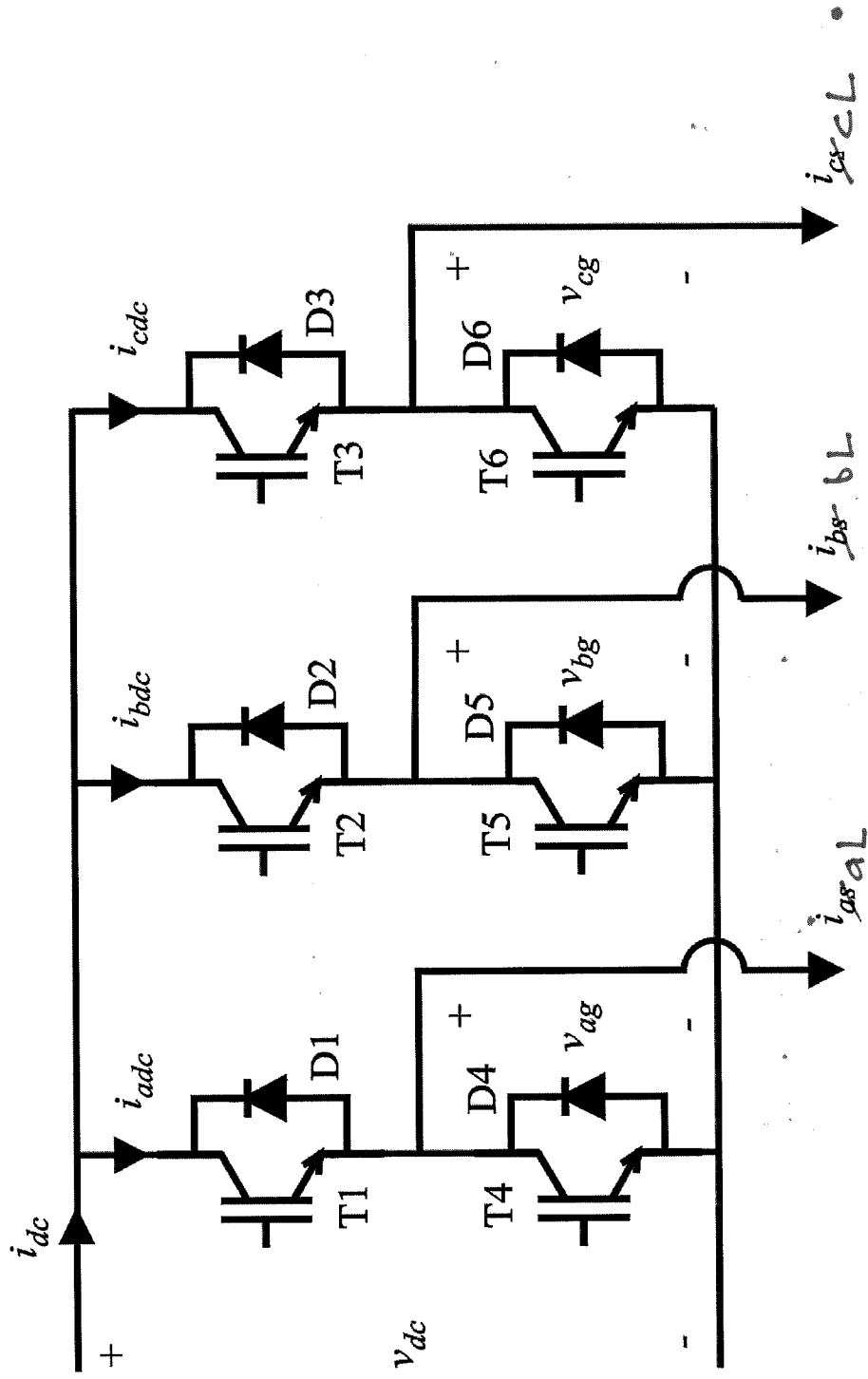


Figure 13.2-1 The three-phase bridge converter topology.