

AVM - Summary

$$(12) \quad \theta_{ci} - \theta = \text{angle}(i_{q_i} - j i_{d_i})$$

$$(13) \quad \begin{bmatrix} V_{bi} \\ \cdot V_{di} \end{bmatrix} = \begin{bmatrix} \cos(\theta_{ci} - \theta) & -s(\cdot) \\ s(\cdot) & \end{bmatrix} \begin{bmatrix} V_{\beta}^{ci} \\ V_{\alpha}^{ci} \end{bmatrix}$$

Sample Study

- Commanded voltage: 230 V l-l rms
- Commanded frequency: 60 Hz
- Peak current limit: 30 A
- Minimum voltage to run: 350
- Maximum voltage to run: 450
- Minimum voltage to start: 400
- Maximum voltage to start: 430
- Estimated l-n capacitance: 160 uF
- Sampling frequency: 450 kHz
- Hysteresis level: 1 A

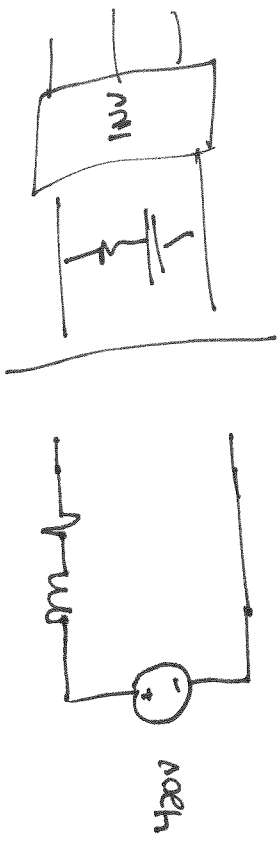
Sample Study

- Forward switch drop: 1 V
- Forward switch resistance: 100 m Ω
- Forward diode drop 2 V
- Forward diode resistance 50 m Ω
- Input capacitor capacitance 590 uF
- Input capacitor resistance 127 u Ω
- Equivalent l-n output capacitance: 150 uF
- Equivalent l-n output capacitor resistance: 2.67 m Ω
- AC inductor inductance: 550 uH
- AC inductor resistance: 38 mW

Sample Study

- DC Source

- 420 V with 50 μH and 50 $\text{m}\Omega$

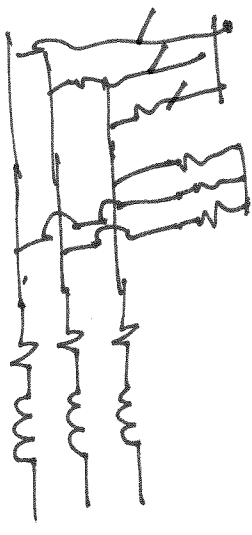


- Loads

- Cable: 100 $\text{m}\Omega$ and 0.1 mH impedance

- Load 1: 105.8 Ω line-to-neutral

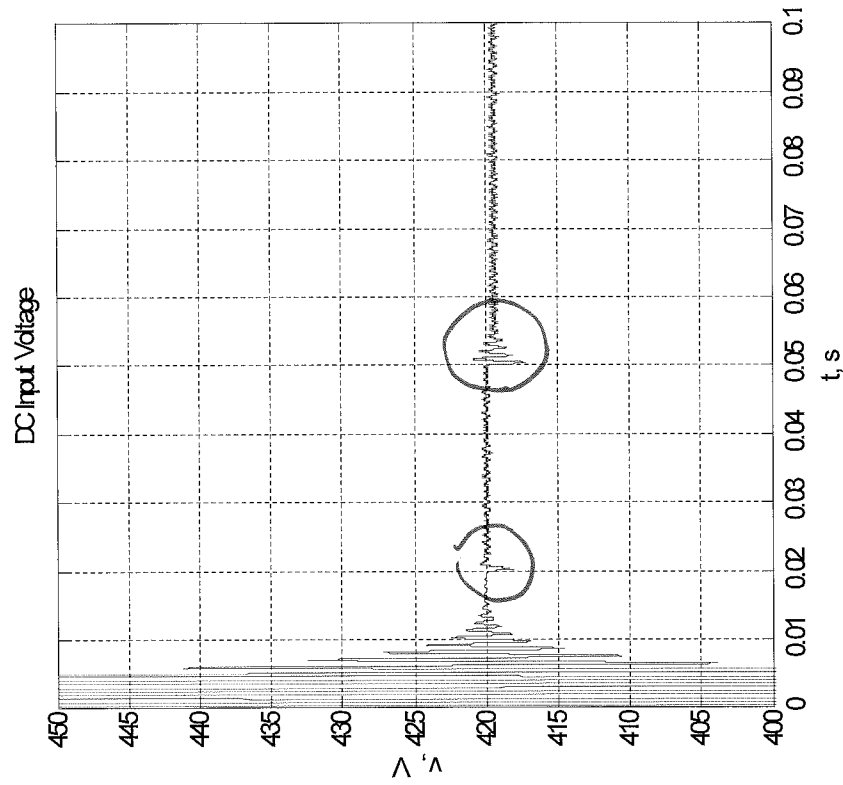
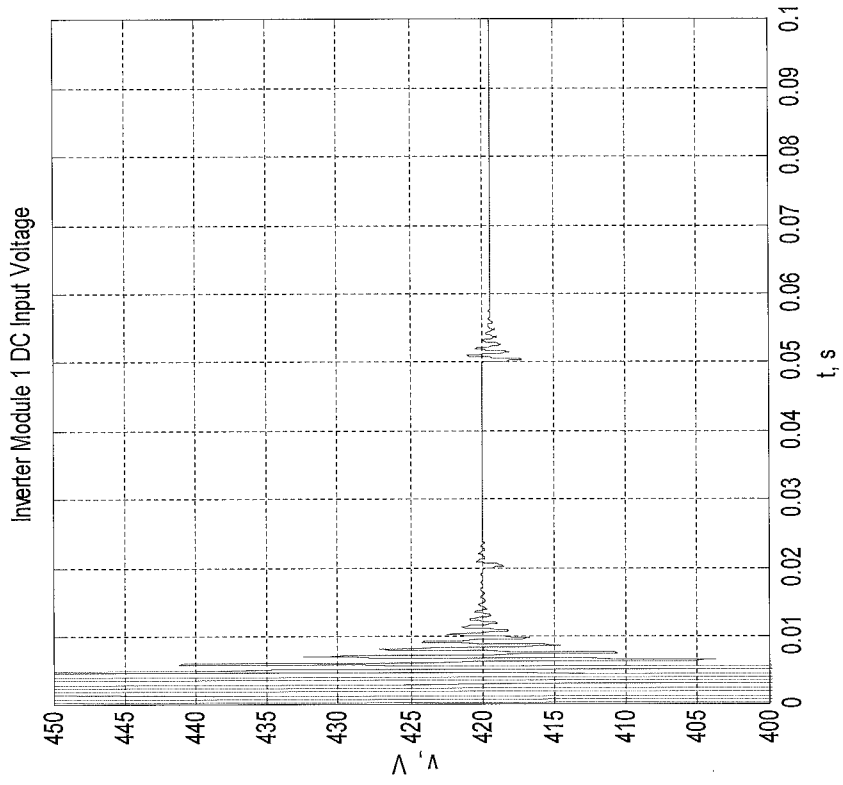
- Load 2: 10.58 Ω line-to-neutral



Scenario

- Power connected at $t=0$
 - Load 1 is initial present
 - Initially device is commanded off
 - Turned on at $t=0.02$ s
- Load 2 replaces load 1 at $t=0.05$ s

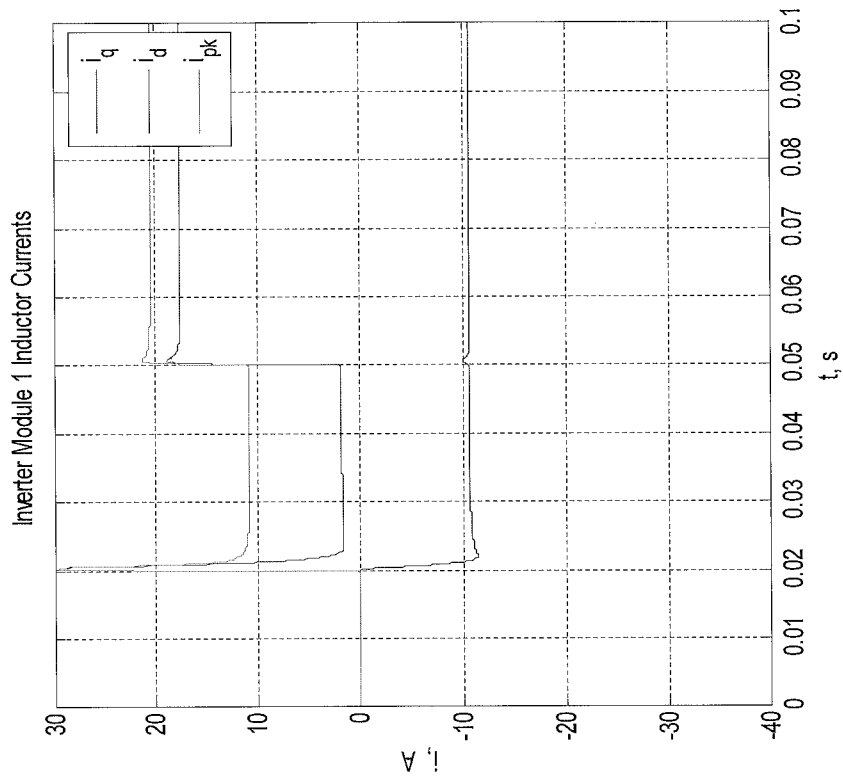
Input Voltage



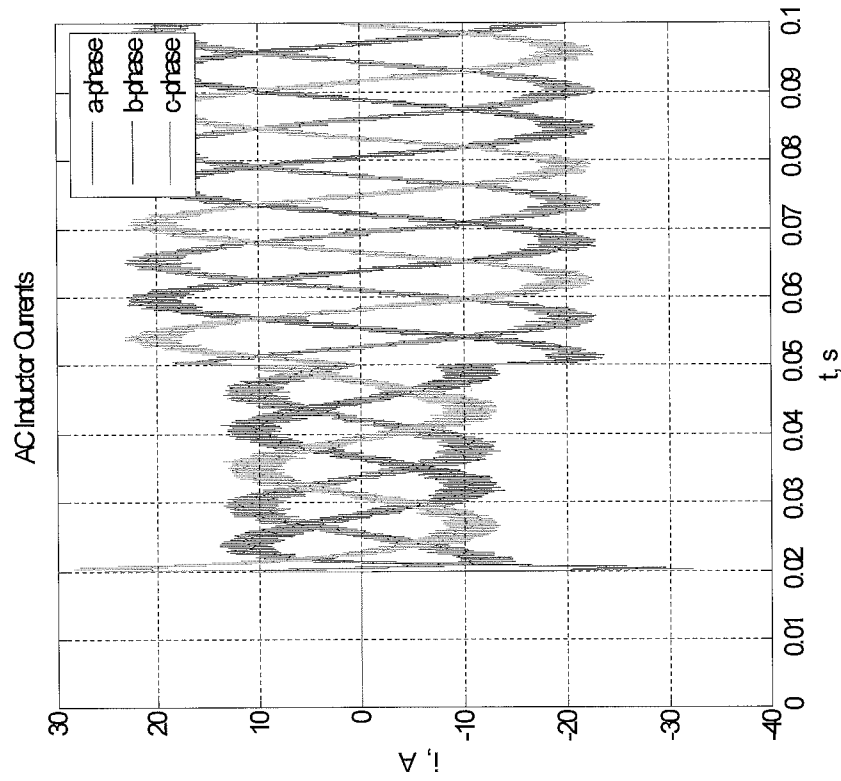
AVM

WLM

Inductor Current



AVM



WLM

**ECE61016 Power Electronics
Converters and Systems**

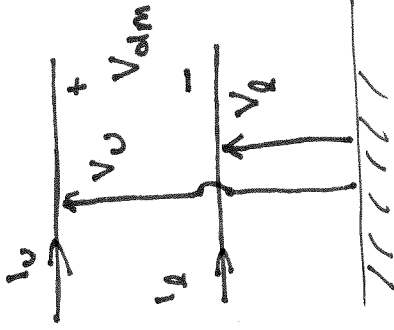
**Common Mode Concerns
Lecture Set 5**

S.D. Sudhoff

Fall 2016

CM and DM Voltages and Currents

- Consider a bus



- Definitions

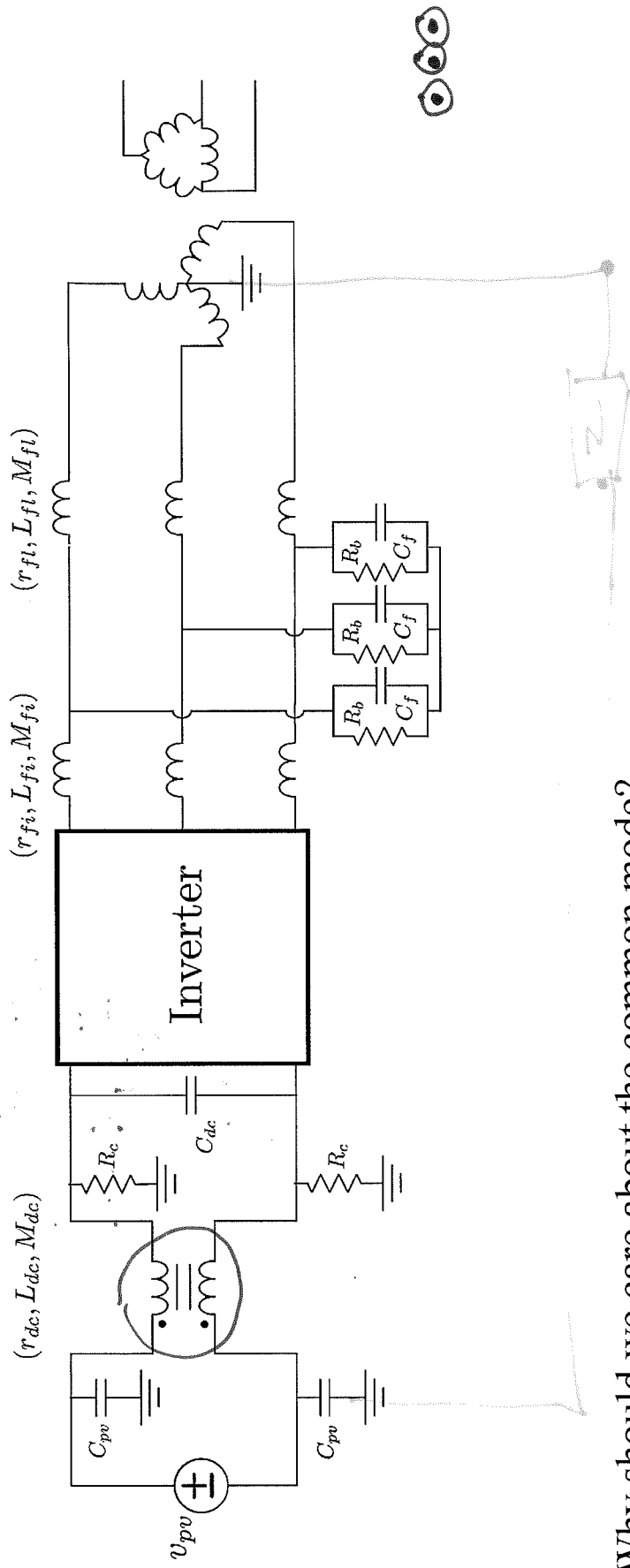
$$i_{dm} = \frac{1}{2}(i_u - i_l)$$

$$i_{cm} = i_u + i_l$$

$$V_{dm} = V_u - V_l$$

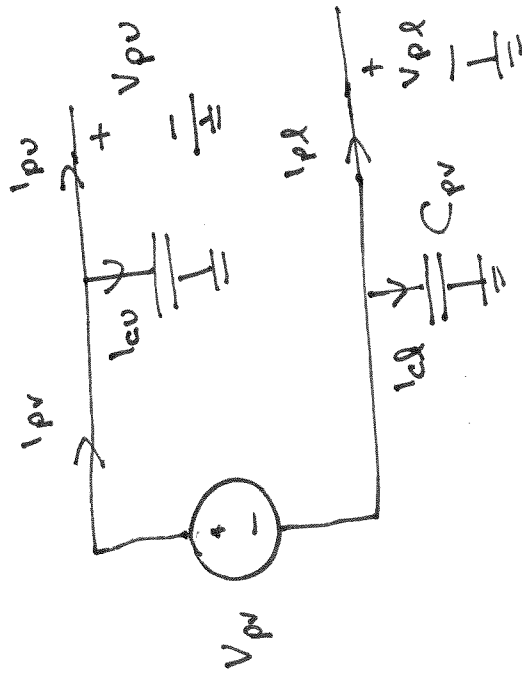
$$V_{cm} = \frac{1}{2}(V_u + V_l)$$

Example: PV Inverter System



Why should we care about the common mode?

Modeling of PV Array



$$I_{cu} = C_{pv} \frac{dV_{pv}}{dt}$$

$$I_{cl} = C_{pv} \frac{dV_{pl}}{dt}$$

$$-V_{pl} - V_{pv} + V_{pu} = 0$$

$$I_{cu} + I_{pv} + I_{cl} + I_{pl} = 0$$

DM Analysis of the PV Array

$$V_{pd} = V_{pv} - V_{pl} = V_{pv}$$

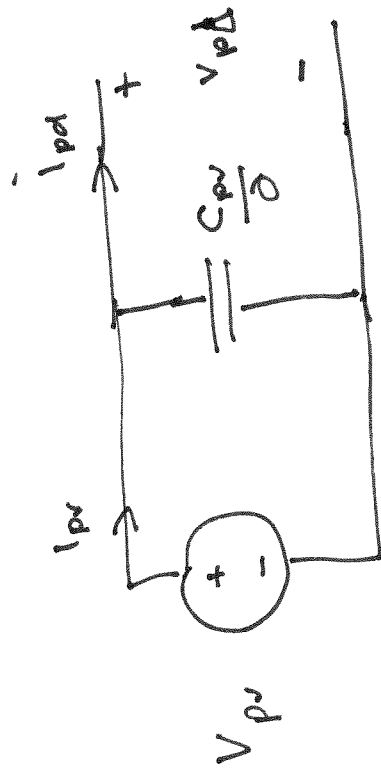
$$I_{pv} = -I_{cu} + I_{pv} = I_{pv} - C_{pv} P V_{pv}$$

$$I_{pl} = -I_{cl} - I_{pv} = -I_{pv} - C_{pv} P V_{pl}$$

$$I_{pd} = \frac{1}{2} (I_{pv} - I_{pl})$$

$$= \frac{1}{2} (I_{pv} - C_{pv} P V_{pv} + I_{pv} + C_{pv} P V_{pl})$$

$$= I_{pv} - \frac{C_{pv} P}{2} (V_{pv} - V_{pl})$$

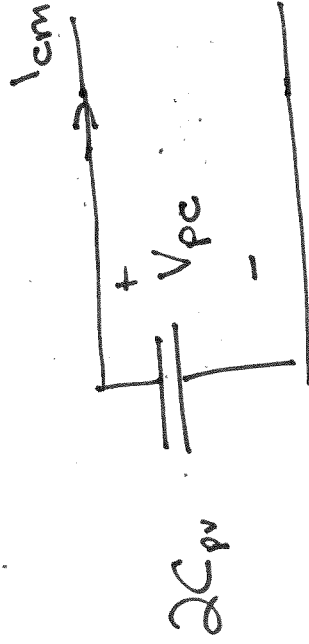


CM Analysis of PV Array

$$V_{pc} = \frac{1}{2} (V_{pv} + V_{pl})$$

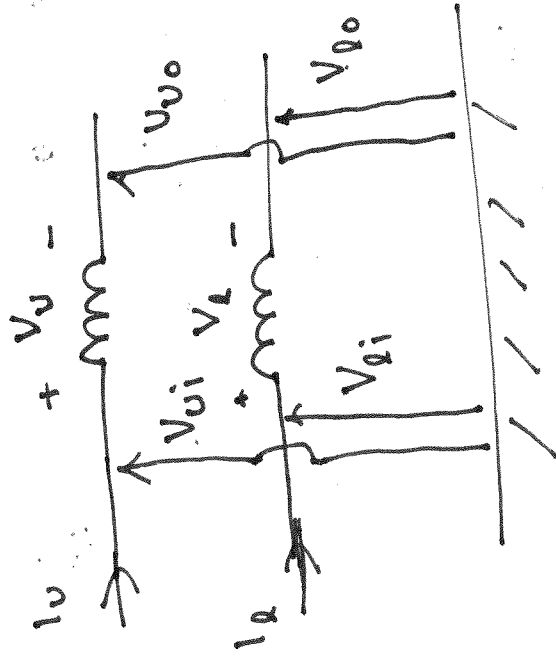
$$= \frac{1}{2} \left(\frac{I_{cu}}{C_{mp}} + \frac{I_{cl}}{C_{mp}} \right)$$

$$\frac{\partial C_{mp} P V_{pc}}{\partial C_{mp}} = I_{cu} + I_{cl} = -(I_{pv} + I_{pl}) = -I_{cm}$$



$$I_{cu} = C_{mp} P V_{pv}$$

Modeling of DC Side Inductor



$$\begin{bmatrix} \lambda_u \\ \lambda_d \end{bmatrix} = \begin{bmatrix} L & M \\ M & L \end{bmatrix} \begin{bmatrix} i_u \\ i_d \end{bmatrix}$$

$$L \geq 0 \quad M > 0$$

M slightly less than L

$$\begin{aligned} -V_{ui} + V_u + V_{uo} &= 0 \\ -V_{di} + V_d + V_{do} &= 0 \\ V_u &= r_l i_u + p \lambda_u \\ V_d &= r_l i_d + p \lambda_d \end{aligned}$$

DM Analysis of DC Side Inductor

$$V_{di} = \text{DM voltage on input} = V_{ui} - V_{ei}$$

$$V_{do} = \text{DM voltage on output} = V_{uo} - V_{eo}$$

$$I_d = \frac{1}{2} (I_u - I_e)$$

$$V_d = \text{DM voltage drop across Ind} = V_{di} - V_{do}$$

$$= V_{ui} - V_{ei} - (V_{uo} - V_{eo})$$

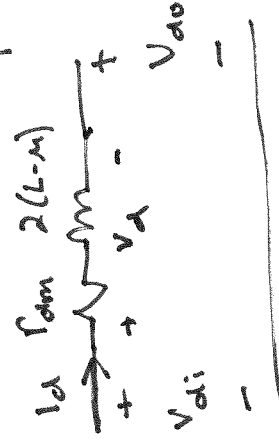
$$= (V_{ui} - V_{uo}) - (V_{ei} - V_{eo}) = V_u - V_e$$

$$= r_l u + p \lambda u - r_l e - p \lambda e$$

$$= r (I_u - I_e) + p (\lambda u - \lambda e)$$

$$= \underbrace{2r}_{P_{rdm}} I_d + \underbrace{2(L-M)}_{L_{edm}} p I_d$$

$$\begin{aligned} \lambda u - \lambda e &= (L-M) I_u \\ &\quad + (M-L) I_e \\ &= (L-M) (I_u - I_e) \\ &= 2(L-M) I_d \end{aligned}$$



(MHL)

DM Analysis of DC Side Inductor

CM Analysis of DC Side Inductor

CM Analysis of DC Side Inductor

$$V_{ci} = \frac{1}{2}(V_{vi} + V_{li})$$

$$V_{co} = \frac{1}{2}(V_{vo} + V_{lo})$$

$$V_{cm} = V_{ci} - V_{co} = \frac{1}{2}(V_{vi} - V_{vo}) + \frac{1}{2}(V_{li} - V_{lo}) = \frac{1}{2}(V_u + V_d)$$

Looking at slide 7

$$\lambda_u + \lambda_d = (L+M)(l_u + l_d) = (L+M)l_c$$

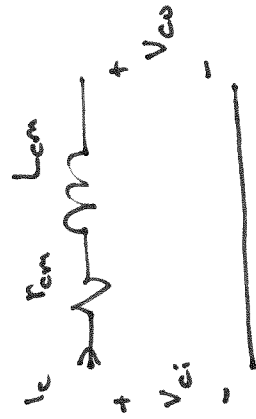
$$V_u = r_l u + p \lambda_u$$

$$V_d = r_l d + p \lambda_d$$

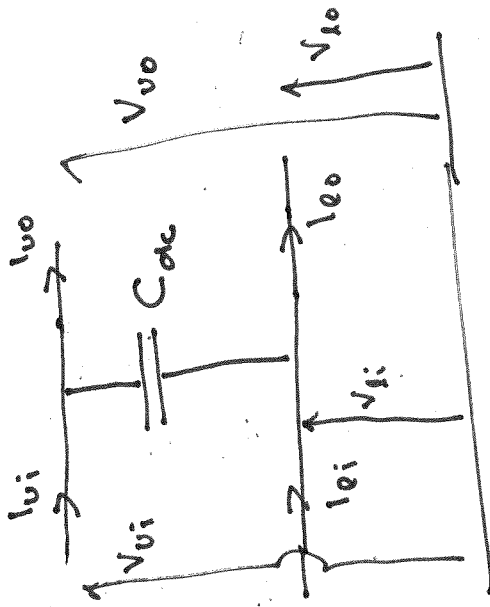
$$V_{cm} = \frac{1}{2}r_l(l_u + l_d) + \frac{1}{2}p(\lambda_u + \lambda_d)$$

$$= \frac{1}{2}r_l l_c + \frac{1}{2}p(L+M)l_c$$

$$= \underbrace{\frac{1}{2}r_l l_c}_{r_{cm}} + \underbrace{\frac{1}{2}(L+M)l_c}_{L_{cm}} p$$



Modeling of DC Side Capacitor



$$V_u = V_{ui} = V_{uo}$$

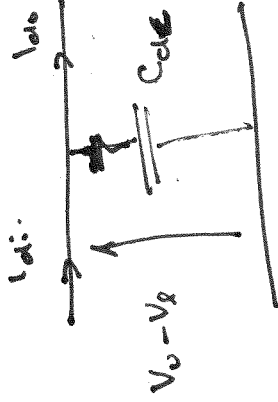
$$V_d = V_{di} = V_{uo}$$

CM

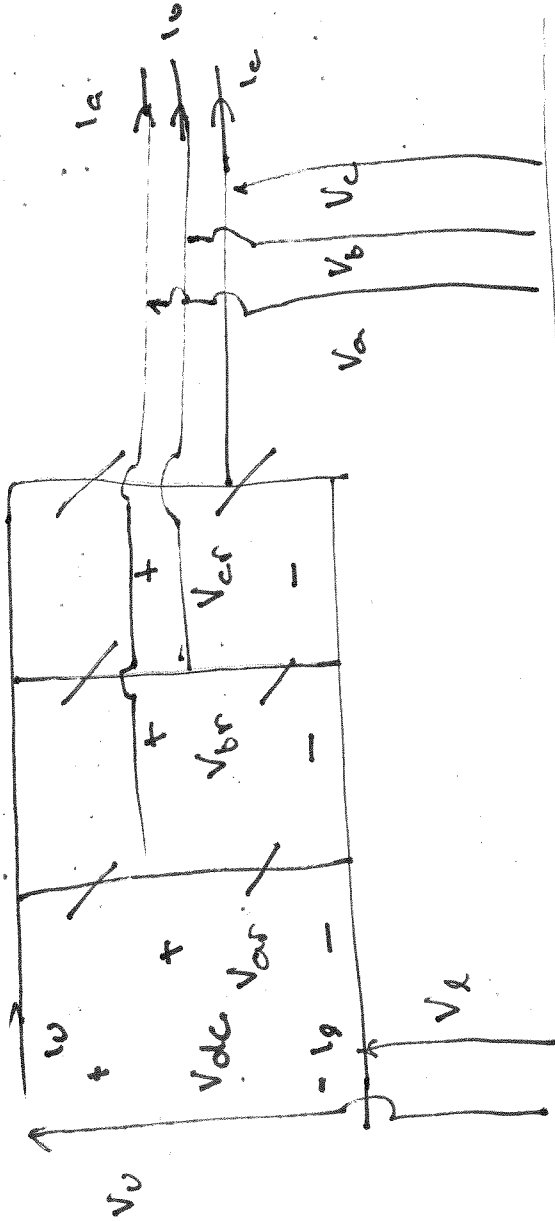
$$\frac{DM}{C_{dc} P (V_u - V_d)} = i_{ui} - i_{ud} = i_{eo} - i_{ri}$$

$$= \frac{1}{2} (i_{ui} - i_{uo}) + \frac{1}{2} (i_{eo} - i_{ri})$$

$$= i_{di} - i_{do}$$



CM Inverter Model



$$V_{cmi} = \frac{1}{2}(V_u + V_g)$$

$$i_{cmi} = i_u + i_g$$

$$i_{cmi} = i_{cmo}$$

$$V_{cm0} = \frac{1}{3}(V_a + V_b + V_c)$$

$$i_{cmo} = i_a + i_b + i_c$$

CM Inverter Model

$$-V_Q - V_{ar} + V_a = 0$$

$$-V_Q - V_{br} + V_b = 0$$

$$-V_Q - V_{er} + V_c = 0$$

$$-3V_Q - (V_{ar} + V_{br} + V_{er}) + (V_{ca} + V_b + V_c) = 0$$

$$V_{dc} = V_Q - V_Q$$

$$V_{dc} + 2V_Q = V_Q + V_Q = 2V_{ci}$$

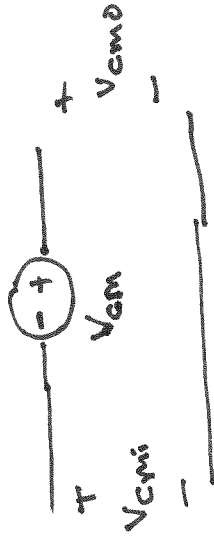
$$V_Q = V_{cmi} - \frac{1}{2}V_{dc}$$

$$-3(V_{cmi} - \frac{1}{2}V_{dc}) - (V_{ar} + V_{br} + V_{er}) + 3V_{cmo} = 0$$

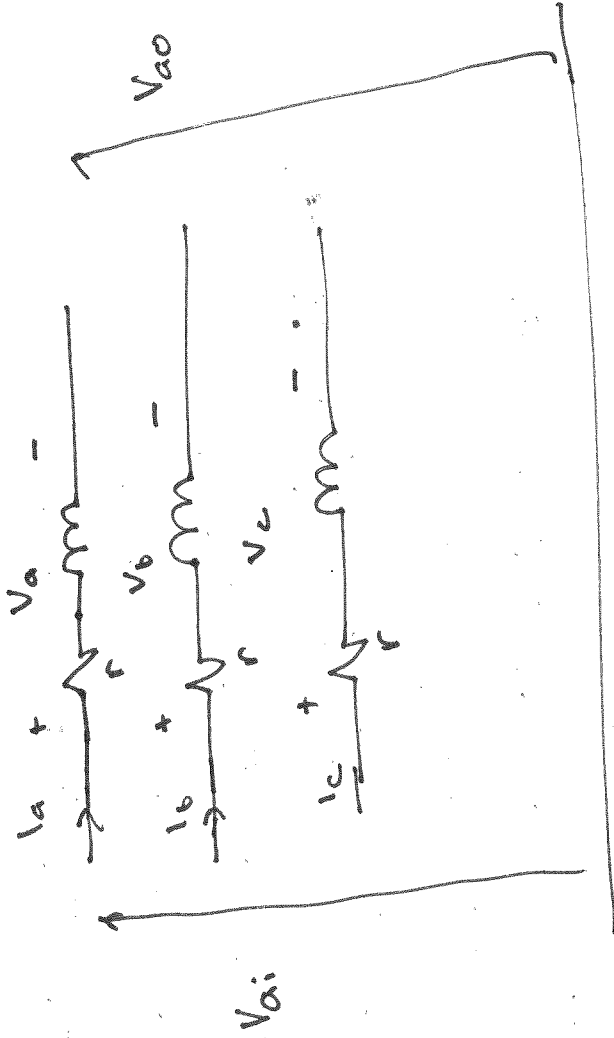
$$-3V_{cmi} + \frac{3}{2}V_{dc} - \frac{1}{3}(V_{ar} + V_{br} + V_{er}) + V_{cmo} = 0$$

$$V_{cmi} - V_{cmo} = \frac{1}{2}V_{dc} - \frac{1}{3}(V_{ar} + V_{br} + V_{er})$$

$$V_{cm} = \frac{1}{3}(V_{ar} + V_{br} + V_{er}) - \frac{1}{2}V_{dc}$$



Modeling of a Three-Phase Inductor



$$\begin{bmatrix} \lambda_a \\ \lambda_b \\ \lambda_c \end{bmatrix} = \begin{bmatrix} L & M & M \\ M & L & M \\ M & M & L \end{bmatrix} \begin{bmatrix} i_a \\ i_b \\ i_c \end{bmatrix}$$

$$\lambda_a + \lambda_b + \lambda_c = (L+2M)(i_a + i_b + i_c)$$

$$= (L+2M) i_{cm}$$

$$V_{cmi} = \frac{1}{3} (V_{ai} + V_{bi} + V_{ci})$$

$$V_{cmo} = \frac{1}{3} (V_{ao} + V_{bo} + V_{co})$$

$$V_{cm} = V_{cmi} - V_{cmo}$$

$$= \frac{1}{3} (V_{ai} - V_{ao}) + \frac{1}{3} (V_{bi} - V_{bo}) + \frac{1}{3} (V_{ci} - V_{co})$$

$$= \frac{1}{3} (V_a + V_b + V_c)$$

Modeling of a Three-Phase Inductor

$$V_a = r i_a + p \lambda_a$$

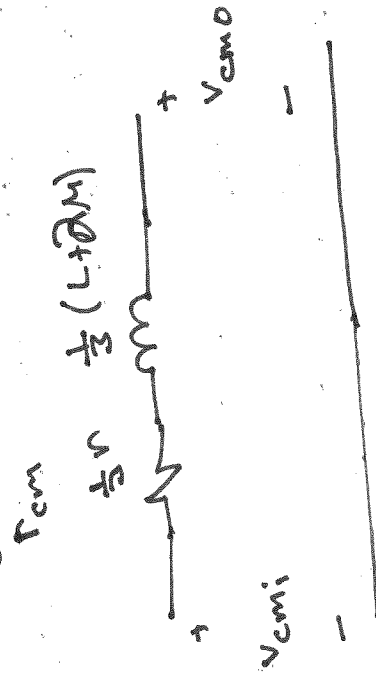
$$V_b = r i_b + p \lambda_b$$

$$V_c = r i_c + p \lambda_c$$

$$3V_{cm} = V_a + V_b + V_c = r \underbrace{(i_a + i_b + i_c)}_{i_{cm}} + p(\lambda_a + \lambda_b + \lambda_c)$$

$$V_{cm} = \frac{1}{3} r i_{cm} + \frac{1}{3} p (\lambda_a + \lambda_b + \lambda_c)$$

$$= \frac{1}{3} r i_{cm} + \underbrace{\frac{1}{3} (L + 2M)}_{L_{cm}} p i_{cm}$$



$$L = L_{os} + L_m$$

$$M = -\frac{1}{2} L_m$$

$$\frac{1}{3} (L + 2M) = \frac{1}{3} L_{os}$$

Modeling the Utility Connection

$$V_{an} = r l_a + \rho \lambda_{as} \quad \text{secondary}$$

$$V_{bn} = r l_b + \rho \lambda_{bs}$$

$$V_{cn} = r l_c + \rho \lambda_{cs}$$

$$V_{an} + V_{bn} + V_{cn} = r l_{cm} + \rho (\lambda_{as} + \lambda_{bs} + \lambda_{cs})$$

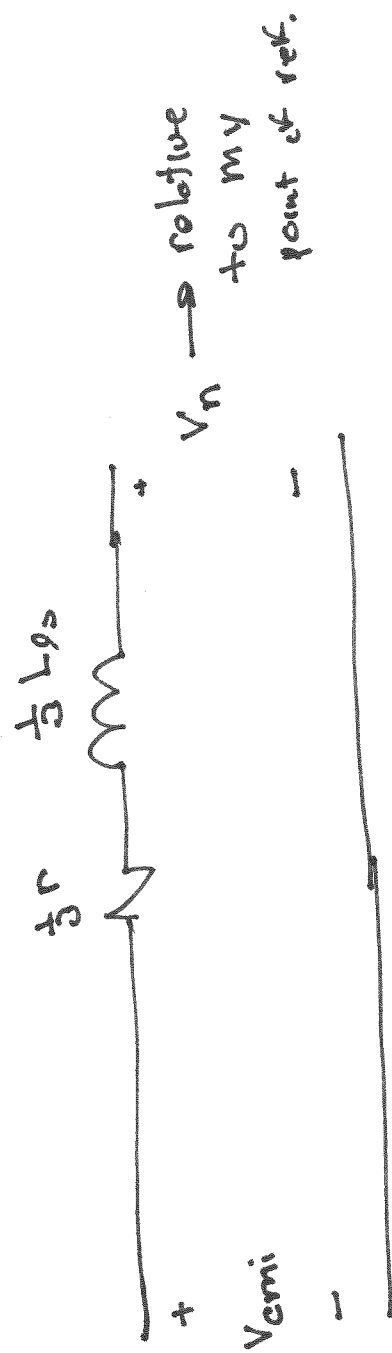
$$\lambda_{abcs} = \begin{bmatrix} L_{as} + L_{bs} & -\frac{1}{2} L_{ms} & -\frac{1}{2} L_{ms} & \text{icbc} \\ -\frac{1}{2} L_{ms} & L_{as} + L_{bs} & -\frac{1}{2} L_{ms} & \\ -\frac{1}{2} L_{ms} & -\frac{1}{2} L_{ms} & L_{ps} + L_{ms} & \\ L_{ps} & -\frac{1}{2} L_{ps} & -\frac{1}{2} L_{ps} & \text{icbc} \rho \\ -\frac{1}{2} L_{ps} & L_{ps} & -\frac{1}{2} L_{ps} & \\ -\frac{1}{2} L_{ps} & -\frac{1}{2} L_{ps} & L_{ps} & \end{bmatrix}$$

$$\lambda_{as} + \lambda_{bs} + \lambda_{cs} = L_{os} (l_a + l_b + l_c) = L_{os} l_{cm}$$

$$V_{an} + V_{bn} + V_{cn} = r l_{cm} + L_{os} \rho l_{cm}$$

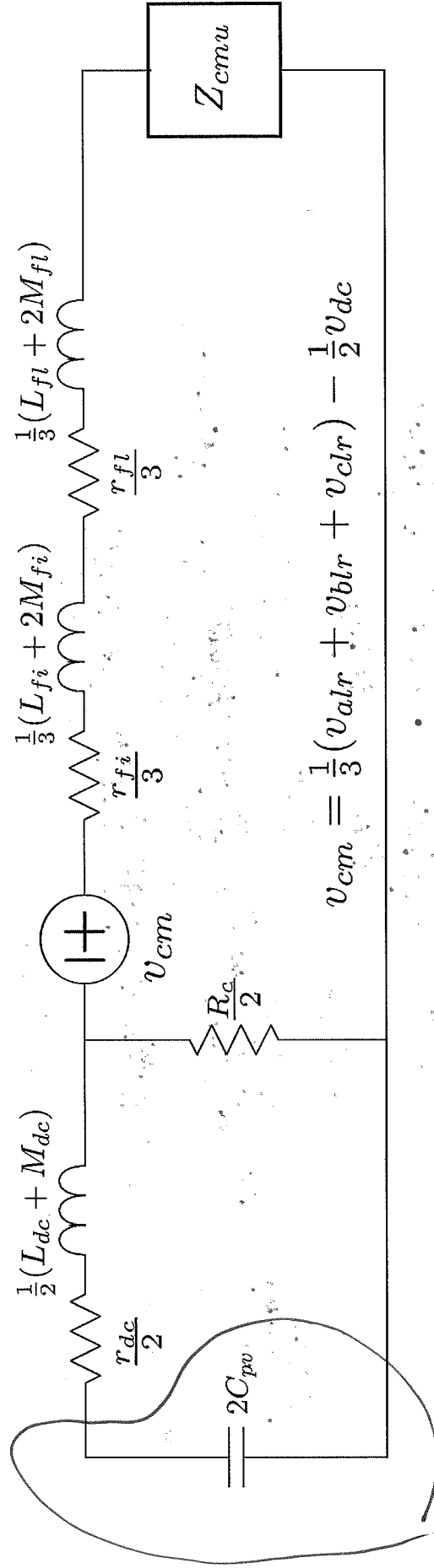
$$-3 V_{cm} + r_{cm} + L_{os} p_{cm} + 3V_n$$

$$V_{cm} = \frac{1}{3} r_{cm} + \frac{1}{3} L_{os} p_{cm} + V_n$$



If I pick my reference to be the ground out pt array
 $V_n = Z_n I_{cm}$

PV Inverter Equivalent Circuit



The common mode load impedance is unknown. The quantity is C_{pv} is also unknown, but we can guarantee a minimum value by adding some.

Simplifying the CM equiv. circuit

- Assumptions:
 - Ignore R_c *utility*
 - Common mode load impedance is RL (neutral of transformer directly connected to ground)

- Define

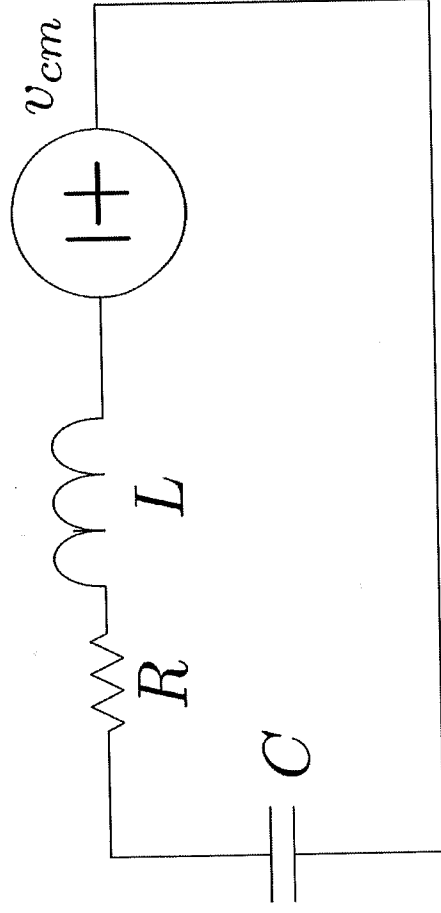
$$Z_{cmu} = r_{cmu} + j\omega L_{cmu}$$

$$L = \frac{1}{2}(L_{dc} + M_{dc}) + \frac{1}{3}(L_{fl} + 2M_{fl}) + \frac{1}{3}(L_{fl} + 2M_{fl}) + L_{cmu}$$

$$R = \frac{1}{2}r_{dc} + \frac{1}{3}r_{fl} + \frac{1}{3}r_{fl} + r_{cmu}$$

$$C = 2C_{pv}$$

Simplified Common Mode Equivalent Circuit



$$v_{cm} = \frac{1}{3}(v_{alr} + v_{blr} + v_{clr}) - \frac{1}{2}v_{dc}$$

DC Side Common Mode Inductor

- Step 1: Strategy for common mode current mitigation
- Step 2: Develop common mode inductor design specifications
- Step 3: Design methodology for inductor

Step 1: Common Mode Current

- The common mode current at a given frequency is given by

$$Z = R + j\omega L + \frac{1}{j\omega C}$$

$$\tilde{i}_{cm} = \frac{\tilde{v}_{cm}}{Z}$$

- So

$$|\tilde{i}_{cm}| = \frac{|\tilde{v}_{cm}|}{\sqrt{R^2 + \left|\omega L - \frac{1}{\omega C}\right|^2}} \leq \frac{|\tilde{v}_{cm}|}{\left|\omega L - \frac{1}{\omega C}\right|}$$

$$\omega L \Rightarrow \frac{1}{\omega C}$$

$$C \Rightarrow \frac{1}{\omega L}$$

Step 1: Strategy

- We must avoid resonances. Therefore we require

$$C \gg \frac{1}{\omega_{sw}^2 L}$$

- This can be assured if

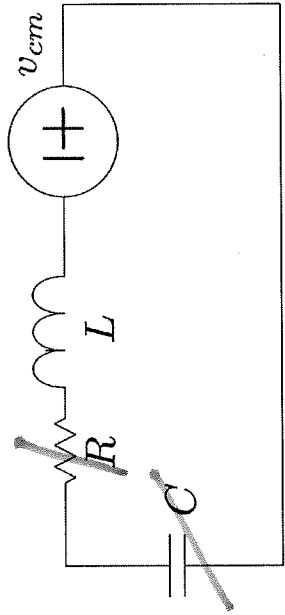
$$C_{pv} \gg \frac{1}{2\omega_{sw}^2 L}$$

- Thus we will take (for example)

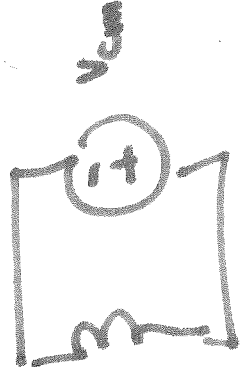
$$L = \frac{1}{2}(L_{dc} + M_{dc}) = L_{cm}$$

$$C_{pv} = \frac{5}{\omega_{sw}^2 L_{cm}}$$

Further Reduced CM Equivalent Circuit



$$v_{cm} = \frac{1}{3}(v_{alr} + v_{blr} + v_{clr}) - \frac{1}{2}v_{dc}$$



Common Mode Flux Linkage

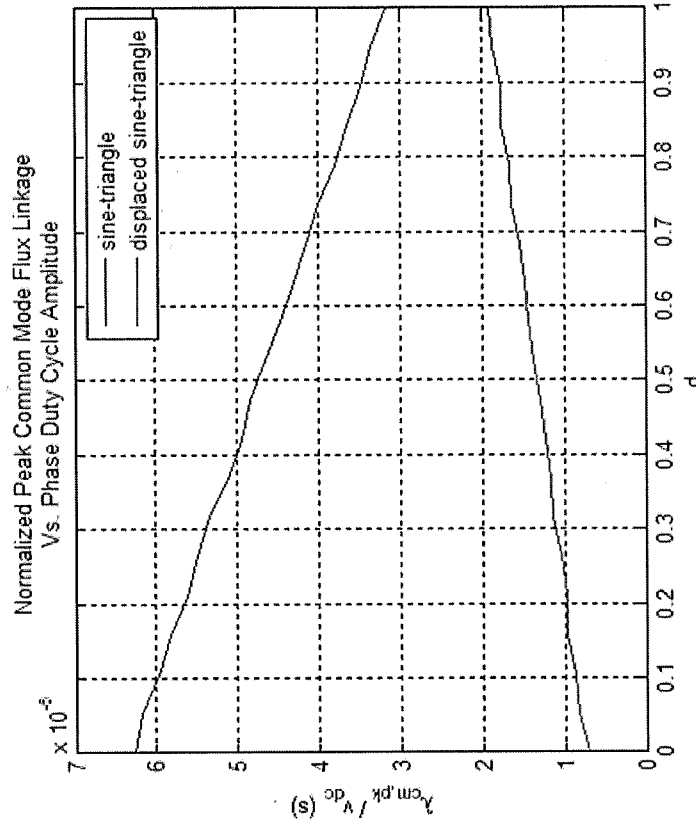
$$p \lambda_{cm} = v_{cm} - \cancel{y \lambda_{cm}}$$

$$\lambda_{cm} = L i_{cm}$$

Step 1: Common Mode Flux Linkage

Peak Common Mode Flux linkage / Vdc

- For sine-triangle modulation, the peak flux linkage goes down with duty cycle.
- Let's assume a duty cycle amplitude of $d=0.627$, which is 80% of the value corresponding to 480 V at the terminals

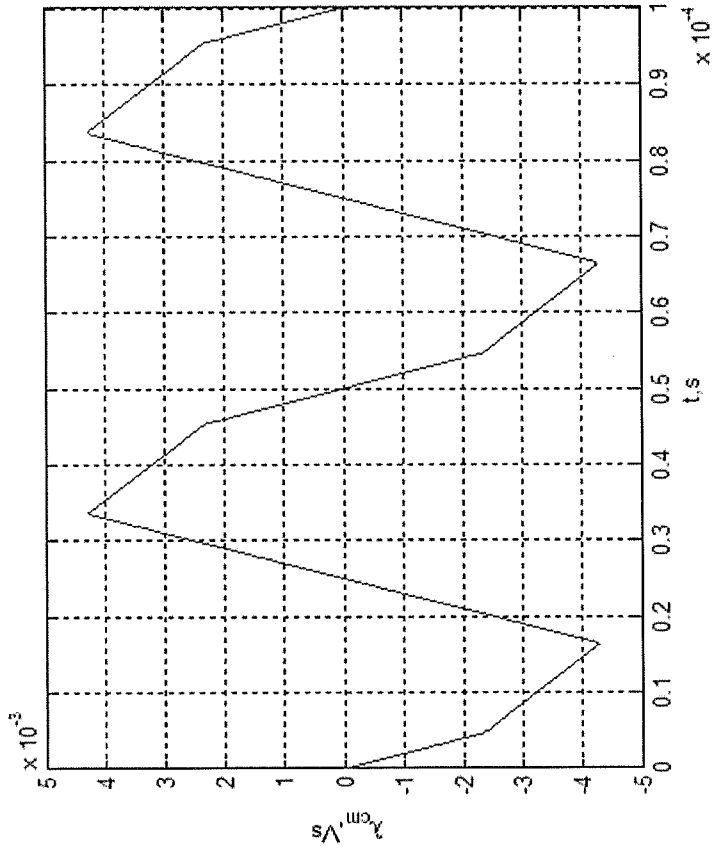
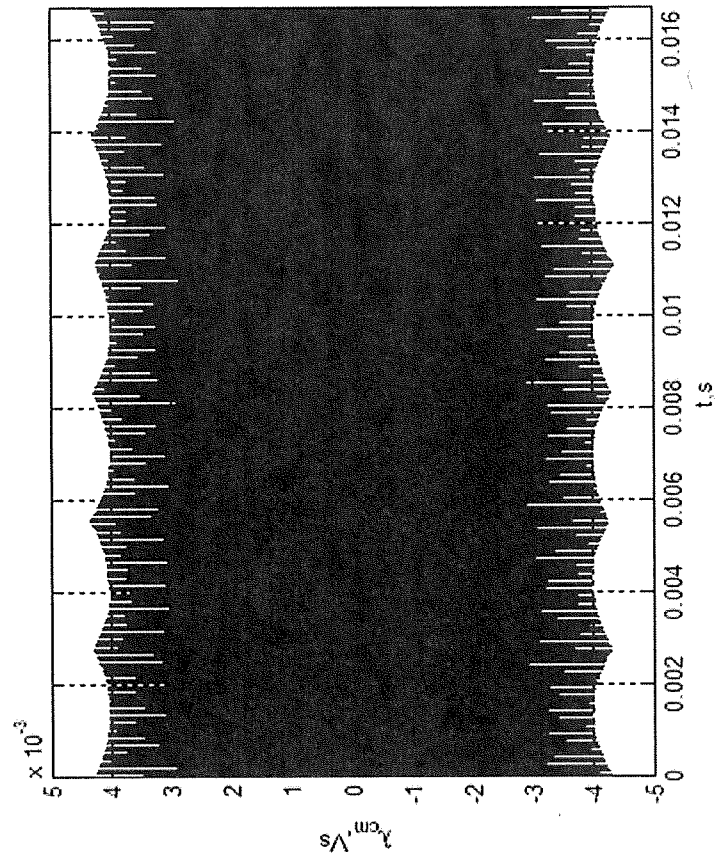


Amplitude of the phase duty cycle

Step 1: Numbers Check

- Case 1
 - Peak common mode current = 120 mA
 - Switching at 20 kHz
 - $L_{cm} = (L_{dc} + M_{dc}) / 2 = 36$ mH
 - Required $C_{pv} = 8.8$ nF
- Case 2
 - Peak common mode current = 120 mA
 - Switching at 50 kHz
 - $L_{cm} = (L_{dc} + M_{dc}) / 2 = 14$ mH
 - Required $C_{pv} = 3.6$ nF
- Conclusion: it won't take much C to make approach valid
- Suggestion: we will add the required capacitance and not rely on parasitics

Step 1: Common Mode Flux Linkage at $d = 0.627$



Step 2: Setting Up Design Problem

- Problem
 - Waveforms are quasi a-periodic
 - Slowly vary amplitude and shape
- Solution – Define a proxy flux linkage waveform
 - Same peak value (saturation)
 - Same rms value (resistive loss)
 - Time derivative has same rms value as the original waveform (proximity effect loss)

Approach to Fitting

$$\lambda_p = a_1 \cos(\omega_{su} t) + b_3 \cos(3\omega_{su} t) + b_3 \sin(3\omega_{su} t)$$

Find

$$\lambda_{p, pk}$$

$$\omega_{su} = 2\pi f_{su}$$

$$\lambda_{p, rms}$$

$$\left(\left(\frac{d\lambda_p}{dt} \right)^2 \right)_{rms}$$

$$\text{Error} = \sqrt{(\lambda_{p, pk} - \lambda_{em, pk})^2 + (\lambda_{p, rms} - \lambda_{em, rms})^2 + \omega_{su}^4 \left(\left(\frac{d\lambda_p}{dt} \right)^2 \right)_{rms} - \left(\left(\frac{d\lambda_{em}}{dt} \right)^2 \right)_{rms}}$$

$$\text{Fit} = \frac{1}{\text{Error} + \dots}$$

Approach to Fitting

Step 2: Proxy CM Flux Linkage Waveform

