Lab Quiz 4

| | Closed Book and Notes – TI 30II XS Calculator Allowed |
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| 1. | A microcontroller designed to operate over a power supply range of 1.2 V to 3.3 V and a clock frequency range of 0 to 100 MHz dissipates a maximum of 500 mW . If the supply voltage used is 1.475 V and the clock frequency is 100 MHz , the power dissipation of the microcontroller will be reduced to approximately: (A) 25 mW (B) 50 mW (C) 75 mW (D) 100 mW (E) none of these |
| 2. | A microcontroller designed to operate over a power supply range of 1.2 V to 3.3 V and a clock frequency range of 0 to 100 MHz dissipates a maximum of 500 mW. If the supply voltage used is 3.3 V and the clock frequency is 5 MHz, the power dissipation of the microcontroller will be reduced to approximately: (A) 25 mW (B) 50 mW (C) 75 mW (D) 100 mW (E) none of these |
| 3. | If a CMOS gate input voltage is 70% of its V _{cc} (power supply) voltage, then: (A) the logic gate will dissipate <i>less</i> power than it would if the input was 10% of its power supply voltage (B) the logic gate will dissipate <i>less</i> power than it would if the input was 90% of its power supply voltage (C) the logic gate will dissipate <i>more</i> power than it would if the input was <i>either</i> 10% <i>or</i> 90% of its power supply voltage |
| | (D) the logic gate will dissipate virtually no power (E) none of the above (E) none of the above (E) none of the above (C): rise time = 30 ns |
| 4. | Based on the timing diagram below, estimate the value of the capacitor (C): rise time = 30 ns (A) 10 pF (B) 12 pF (C) 15 pF (D) 18 pF (E) none of these $2500 \times C = 30 \times 10^{-9}$ |
| 5. | Based on the timing diagram below, estimate the ON resistance of the open-drain NAND gate: (A) 250 Ω (B) 567 Ω (C) 833 Ω (D) 1000 Ω (E) none of these |
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| | $= 10 \times 10^{-1}$ |
| | \mathbb{T}^{c} $= 10 \times 10^{-12}$ |
| | Vout = 0.933 X /0" = 833 SZ |
| | |
| | → 10 ns |