
LEARNING OUTCOME #3: “an ability to analyze and design sequential logic circuits.”

1. B – LSN p. 5, PEXA q. 1, PEXB q. 1
2. C – LSN p. 13, PEXA q. 2, PEXB q. 2
3. D – LSN p. 30, PEXA q. 3, PEXB q. 3
4. B – LSN, p. 30, PEXA q. 4, PEXB q. 4
5. B – LSN p. 9-10, HW 9 q. 2, HW 9 q. 3, PQ9 q. 1-3, PEXA q. 15-20, PEXB q. 10-13
6. B – LSN p. 9-10, HW 9 q. 2, HW 9 q. 3, PQ9 q. 1-3, PEXA q. 15-20, PEXB q. 10-13
7. A – LSN p. 9-10, HW 9 q. 2, HW 9 q. 3, PQ9 q. 1-3, PEXA q. 15-20, PEXB q. 10-13
8. C – LSN p. 9-10, HW 9 q. 2, HW 9 q. 3, PQ9 q. 1-3, PEXA q. 15-20, PEXB q. 10-13
9. B – LSN p. 9-10, HW 9 q. 2, HW 9 q. 3, PQ9 q. 1-3, PEXA q. 15-20, PEXB q. 10-13
10. A – LSN p. 9-10, HW 9 q. 2, HW 9 q. 3, PQ9 q. 1-3, PEXA q. 15-20, PEXB q. 10-13
11. C – LSN p. 3-5, HW 7 q. 2, PQ 8, LQ 8, PEXA q. 10-14, PEXB q. 5-9
12. A – LSN p. 3-5, HW 7 q. 2, PQ 8, LQ 8, PEXA q. 10-14, PEXB q. 5-9
13. E – LSN p. 3-5, HW 7 q. 2, PQ 8, LQ 8, PEXA q. 10-14, PEXB q. 5-9
14. D – LSN p. 3-5, HW 7 q. 2, PQ 8, LQ 8, PEXA q. 10-14, PEXB q. 5-9
15. D – LSN p. 3-5, HW 7 q. 2, PQ 8, LQ 8, PEXA q. 10-14, PEXB q. 5-9
16. B – LSN p. 7, PQ 9 q. 5, PEXA q. 5 & 24
17. D – LSN p. 11 & 14, PEXA q. 23
18. C – LSN p. 20
19. E – LSN p. 8, PQ 9 q. 4, PEXA q. 6 & 25
20. E – LSN p. 4, Lab 8 step 2, HW 2 q. 4, LQ 9 q. 4-5
21. D – LSN p. 9, PEXA q. 22
22. A – LSN p. 20, HW 9 q. 1, PEXB q. 14-16
23. C – LSN p. 20, HW 9 q. 1, PEXB q. 14-16
24. D – LSN p. 20, HW 9 q. 1, PEXB q. 14-16
25. C – LSN p. 20, HW 9 q. 1, PEXB q. 14-16
26. B – LSN p. 26-28, PQ 10, PEXA q. 8-9
27. C – LSN p. 26-28, PQ 10, PEXA q. 8-9
28. C – LSN p. 26-28, PQ 10, PEXA q. 8-9
29. *N/A – question was eliminated due to typo in case statement*
30. D – LSN p. 21-25, PEXA q. 29-30