A Convex Relaxation Approach to Optimal Placement of Phasor Measurement Units

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Abstract—Instrumenting power networks with phasor measurement units (PMUs) facilitates several tasks including optimum power flow, system control, contingency analysis, visualization, and integration of renewable resources, thus enabling situational awareness - one of the key steps toward realizing the smart grid vision. The installation cost of PMUs currently prohibits their deployment on every bus, which in turn motivates their strategic placement across the power grid. As state estimation is at the core of grid monitoring, PMU deployment is optimized here based on estimation-theoretic criteria. Considering both voltage and current PMU readings and incorporating conventionally derived state estimates under the Bayesian framework, PMU placement is formulated as an optimal experimental design task. To obviate the combinatorial search involved, a convex relaxation is also developed to obtain solutions with numerical optimality guarantees. In the tests performed on standard IEEE 14- and 118-bus benchmarks, the proposed relaxation is very close to and oftentimes attains the optimum.

I. INTRODUCTION

Phasor measurement units are contemporary metering devices installed on system buses to measure phasors of bus voltages and currents flowing across lines [13], [6]. Merits of PMUs (a.k.a. synchrophasors) over conventional power meters include increased precision in measuring phasor angles due to network-wide synchronization and higher sampling rates.

PMU penetration has so far been rather limited, mainly because of the installation and networking costs involved. However, their important role in network operation and the growing maturity of PMU technology are expected to markedly increase their deployment [4]. According to the North American Synchrophasor Initiative, the number of PMUs installed and networked in the eastern/western interconnection will likely raise from 105/56 as of 2009 to 400-600 by 2014 [11].

Strategic placement of synchrophasors is currently a critical issue for the power operators worldwide, and has been considered in the context of topological observability (existence of a spanning measurement tree) [2], and incomplete observability [12]. Building on these notions, optimal PMU placement has been posed as a binary linear program [7]; as a binary quadratic one [5]; or as a stochastic optimization problem [1].

In this work, PMU sites are selected purely based on estimation-theoretic criteria. Estimating the power grid state

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is traditionally performed by the supervisory control and data acquisition (SCADA) center based on conventional meter readings [16, Ch. 12]. The improvement in estimation accuracy by combining PMU and SCADA measurements has been documented in [17]. Similar to [10], PMU placement is formulated here as a variation of the optimal experimental design problem [14], [3, Sec. 7.5]. After posing system state estimation as a linear regression problem, SCADAbased estimates are utilized as Gaussian priors (Section II). PMU placement is then optimized by minimizing the error covariance matrix (Section III). The combinatorial problem involved is suboptimally solved after relaxing it to a convex semidefinite program (SDP) [3], [8]. The SDP-derived cost value yields bounds on the suboptimality gap. Surprisingly, the numerical results of Section V show that this gap is small or oftentimes zero for the IEEE 14- and 118-bus power network benchmarks [15]. In contrast, the approach of [10] ignores current measurements of PMU lines, and estimates only the phase of the system state using a greedy algorithm.

Notation: Lower- (upper-) case boldface letters denote column vectors (matrices), and calligraphic letters stand for sets; $\mathbf{1}_N$ ($\mathbf{0}_N$) denotes the all-ones(zeros) vector of length N; $(\cdot)^T$ and $(\cdot)^*$ transposition and complex conjugation, respectively; $\mathcal{N}(\mathbf{m}, \Sigma)$ stands for the multivariate Gaussian probability density function with mean \mathbf{m} and covariance matrix Σ .

II. PMU-BASED STATE ESTIMATION

Consider a power grid consisting of N_b buses connected through N_l transmission lines. The problem of estimating the state of the grid is equivalent to estimating the complex voltages across its buses. In rectangular coordinates, the system state is described by the $2N_b \times 1$ vector $\mathbf{v}_o := [\mathbf{v}_{o,r}^T \ \mathbf{v}_{o,i}^T]^T$, where $\mathbf{v}_{o,r}$ and $\mathbf{v}_{o,i}$ comprise the real and imaginary parts of nodal voltages, respectively.

Consider the PMU measurements collected at the n-th bus. Without loss of generality, these readings include the complex bus voltage and the complex currents flowing over the L_n lines incident to the n-th bus. The synchrophasor measurements in rectangular coordinates are collected in the vector $\mathbf{z}_n \in \mathbb{R}^{M_n}$ with $M_n := 2(L_n + 1)$, and obey the following linear model

$$\mathbf{z}_n = \mathbf{H}_n \mathbf{v}_o + \mathbf{w}_n \tag{1}$$

where $\mathbf{H}_n \in \mathbb{R}^{M_n \times 2N}$ is the associated regression matrix, and $\mathbf{w}_n \sim \mathcal{N}(\mathbf{0}, \mathbf{\Sigma}_n)$ denotes the additive Gaussian noise vector

that is assumed independent across PMUs.

To capture presence or absence of a PMU, a binary variable a_n is introduced per bus: its value is 1 if a PMU is present at the n-th bus; and 0, otherwise. For a given PMU indicator vector $\mathbf{a} := [a_1 \cdots a_{N_b}]^T$, the maximum likelihood estimate (MLE) of the system state obeying the linear-Gaussian model (1), is

$$\hat{\mathbf{v}}_{\text{MLE}} = \mathbf{A}^{-1} \left(\sum_{n=1}^{N_b} a_n \mathbf{H}_n^T \mathbf{\Sigma}_n^{-1} \mathbf{z}_n \right)$$
 (2)

where matrix A (assumed non-singular) is given by

$$\mathbf{A} := \sum_{n=1}^{N_b} a_n \mathbf{H}_n^T \mathbf{\Sigma}_n^{-1} \mathbf{H}_n. \tag{3}$$

Invertibility of A apparently depends on the non-zero entries of a. As the number of PMUs is small, especially during their initial deployment phase, system identifiability is at risk. By incorporating SCADA measurements however, it is possible to regularize the system matrix, and thus enable state estimation even when A is singular [17]. Nonetheless, simply aggregating SCADA and PMU readings faces three challenges: (i) SCADA measurements are available every 1–5secs, whereas PMU ones are sampled every 0.1–0.2secs [4]; (ii) explicitly including conventional measurements results in a nonlinear estimation problem of even higher dimensionality; and (iii) upgrading the existing estimation software to accommodate high-rate PMU readings compromises backward compatibility [17].

These challenges are addressed here using a Bayesian estimation approach. The SCADA-based state estimate $\hat{\mathbf{v}}_s$ expressed in rectangular coordinates is used as prior information for the PMU-based estimation task. Specifically, it is assumed that the actual state vector is distributed as $\mathbf{v}_o \sim \mathcal{N}(\hat{\mathbf{v}}_s, \mathbf{\Sigma}_s)$. Based on the linear model of (1) and the SCADA-based Gaussian prior information, the system state can now be obtained via the maximum a-posteriori probability (MAP) estimate uniquely given by

$$\hat{\mathbf{v}}_p = \mathbf{\Sigma}_p \left(\sum_{n=1}^{N_b} a_n \mathbf{H}_n^T \mathbf{\Sigma}_n^{-1} \mathbf{z}_n + \mathbf{\Sigma}_s^{-1} \hat{\mathbf{v}}_s \right)$$
(4)

where the so-called gain matrix is defined as [16], [13]

$$\Sigma_p := \left(\mathbf{A} + \Sigma_s^{-1}\right)^{-1}.\tag{5}$$

Standard results assure that $\hat{\mathbf{v}}_p \sim \mathcal{N}(\mathbf{v}_o, \Sigma_p)$ [9].

Phase Alignment: To align the phase of the SCADA-based prior with that of PMU readings, a PMU needs to be installed at the so-called reference bus – typically enumerated as first; see also [13]. Once the reference bus has been PMU-instrumented, its measured voltage phase is subtracted from all other PMU readings so that $\mathrm{Im}(v_o^1)$ is artificially set to zero in the PMU-based system state. This clearly implies that $a_1=1$ and that $\mathrm{Im}(v_o^1)$ is removed from the state vector. For notational brevity, \mathbf{v}_o will henceforth refer to the reduced $(2N_b-1)\times 1$ vector, while \mathbf{H}_n 's (\mathbf{A}) will denote the corresponding matrices obtained after ignoring the (N+1)-st column (and row) from the respective matrices of (1) and (3).

III. OPTIMAL PMU PLACEMENT

Building on the state estimate in (4), the problem of PMU placement can be now stated as follows. Given

- i) a power network of N_b buses (nodes);
- ii) matrices $\{\mathbf{H}_n, \mathbf{\Sigma}_n\}_{n=1}^{N_b}$ (cf. (1) and Section IV-A);
- iii) the covariance matrix Σ_s (cf. Section IV-B);
- iv) an integer k with $k \leq N$,

and assuming a PMU installed at the reference bus, the goal is to choose (k-1) buses to be PMU-instrumented so that the estimation error of (4) is minimized.

PMU placement is cast here as a variation of the optimal experimental design problem [14]. The state estimation error for a specific PMU placement a has covariance matrix $\Sigma_p(\mathbf{a}) = \left(\mathbf{A}(\mathbf{a}) + \Sigma_s^{-1}\right)^{-1}$, where the dependence on a will be explicitly denoted throughout this section.

Apparently, between two candidate placements \mathbf{a} and \mathbf{a}' with $\mathbf{1}_{N_b}^T \mathbf{a} = \mathbf{1}_{N_b}^T \mathbf{a}' = k$, placement \mathbf{a} is preferable over \mathbf{a}' if $\Sigma_p(\mathbf{a}') \succeq \Sigma_p(\mathbf{a})$. But, if $\Sigma_p(\mathbf{a}') - \Sigma_p(\mathbf{a})$ is an indefinite matrix, none of the placements is better than the other. To overcome this partial ordering limitation, placements are typically ranked based on a scalar-valued function of $\Sigma_p(\mathbf{a})$, that is to be minimized [14], [3]. Typical function choices are:

- (c1) E-optimal design: $f_E(\mathbf{a}) := \lambda_{\max}(\Sigma_p(\mathbf{a}))$, where λ_{\max} denotes the maximum eigenvalue of $\Sigma_p(\mathbf{a})$;
- (c2) A-optimal design: $f_A(\mathbf{a}) := \operatorname{trace}(\Sigma_p(\mathbf{a}))$ that is equal to the sum of the eigenvalues of $\Sigma_p(\mathbf{a})$;
- (c3) M-optimal design: $f_M(\mathbf{a}) := \max_i \{ [\Sigma_p(\mathbf{a})]_{ii} \}$ corresponding to the maximum diagonal entry of $\Sigma_p(\mathbf{a})$; and
- (c4) D-optimal design: $f_D(\mathbf{a}) := \log \det (\Sigma_p(\mathbf{a}))$, where $\det(\cdot)$ denotes matrix determinant.

Details and interesting geometric interpretations regarding choices (c1)-(c4) can be found in [3, Sec. 7.5], [14].

After this scalarization step, the C-optimal PMU placement can be found as the solution of the optimization problem

$$\tilde{\mathbf{a}}_C := \arg\min_{\mathbf{a}} \ f_C(\mathbf{a})$$
 (6a)

s.t.
$$\mathbf{a}^T \mathbf{1}_{N_b} = k, \ a_1 = 1$$
 (6b)

$$\mathbf{a} \in \{0, 1\}^{N_b} \tag{6c}$$

where $C \in \{E, A, M, D\}$. Unfortunately, solving (6) entails combinatorial complexity [3], [8]. Suboptimal solutions can be obtained by converting the troublesome binary constraint to the box constraint $\mathbf{0}_{N_b} \leq \mathbf{a} \leq \mathbf{1}_{N_b}$ with the inequalities understood entry-wise [3]. Since $\{0,1\}^{N_b} \subset [0,1]^{N_b}$, such a conversion is a *relaxation* of the original problem. After defining the relaxed feasible set $\mathcal{A} := \{\mathbf{a} : \mathbf{a}^T \mathbf{1}_{N_b} = k, \ a_1 = 1, \ \mathbf{0}_{N_b} \leq \mathbf{a} \leq \mathbf{1}_{N_b}\}$, the relaxed problem is expressed as

$$\check{\mathbf{a}}_C := \arg\min_{\mathbf{a} \in \mathcal{A}} \ f_C(\mathbf{\Sigma}_p(\mathbf{a})). \tag{7}$$

The problem in (7) can be efficiently solved as a convex problem for all $C \in \{E, A, M, D\}$ [3], [8]. The convex formulations are presented briefly for completeness.

For the E-optimal design, instead of minimizing $\lambda_{\max}(\Sigma_p(\mathbf{a}))$, one can alternatively maximize $\lambda_{\min}(\Sigma_p^{-1}(\mathbf{a}))$

by introducing the auxiliary variable t and solving the SDP

$$(\check{\mathbf{a}}_E, \check{t}_E) := \arg\min_{\mathbf{a} \in \mathcal{A}, t} \left\{ -t : \mathbf{A}(\mathbf{a}) + \mathbf{\Sigma}_s^{-1} \succeq t \mathbf{I}_{2N_b - 1} \right\}.$$
 (8)

For the A-optimal design, minimizing the trace of $\Sigma_p(\mathbf{a})$ can be accomplished after introducing the auxiliary vector variable $\mathbf{t} := [t_1 \dots t_{2N_b-1}],$ leading to the SDP

$$(\check{\mathbf{a}}_A, \check{\mathbf{t}}_A) := \arg\min_{\mathbf{a} \in A, \mathbf{t}} \ \mathbf{t}^T \mathbf{1}_{2N_b - 1} \tag{9}$$

$$(\check{\mathbf{a}}_{A}, \check{\mathbf{t}}_{A}) := \arg \min_{\mathbf{a} \in \mathcal{A}, \mathbf{t}} \mathbf{t}^{T} \mathbf{1}_{2N_{b}-1}$$

$$\text{s.t.} \begin{bmatrix} \mathbf{A}(\mathbf{a}) + \mathbf{\Sigma}_{s}^{-1} & \mathbf{e}_{k} \\ \mathbf{e}_{k}^{T} & t_{k} \end{bmatrix} \succeq \mathbf{0}, \ k = 1, \dots, 2N_{b}-1.$$

The M-optimal design can be suboptimally solved by the SDP

$$(\check{\mathbf{a}}_M, \check{t}_M) := \underset{\mathbf{a} \in \mathcal{A}, t}{\min} t$$
 (10)

s.t.
$$\begin{bmatrix} \mathbf{A}(\mathbf{a}) + \mathbf{\Sigma}_s^{-1} & \mathbf{e}_k \\ \mathbf{e}_k^T & t \end{bmatrix} \succeq \mathbf{0}, \ k = 1, \dots, 2N_b - 1.$$

Finally, the relaxed D-optimal placement can be expressed as the convex optimization problem

$$\check{\mathbf{a}}_D := \arg\min_{\mathbf{a} \in A} -\log \det \left(\mathbf{A}(\mathbf{a}) + \Sigma_s^{-1} \right) \tag{11}$$

that can be efficiently solved via standard software.

The minimizers $\check{\mathbf{a}}_C$ of (8)-(11) do not necessarily have binary entries. A simple heuristic to obtain a binary solution is to set the largest k entries of $\check{\mathbf{a}}_C$ to 1, and zero the rest [3]. The so-acquired vector, denoted by $\hat{\mathbf{a}}_C$, belongs to the feasible set of the original non-convex problem (6), but in general it is not a minimizer of (6). It provides though the upper bound $f_C(\tilde{\mathbf{a}}_C) \leq f_C(\hat{\mathbf{a}}_C)$ for all C. Additionally, due to the relaxation, the minimizers of (8)-(11) yield also the lower bound $f_C(\check{\mathbf{a}}_C) \leq f_C(\check{\mathbf{a}}_C)$ for all C. When the nonnegative gap $f_C(\hat{\mathbf{a}}_C) - f_C(\check{\mathbf{a}}_C)$ becomes zero, the relaxation is deemed exact in the sense that $f_C(\hat{\mathbf{a}}_C) = f_C(\tilde{\mathbf{a}}_C)$ [8].

IV. INPUT DATA

A. Regression Matrices

Let $\tilde{\mathbf{v}} = \mathbf{v}_r + j\mathbf{v}_i$ be the $N_b \times 1$ vector of complex nodal voltages. The vector of complex nodal currents is [18]

$$\tilde{\mathbf{i}} = \mathbf{Y}\tilde{\mathbf{v}} \tag{12}$$

where $\mathbf{Y} \in \mathbb{C}^{N_b \times N_b}$ denotes the bus admittance matrix, and can be explicitly expressed in terms of the bus admittances, the line series admittances and charging susceptances, and potential tap ratios and phase shifters. Similarly, the $2N_l \times 1$ vector of complex line currents can be written as [18]

$$\tilde{\mathbf{i}}_{\mathrm{fl}} = \mathbf{Y}_{\mathrm{fl}}\tilde{\mathbf{v}} \tag{13}$$

where $\mathbf{Y}_{\mathrm{fl}} \in \mathbb{C}^{2N_l \times N_b}$ is the line-to-bus admittance matrix. Note that the current flowing from bus m to bus n is not equal to the negative of the current flowing in the reverse direction. Hence, each line is considered twice in i_{fl} .

The regression matrices \mathbf{H}_n in (1) can now be expressed as

$$\mathbf{H}_{n} = \begin{bmatrix} \mathbf{e}_{n}^{T} & \mathbf{0}^{T} \\ \mathbf{0}^{T} & \mathbf{e}_{n}^{T} \\ \mathbf{S}_{n} \operatorname{Re}(\mathbf{Y}_{f}) & -\mathbf{S}_{n} \operatorname{Im}(\mathbf{Y}_{f}) \\ \mathbf{S}_{n} \operatorname{Im}(\mathbf{Y}_{f}) & \mathbf{S}_{n} \operatorname{Re}(\mathbf{Y}_{f}) \end{bmatrix}$$
(14)

where e_n is the n-th canonical vector, and S_n denotes the binary $L_n \times 2N_l$ matrix selecting the rows of \mathbf{Y}_{fl} corresponding to the lines originating from bus n.

B. Covariance of the SCADA-based State Estimate

Conventional power meters measure subsets of nodal real and reactive power injections, real and reactive power line flows, as well as nodal voltage magnitudes. Consider M such measurements concatenated in $\mathbf{z} \in \mathbb{R}^M$ that are related to \mathbf{v}_o through the function $\mathbf{h}(\cdot): \mathbb{R}^{2N_b} \to \mathbb{R}^M$. The SCADA reading

$$\mathbf{z} = \mathbf{h}(\mathbf{v}_o) + \mathbf{e} \tag{15}$$

where $\mathbf{e} \sim \mathcal{N}(\mathbf{0}, \mathbf{\Sigma}_e)$ denotes the noise vector. Supposing that the SCADA-based estimate $\hat{\mathbf{v}}_s$ has converged to the MLE based on (15), the estimate $\hat{\mathbf{v}}_s$ is asymptotically (as $M \to \infty$) normal; that is, $\hat{\mathbf{v}}_s \stackrel{a}{\sim} \mathcal{N}(\mathbf{v}_o, \Sigma_s(\mathbf{v}_o))$, where [9]

$$\Sigma_s(\mathbf{v}_o) = \left(\mathbf{J}^T(\mathbf{v}_o)\Sigma_e^{-1}\mathbf{J}(\mathbf{v}_o)\right)^{-1} \tag{16}$$

and $\mathbf{J}(\mathbf{v}_o) := \nabla_{\mathbf{v}} \mathbf{h}(\mathbf{v})|_{\mathbf{v} = \mathbf{v}_o}$ is the $M \times 2N_b$ Jacobian matrix of h(v) evaluated at $v = v_o$. Since h(v) is nonlinear, the covariance matrix in (16) depends on the actual nodal voltage values \mathbf{v}_{o} , which are unknown. To resolve this vicious cycle, one approach is to surrogate (16) by $\Sigma_s(\hat{\mathbf{v}}_s)$ in (4).

For optimal PMU placement however, the goal is to solve (6) or (7) without having acquired any measurements and for all possible values of v_o . Toward this end, the idea here is to replace \mathbf{v}_o in (16) by the so-called flat voltage profile $\mathbf{v}_{\mathrm{flat}} := [\mathbf{1}_{N_{b}}^{T} \ \mathbf{0}_{N_{b}}^{T}]^{T}$, commonly used for initializing state estimation algorithms [16]. For this reason, wherever Σ_s appears in the previous section it is replaced by $\Sigma_s(\mathbf{v}_{\text{flat}})$. Then, the Jacobian $J(v_{flat})$ can be neatly expressed in terms of Y and Y_{fl}. (Pertinent expressions are skipped due to space limitations.)

V. SIMULATED TESTS

The relaxed PMU placement methods developed are evaluated numerically in this section using the IEEE 14- and 118-bus power network benchmarks [15], [18]. Regarding SCADA measurements, 50% of the bus voltage magnitudes and (real/reactive) powers are measured at buses and lines, selected uniformly at random. The covariance matrix Σ_e is modeled as diagonal: the standard deviation for voltage magnitude, bus power injections, and line power flows is 0.01, 0.015, and 0.02, respectively [18]. PMU measurements are assumed to have a diagonal Σ_n too. Compliant with SCADA readings, the standard deviation for bus voltage and line current PMU readings is 0.01 and 0.02, respectively.

For the IEEE 14-bus test case, PMU placements were evaluated for $k \in [1, 14]$ units. Figures 1-3 depict the cost values for: (i)-(ii) $f_C(\hat{\mathbf{a}}_C)$ and $f_C(\check{\mathbf{a}}_C)$ acquired by solving (7); (iii) a random placement cost; and, (iv) the cost $f_C(\tilde{\mathbf{a}}_C)$ obtained by exhaustively solving (6); all for $C \in \{A, M, D\}$. In most cases tested, $\hat{\mathbf{a}}_C$ coincided with $\tilde{\mathbf{a}}_C$, as verified by the zero gap $f(\hat{\mathbf{a}}_C) - f(\check{\mathbf{a}}_C)$. In general, $\hat{\mathbf{a}}_M$ differs from $\tilde{\mathbf{a}}_M$ by at most one unit. For the IEEE 118-bus case,

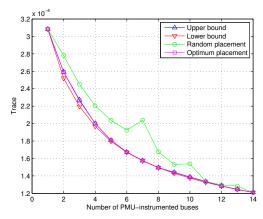


Fig. 1. IEEE 14-bus test case: A-optimal design.

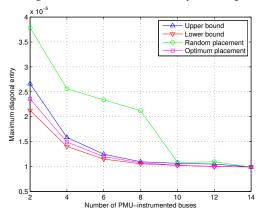


Fig. 2. IEEE 14-bus test case: M-optimal design.

the network size renders combinatorial search prohibitively complex. However, the relaxed E-optimal PMU placement is possible, as confirmed by Fig. 4.

Regarding the 14-bus test case, note first that the minimizers $\tilde{\mathbf{a}}_C$ for $C \in \{E, M\}$ do not possess a nesting property for varying k, meaning that the optimal k-placement is not necessarily a subset of the optimal (k+1)-one. This implies that greedy approaches cannot yield the optimum placement. Secondly, the numerical tests conducted verify the importance of the SCADA-based prior: For the A-optimal placement and k=14, the trace of $\Sigma_p(\tilde{\mathbf{a}}_A)$ increases from 1×10^{-4} to 2.5×10^{-4} when the prior is ignored. More critically, without the SCADA-based prior in (4), one needs $k\geq 5$ units to obtain a finite $\Sigma_p(\tilde{\mathbf{a}}_A)$.

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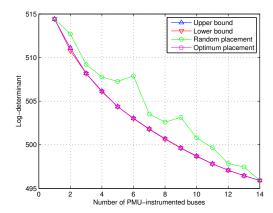


Fig. 3. IEEE 14-bus test case: D-optimal design.

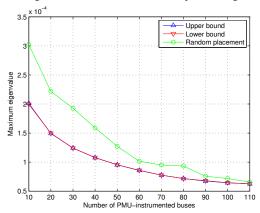


Fig. 4. IEEE 118-bus test case: E-optimal design.

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