

# ECE606: Solid State Devices

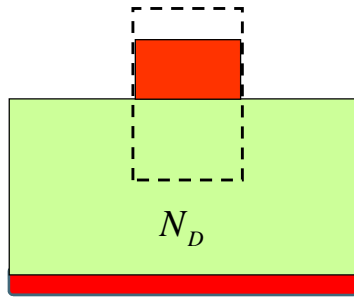
## Lecture 17

### Schottky Diode

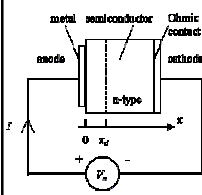
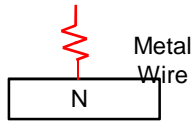
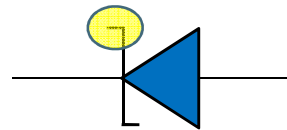
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- 1) Importance of metal-semiconductor junctions
- 2) Equilibrium band-diagrams
- 3) DC Thermionic current (simple derivation)
- 4) Intermediate Summary
- 5) DC Thermionic current (detailed derivation)
- 6) AC small signal and large-signal response
- 7) Additional information
- 8) Conclusions

**Reference:** Semiconductor Device Fundamentals, Chapter 14, p. 477



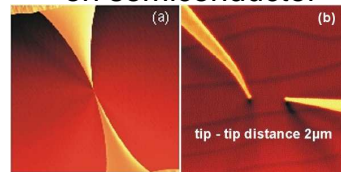
Symbols



Detectors

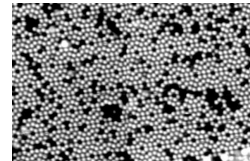


STM(scanning tunneling microscope) on semiconductor




[www.fz-juelich.de/ibn/index.php?index=674](http://www.fz-juelich.de/ibn/index.php?index=674)

Original Bipolar Transistors



Originally, Gelena (PbS), Si as semiconductor and Phospor Bronze for metal (cat's whisker)

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	<b>Equilibrium</b>	<b>DC</b>	<b>Small signal</b>	<b>Large Signal</b>	<b>Circuits</b>
<b>Diode</b>					
<b>Schottky</b>					
<b>BJT/HBT</b>					
<b>MOS</b>					

$$\nabla \cdot \mathbf{D} = q(p - n + N_D^+ - N_A^-)$$

$$\frac{\partial n}{\partial t} = \frac{1}{q} \nabla \cdot \mathbf{J}_N - r_N + g_N$$

$$\mathbf{J}_N = qn\mu_N \mathbf{E} + qD_N \nabla n$$

$$\frac{\partial p}{\partial t} = -\frac{1}{q} \nabla \cdot \mathbf{J}_P - r_P + g_P$$

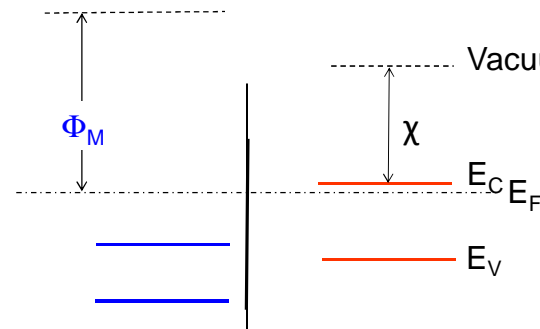
$$\mathbf{J}_P = qp\mu_P \mathbf{E} - qD_P \nabla p$$

Equilibrium

DC  $dn/dt=0$

Small signal  $dn/dt \sim j\omega n$

Transient --- full sol.



1.  $E_F$  flat in equilibrium
2.  $E_C/E_V$  in Metal
3. Vacuum level in Metal
4.  $E_C/E_V$  in semiconductor
5. Vacuum level in semiconductor

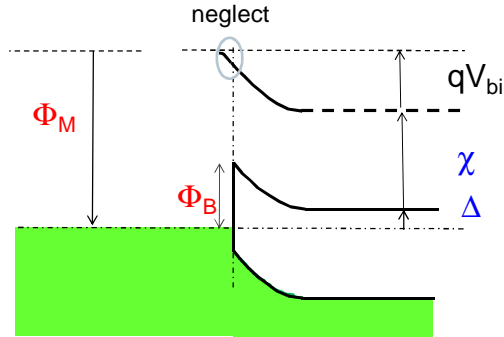
6. Connect vacuum levels

7. duplicate the connections down to  $E_C/E_V$

Since  $N_A$  is large,  
 $x_p$  is negligible ..

$$N_A x_p = N_D x_n$$

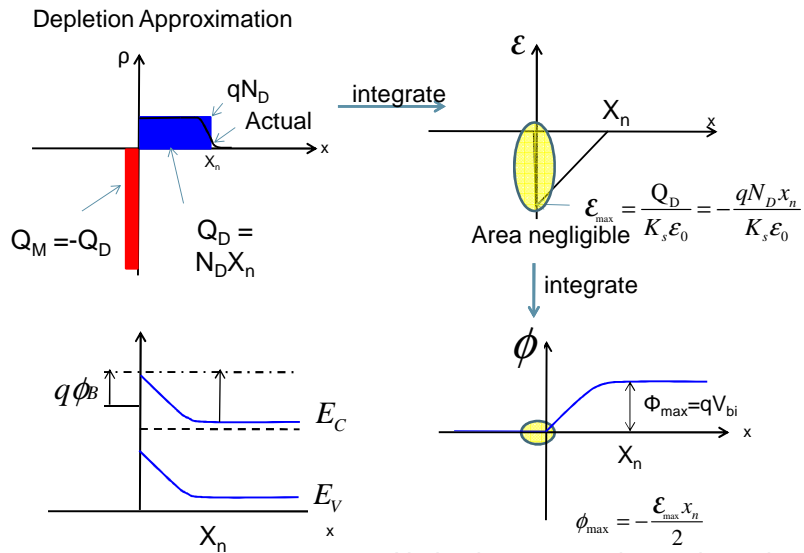
Charge(metal) = charge(semiconductor)



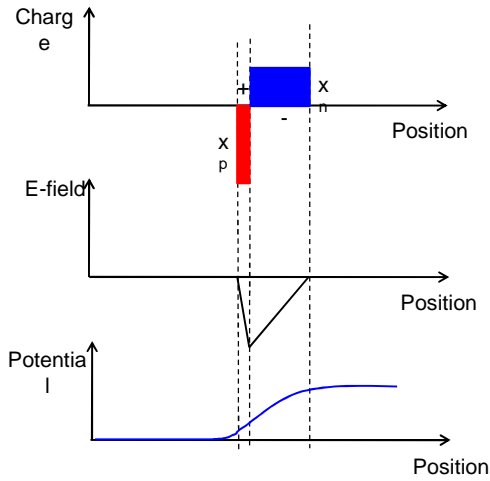
$$\Delta + \chi + qV_{bi} = \Phi_M$$

$$qV_{bi} = (\Phi_M - \chi) - \Delta \equiv \Phi_B - \Delta$$

$$= \Phi_B - k_B T \ln \frac{N_D}{N_C} \text{ non-degenerate}$$



Notice how we neglect  $x_p$  here, is there a proof?



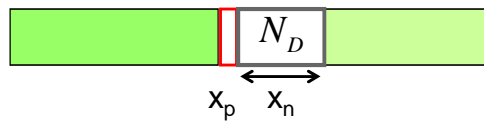
$$\mathcal{E}(0^+) = \frac{qN_D x_n}{k_s \epsilon_0}$$

$$\mathcal{E}(0^-) = \frac{qN_M x_p}{k_s \epsilon_0} \quad ?$$

$$\Rightarrow N_D x_n = N_M x_p$$

$$qV_{bi} = \frac{\mathcal{E}(0^-) x_n}{2} + \frac{\mathcal{E}(0^+) x_p}{2}$$


$$= \frac{qN_D x_n^2}{2k_s \epsilon_0} + \frac{qN_M x_p^2}{2k_s \epsilon_0}$$



$$\left. \begin{aligned} N_D x_n &= N_M x_p \\ qV_{bi} &= \frac{qN_D x_n^2}{2k_s \epsilon_0} + \frac{qN_M x_p^2}{2k_s \epsilon_0} \end{aligned} \right\} \begin{aligned} x_n &= \sqrt{\frac{2k_s \epsilon_0}{q} \frac{N_M}{N_D (N_M + N_D)} V_{bi}} \rightarrow \sqrt{\frac{2k_s \epsilon_0}{q} \frac{1}{N_D} V_{bi}} \\ x_p &= \sqrt{\frac{2k_s \epsilon_0}{q} \frac{N_D}{N_M (N_M + N_D)} V_{bi}} \rightarrow 0 \end{aligned}$$

This is why we can neglect  $x_p$

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	Equilibrium	DC	Small signal	Large Signal	Circuits
Diode					
Schottky					
BJT/HBT					
MOS					

$$\nabla \cdot \mathbf{D} = q(p - n + N_D^+ - N_A^-) \leftarrow \text{Band diagram ...}$$

$$\frac{\partial n}{\partial t} = \frac{1}{q} \nabla \cdot \mathbf{J}_N - r_N + g_N$$

This works for doping-modulated Semiconductors.

$$\mathbf{J}_N = qn\mu_N \mathbf{E} + qD_N \nabla n \leftarrow$$

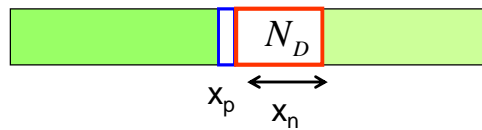
Does not work for heterostructures (when the conduction band is not continuous)

$$\frac{\partial p}{\partial t} = -\frac{1}{q} \nabla \cdot \mathbf{J}_P - r_P + g_P$$

Metal-Semiconductor is a HS

$$\mathbf{J}_P = qp\mu_p \mathbf{E} - qD_P \nabla p$$

*Need theory of thermionic emission*

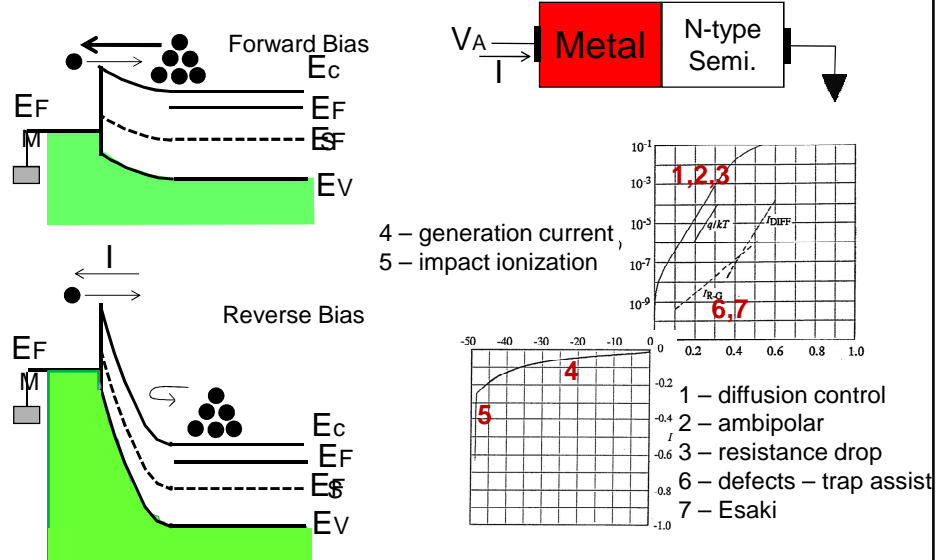
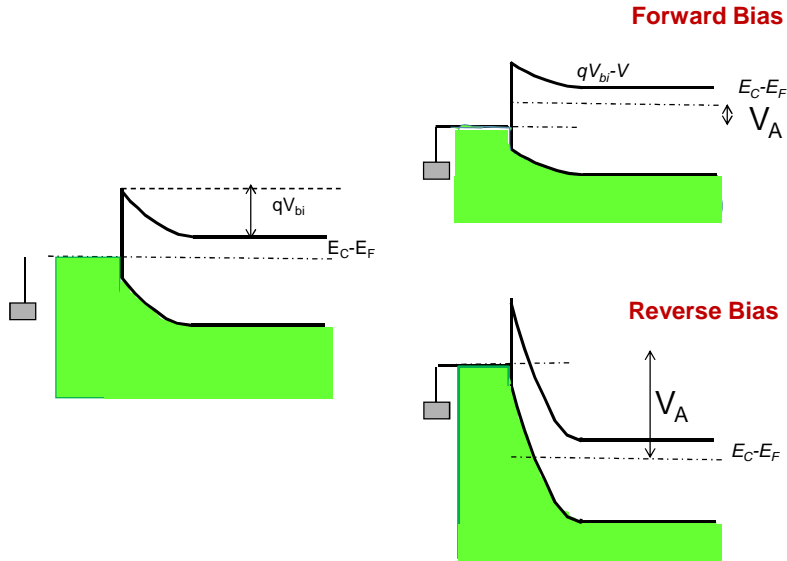


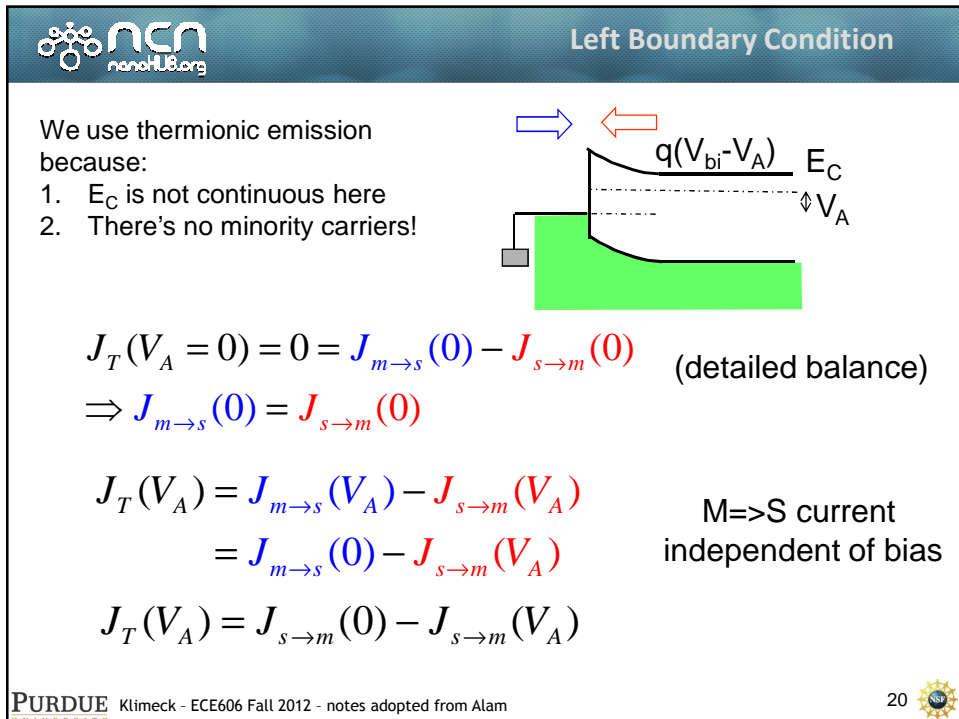
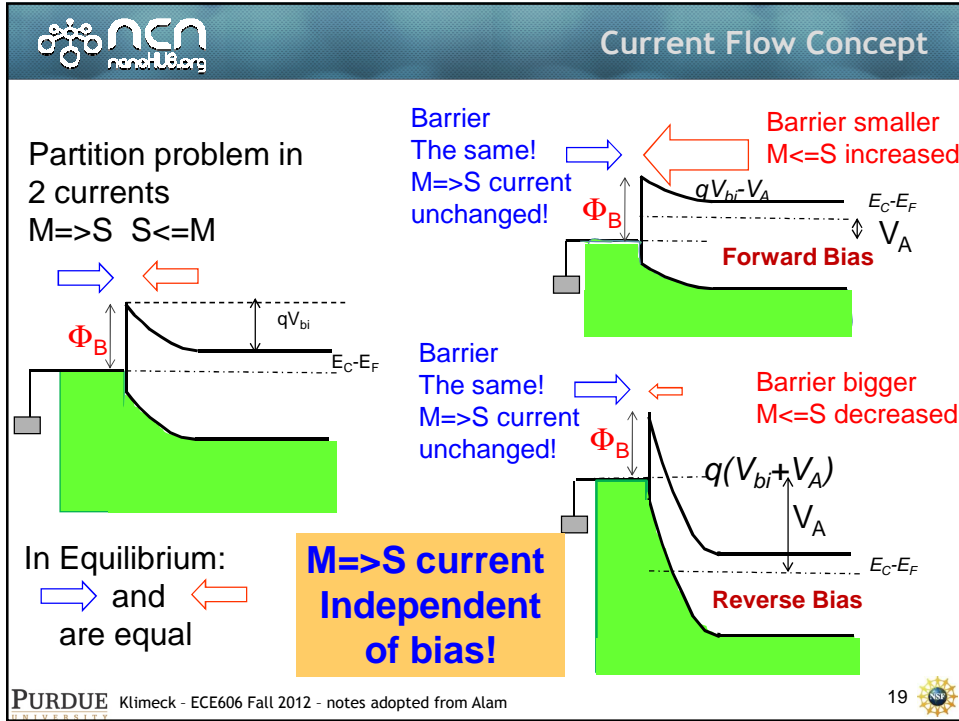
$$x_n = \sqrt{\frac{2k_s \epsilon_0}{q} \frac{N_M}{N_D(N_M + N_D)} V_{bi}} \rightarrow \sqrt{\frac{2k_s \epsilon_0}{q} \frac{1}{N_D} (V_{bi} - V_A)}$$

$$x_p = \sqrt{\frac{2k_s \epsilon_0}{q} \frac{N_D}{N_M(N_M + N_D)} V_{bi}} \rightarrow 0$$

Forward bias:  $x_n$  decrease  
Reverse bias:  $x_n$  increase







$J_{m \rightarrow s}(0) =$ 
 $J_{s \rightarrow m}(0) = -q \frac{n_s}{2} e^{-q \frac{V_{bi}}{kT}} v_{th} = J_{m \rightarrow s}(0)$

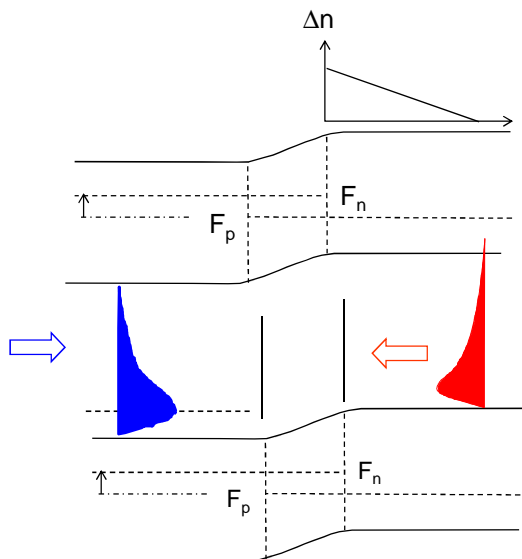
$J_{m \rightarrow s}(V_A) = -q \frac{n_m}{2} e^{-\frac{q\Phi_B}{kT}} v_{thM}$ 
 $J_{s \rightarrow m}(V_A) = -q \frac{n_s}{2} e^{-q \frac{V_{bi}-V_A}{kT}} v_{th}$

$= -q \frac{n_s v_{th}}{2} e^{-\frac{qV_{bi}}{kT}} \times e^{\frac{qV_A}{kT}}$   
 $= J_{s \rightarrow m}(0) e^{\frac{qV_A}{kT}}$   
 $= J_{m \rightarrow s}(0) e^{\frac{qV_A}{kT}}$   
 $= -q \frac{n_m v_{thM}}{2} e^{-\frac{q\Phi_B}{kT}} e^{\frac{qV_A}{kT}}$

$J_T = J_{s \rightarrow m}(0) - J_{s \rightarrow m}(V_A) = \frac{qn_m v_{thM}}{2} e^{-\frac{q\Phi_B}{kT}} \left[ e^{\frac{qV_A}{kT}} - 1 \right]$

Only half of them goes to the right

Only the ones above the barrier goes to the right



Check that both gives the same result for a diode...

Thermionic Emission theory is a more general approach, can be used for device so small that no scattering occur (can't use diffusion!).

Schottky barrier diode is a majority carrier device of great historical importance.

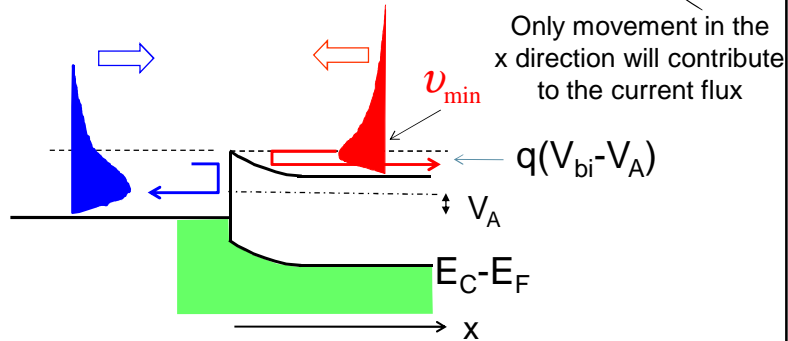
There are similarities and differences with p-n junction diode: for electrostatics, it behaves like a one-sided diode, but current, the drift-diffusion approach requires modification.

The trap-assisted current, avalanche breakdown, Zener tunneling all could be calculated in a manner very similar to junction diode.

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Energy  $< \frac{1}{2} m^* v_{\min}^2$  will be bounced back ~ DOS Occupation (non-degenerate)

$$J_{s \rightarrow m} = -q \int_{-\infty}^{\infty} \int_{-\infty}^{\infty} \int_{-\infty}^{-v_{\min}} \frac{\Omega}{4\pi^3} dk_x dk_y dk_z e^{-(E-E_F)\beta} v_x$$



Only movement in the x direction will contribute to the current flux

$$J_{s \rightarrow m} = -q \int_{-\infty}^{\infty} \int_{-\infty}^{\infty} \int_{-\infty}^{-v_{\min}} \frac{\Omega}{4\pi^3} dk_x dk_y dk_z e^{-(E-E_F)\beta} v_x$$

$$E - E_F = (E - E_C) + (E_C - E_F) = \frac{1}{2} m^* v_x^2 + \frac{1}{2} m^* v_y^2 + \frac{1}{2} m^* v_z^2 + (E_C - E_F)$$

$$= q e^{-(E_C - E_F)\beta} \int_{-\infty}^{\infty} \int_{-\infty}^{\infty} \int_{-\infty}^{-v_{\min}} \frac{\Omega}{4\pi^3} \frac{d(m^* v_x)}{\hbar} \frac{d(m^* v_y)}{\hbar} \frac{d(m^* v_z)}{\hbar} e^{-\frac{(m^* v_x^2 + m^* v_y^2 + m^* v_z^2)}{2} \beta} v_x$$

$$J_{s \rightarrow m} = \frac{q\Omega(m^*)^3}{4\pi^3 \hbar^3} e^{-(E_C - E_F)\beta} \left[ \int_{-\infty}^{\infty} e^{-\left(\frac{m^* v_y^2}{2}\right)\beta} dv_y \right] \left[ \int_{-\infty}^{\infty} e^{-\left(\frac{m^* v_z^2}{2}\right)\beta} dv_z \right] \left[ \int_{-\infty}^{-v_{\min}} dv_x e^{-\left(\frac{m^* v_x^2}{2}\right)\beta} v_x \right]$$

$$v_{\min} = \sqrt{\frac{2q}{m^*}}(V_{bi} - V_A)$$

$$J_{s \rightarrow m} = \frac{q\Omega(m^*)^3}{4\pi^3\hbar^3} e^{-(E_c - E_F)\beta} \left[ \int_{-\infty}^{\infty} e^{-\left(\frac{m^*v_y^2}{2}\right)\beta} dv_y \right] \left[ \int_{-\infty}^{\infty} e^{-\left(\frac{m^*v_z^2}{2}\right)\beta} dv_z \right] \left[ \int_{-\infty}^{-v_{\min}} dv_x e^{-\left(\frac{m^*v_x^2}{2}\right)\beta} v_x \right]$$

$\sqrt{\pi}$

$\sqrt{\pi}$

$\frac{1}{2} e^{-q(V_{bi} - V_A)\beta}$

$$J_{s \rightarrow m} = \frac{4\pi q m^* k^2}{h^3} T^2 e^{(E_F - E_C - qV_{bi})\beta} e^{qV_A\beta} = A_0 e^{qV_A\beta}$$

$$J_T = J_{s \rightarrow m} - J_{m \rightarrow s} = A_0 (e^{qV_A\beta} - 1)$$

Compare to the current of p-n diodes...

$$J_T = J_{s \rightarrow m} - J_{m \rightarrow s} = A_0 (e^{qV_A\beta} - 1) \quad \text{Schottky diode}$$

$$J_T = -q \left[ \frac{D_n}{W_p} \frac{n_i^2}{N_A} + \frac{D_p}{W_n} \frac{n_i^2}{N_D} \right] (e^{qV_A\beta} - 1) \quad \text{p-n diode}$$

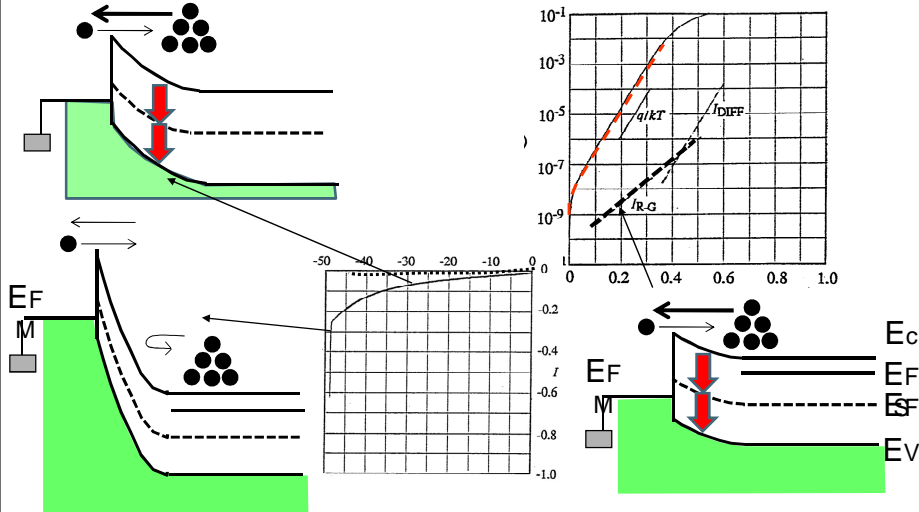
Both of them depends exponentially on  $V_A$ , however current of p-n diodes depends more on temperature (since  $n_i$  depends strongly on  $E_g$ ), where the Schottky diode doesn't have that dependence.

However,  $E_g$  hides in...

$$J_{s \rightarrow m} = \frac{4\pi q m^* k^2}{h^3} T^2 e^{(E_F - E_C - qV_{bi})\beta} e^{qV_A\beta} = A_0 e^{qV_A\beta}$$

The information of material hides in  $m^*$

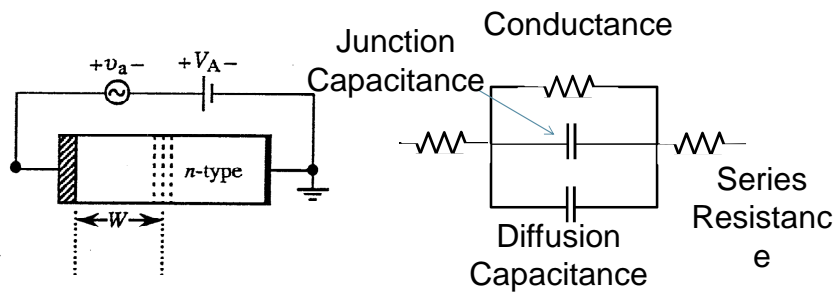
The information of doping concentration hides in  $E_F - E_C$



**SAME technique as in p-n junction except integrate to  $x_p$  only**

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Schottky			◆		
BJT/HBT					
MOS					



We will not have Diffusion Capacitance here...



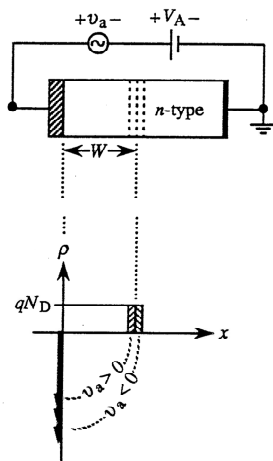
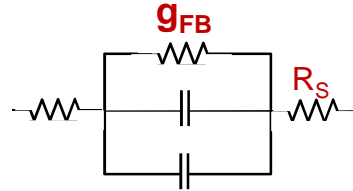
$m$  depends on which operation regime you are

$$I = I_o \left( e^{q(V_A - R_S I) \beta / m} - 1 \right)$$

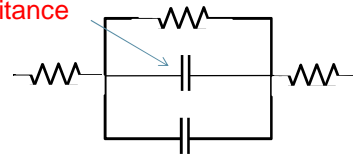
$$\ln \frac{I + I_o}{I_o} = q(V_A - R_S I) \beta / m$$

$$\frac{m}{q\beta(I + I_o)} = \frac{dV_A}{dI} - R_S$$

$$\frac{1}{g_{FB}} = R_S + \frac{m}{q\beta(I + I_o)}$$



Junction Capacitance

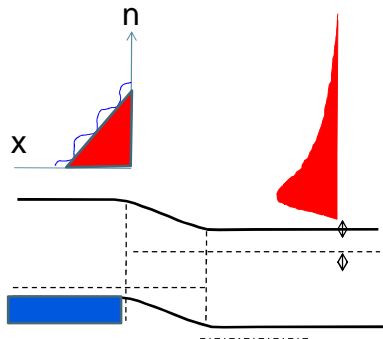


$$C_J = \frac{\kappa_s \epsilon_0 A}{W}$$

$$C_J = \frac{\kappa_s \epsilon_0 A}{\sqrt{\frac{2\kappa_s \epsilon_0}{qN_D} (V_{bi} - V_A)}}$$

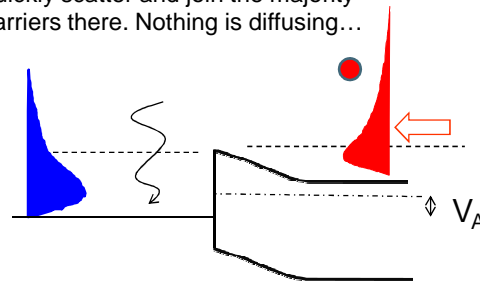
Response time – dielectric  
Very fast propagation

**p-n Diode**



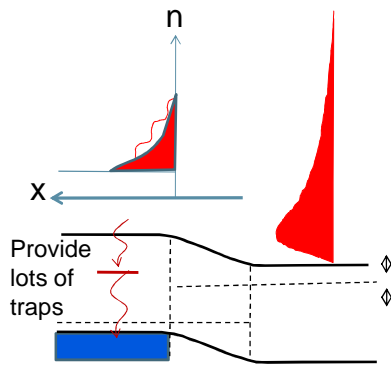
**Schottky diode**

When electrons reach the metal side, they quickly scatter and join the majority carriers there. Nothing is diffusing...

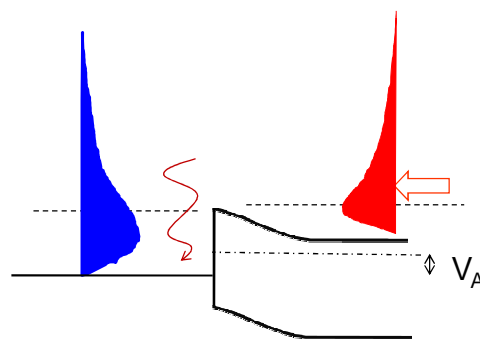


No minority carrier transport and therefore no diffusion capacitance ..

**p-n Diode**



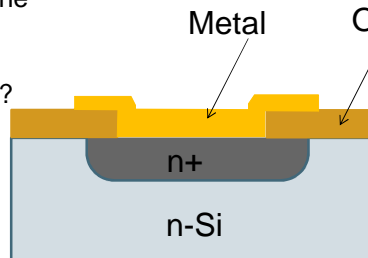
**Schottky diode**



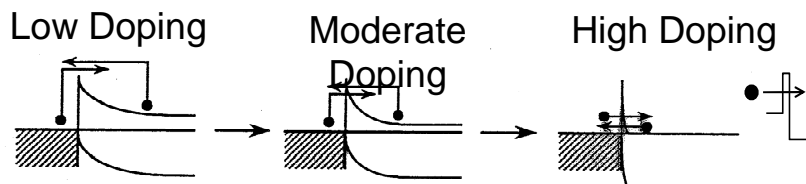
Short minority carrier lifetime in p-n junction diode equivalent to rapid energy relaxation in SB diode.

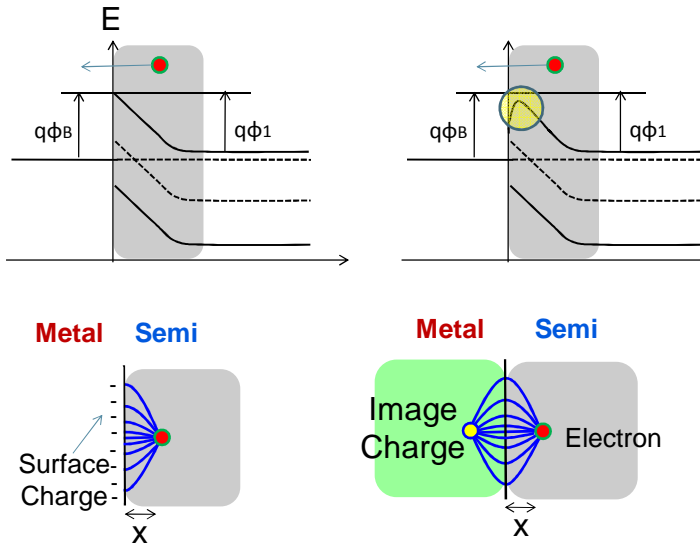
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What if we just want the p-n junction? How to reduce the barrier of Metal-Semiconductor?

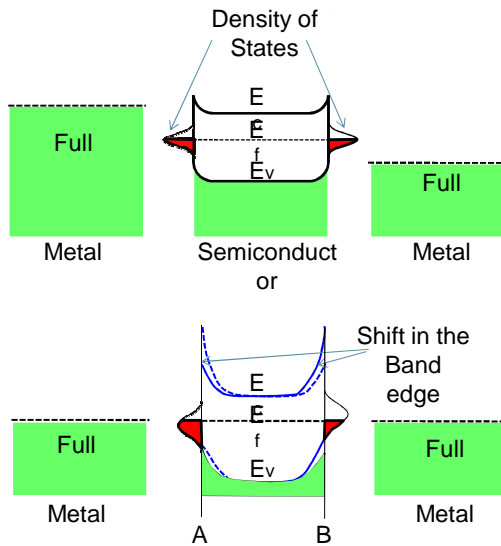


For majority carriers:  
there's not barrier  
For minority carriers:  
high doping allows them  
to tunnel through  
→Metal will just act like  
Ohmic Contact





The fields on the metal must be perpendicular (otherwise there will be tangential current flow which is impossible) as if there's an image charge (positive) in the metal, results in lowering the barrier height



Why sometimes we don't get conductance at all?  
 surface states bends  $E_C/E_V$  so that the Fermi level is at the center of the bandgap, they exchange carriers with metals, blocking the bulk semiconductor inside → no modulation in potential barrier even you connected to different metals.

Regardless the workfunction, no modulation in potential. (e.g. Modern high-k dielectrics)

- 1) Schottky diodes have wide range of applications in practical devices.
- 2) The key distinguishing feature of Schottky diode is that it is a majority carrier device.
- 3) We use a different technique to calculate the current in a majority carrier device.  
=> thermionic emission.
- 4) Elimination of diffusion capacitance make the response of the diodes very fast.

