Electrical Engineering 452 Electric Machines and Power Electronic Drives (Spring 2013)

Instructor Dr. Dionysios Aliprantis, Assistant Professor, ECpE Contact 1124 Coover Hall, 294-7387, dali@iastate.edu

Office hours Wednesday 9:00–10:00 am

Classroom Coover 1011

Tuesday & Thursday 11:00-11:50 am

Laboratory Coover 1102

Section A: Tuesday 4:10–7:00 pm Section B: Wednesday 12:10–3:00 pm Section C: Wednesday 3:10–6:00 pm

Web page http://home.eng.iastate.edu/~dali/EE452_index.html

Grades will be posted on Blackboard Learn

TA #1 (labs) Nicholas David
Contact ndavid@iastate.edu

Office hours TBD

TA #2 (grader) Renhai Zhou

Contact <u>leo2009@iastate.edu</u>

Office hours TBD

Textbook #1 A. M. Trzynadlowski, "Introduction to Modern Power

Electronics," 2nd edition, Wiley, 2010, ISBN 978-0-470-40103-3

Textbook #2 P. C. Krause, O. Wasynczuk, S. D. Pekarek, "*Electromechanical*

Motion Devices," 2nd edition, Wiley, 2012, ISBN 978-1-1182-

9612-7

Learning Goals:

- 1. Fundamentals of power electronic circuits
- 2. Fundamentals of rotating electric machines and motor drives (electromechanical energy conversion)
- 3. Applications of power electronics and motor drives, especially in renewable energy and electric vehicles
- 4. Computer simulation of power electronics and motor drives
- 5. Microprocessor-based control of power electronics and motor drives

Course Prerequisites:

E E 303; E E 330 or E E 332; credit or registration in E E 324. Familiarity with Matlab/Simulink is useful.

Tests:

There will be two or three announced midterms, and a final exam (or project). You are expected to take all tests unless you become injured or very ill or experience a death in your family (in such cases, please contact me as early as possible).

Homework Assignments and Lab Exercises:

Besides the tests there will be homework assignments. For each assignment, it will be your responsibility to complete and hand in on time.

Lab exercises are to be delivered at the end of each lab session.

Class/Lab Attendance:

You are strongly encouraged to attend class, but roll will not be called. You are responsible for all information presented in class. The Web site, instructor, and TAs, although freely available to you, are not responsible for providing you with in-class information if you choose not to attend class.

Attending the laboratory sessions is mandatory, and it will be monitored closely.

Class/Lab Preparation:

Reading the appropriate material in the text before the class or laboratory session is essential.

Course Grading Policy:

Homework	20%
Laboratory exercises	40%
Midterm exams	20%
Final exam	20%

Letter grades will be determined by the following guidelines:

≥85%	A
≥80%	A-
≥75%	B+
≥70%	В
≥65%	B-
≥60%	C+
≥55%	С
≥50%	C-
<50%	F

Communication:

Feel free to communicate with me in any way that is convenient to you (after class, during office hours, phone, e-mail), for questions about the course material or assignments. Inquiries regarding recordings of your grades should be directed to

the grader. All other inquiries, including questions on the grading of assignments or in-class material, special situations, etc., should be directed to the instructor.

Special Needs:

Please address any special needs or special accommodations with me at the beginning of the semester or as soon as you become aware of your needs. Those seeking accommodations based on disabilities should obtain a Student Academic Accommodation Request (SAAR) form from the Disability Resources (DR) office (515-294-7220 or TTY 515-294-6635). DR is located on the main floor of the Student Services Building, Room 1076.

Academic Misconduct:

Academic Misconduct in any form is in violation of Iowa State University Student Disciplinary Regulations and will not be tolerated. This includes, but is not limited to: copying or sharing answers on tests or assignments, plagiarism, and having someone else do your academic work. Depending on the act, a student could receive an F grade on the test/assignment, F grade for the course, and could be suspended or expelled from the University. See the Conduct Code at http://www.dso.iastate.edu/ja for more details and a full explanation of the Academic Misconduct policies.

Lab Schedule

LAB#	TOPIC
1	Introduction to simulation of power electronic circuits with
	Matlab/Simulink & ASMG modeling software
2	Introduction to the TI F28035 microcontroller
3	Figures of merit
4	Introduction to dc/dc choppers.
	Buck converter with RL load (simulation)
5	Buck converter with RL load (hardware) – Part I
6	Buck converter with RL load (hardware) – Part II
7	Three-phase inverter with space vector modulation (simulation)
8	Three-phase inverter with space vector modulation (hardware)
9	Squirrel-cage induction motor – steady-state performance with V/Hz
	control (hardware)
10	Quadrature encoder – speed and position sensing
11	Squirrel-cage induction motor dynamics (simulation)
12	Squirrel-cage induction machine parameter identification (hardware)
13	Vector control of induction motor (simulation)
14	Vector control of induction motor (hardware)
15	Electric vehicle motor drive – system design