# ECE 255 ELECTRONIC CIRCUIT ANALYSIS AND DESIGN Fall 2017

**Division 2: Chew TR 1:30PM - 2:45PM ECE 170**

TEXT: Microelectronic Circuits, 7th Edition, Adel S. Sedra and Kenneth C. Smith, 2014

## INSTRUCTOR: Weng Cho CHEW, WANG 3053, Phone 4-5402, Email: wcchew@purdue.edu

OFFICE HOUR: TR: 3:00PM – 4:00PM, at WANG 3053 or by appointment

TEACHING ASSISTANT: Hongjie JIANG Email: jiang289@purdue.edu

 Gregory CHANG Email: changg@purdue.edu

## TA OFFICE HOUR at MSEE 180: Hongjie JIANG: Mon and Fri  10-12 am, Thu 4-6 pm

Greg CHANG: Tue 1-4 pm, Wed 2:30-5:30 pm

COURSE WEBSITE: Purdue Black Board Learn. Login Required.

LEARNING OBJECTIVES:

*A student who successfully fulfills the course requirements will have demonstrated:*

1. The ability to identify and correctly utilize the external lead structure and basic electrical characteristics of common semiconductor devices (pn junctions, MOSFETs, and BJTs).
2. The ability to analyze and design d.c. bias circuits.
3. The ability to utilize d.c. and a.c. models of semiconductor devices in both analysis and design.
4. The ability to analyze and design single and multistage amplifiers at low, mid and high frequencies.
5. The ability to use a CAD tool (e.g., SPICE) in circuit analysis and design.

GRADING:

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| --- | --- | --- | --- | --- | --- |
| Homework & SPICE | 10% |  |  |  |  |
| In Class Participation | 5% |  |  |  |  |
| Exam 1 | 15% | Wed | Sep. 27 | 8:00pm-9:00pm | WTHR 200 |
| Exam 2Exam 3 | 15%15% | MonWed | Oct. 16Nov. 15 | 6:30pm-7:30pm8:00pm-9:00pm | PHYS 114 WTHR 200 |
| Final | TBD |  |  |  |  |

All students need to meet the listed learning objectives and outcomes in order to pass the course. Course grading will be A, B, C, D, F, I. All grades will be posted on Black Board Learn as soon as they are available.

EXAMS:

All exams are closed book and close notes. The only calculator allowed in the exam is the TI-30X IIS (available at the University Book Store or Follett)

HOMEWORK:

Homework assignments will be distributed weekly and due the following week unless specified otherwise by the instructor. Homework should be dropped in the assigned box in MSEE 180 by 5:00pm on the due day. You may work together on homework, as it may help understanding and learning the material effectively. Please list the names of the other students from your discussion group and be sure to hand in your own work. Solutions to all homework will be posted on Blackboard after the due day.

SPICE PROJECTS:

You CANNOT PASS the course unless you earn at least 50% of the SPICE project credits.

IN CLASS PARTICIPATION AND CLASS ATTENDANCE:

You are encouraged to participate in class by asking questions, answering questions, and attend the class regularly.

 GROUP CONTACT:

A mass email distribution list will be created through Blackboard. Please make sure you check your registered email address for course announcements and other important information.

ACADEMIC DISHONESTY POLICIES:

Every member of the Purdue community is expected to practice honorable and ethical behavior both inside and outside the classroom. Any actions that might unfairly improve a student’s score on homework, quizzes, or examinations will be considered cheating and will not be tolerated.

Examples of cheating include (but are not limited to):

* Sharing results or other information during an examination.
* Bringing forbidden material or devices to an examination.
* Working on an exam before or after the official time allowed.
* Requesting a re-grade of answers or work that has been altered.
* Submitting homework that is not your own work or engaging in forbidden homework collaborations.
* Let others use your clicker (if clicker is used) and pretend to be you in class

At the instructor’s discretion, cheating on an assignment or examination will result in a reduced score, a zero score, or a failing grade for the course. All occurrences of academic dishonesty will be reported to the Assistant Dean of Students and copied to the ECE Associate Head of Education. If there is any question as to whether a given action might be considered as cheating, please see the instructor for the teaching assistant before you engage in any such action.

INCOMPLETE, HOMEWORK, EXAM AND EMERGENCY POLICIES:

1. Incomplete grade is only for students who do most of the required work and at the end of the semester due to a **well-documented emergency** cannot finish the course.
2. **No make-up exams** will be given.
3. **Only** in **well-documented emergency situations,** we will allow a student to take the exam at a different time, no other excuses are accepted. Hunting, fishing, family reunions, fraternity events are not considered emergencies.
4. You cannot do extra work after the semester is over to change your grade. **All grades are FINAL once submitted**.
5. If you have any issue or difficulty with the course **you need to contact us during the semester and seek help in advance.**
6. In the event of a major campus emergency, course requirements, deadlines and grading percentages are subject to changes that may be necessitated by a revised semester calendar or other circumstances beyond the instructor’s control. Here are ways to get information about changes in this course.
* Course webpage on Purdue Blackboard
* Instructor’s email
* Instructor’s phone

### Course Outline

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| --- | --- | --- | --- |
| Week | Date | Topic | Chapter |
| 1 | 08/21 – 08/25 | Introduction to semiconductors; Diodes | Ch. 3.1-3.4  |
| 2 | 08/28 – 09/01 | Diodes, diode models and applications | Ch. 3.4-3.5Ch. 4.1-4.3 |
| 3 | 09/04 – 09/08 | **LABOR DAY**; Diodes and applications; BJT | Ch. 4.4-4.6Ch. 6.1 |
| 4 | 09/11 – 09/15 | BJT and BJT circuits | Ch. 6.2 -6.3 |
| 5 | 09/18 – 09/22 | MOSFET and MOSFET circuits | Ch. 5.1-5.2 |
| 6 | 09/25 – 09/29 | **EXAM 1;** MOSFET circuits | Ch. 5.3 |
| 7 | 10/02 – 10/06 | Small Signal Model for BJT | Ch. 7.2-7.3 |
| 8 | 10/09 – 10/13 | **FALL BREAK**; BJT amplifiers | Ch. 7.3 |
| 9 | 10/16 – 10/20 | **EXAM 2;** BJT amplifiers**;**  | Ch. 7.3 |
| 10 | 10/23 – 10/27 | Small Signal Model for MOSFET | Ch. 7.2-7.3 |
| 11 | 10/30 – 11/03 | MOSFET amplifiers | Ch. 7.3 |
| 12 | 11/06 – 11/10 | Differential amplifiers  | Ch. 9.1-9.3 |
| 13 | 11/13 – 11/17 | **EXAM 3;** Current Mirrors & Operational amplifiers  | Ch. 8.2, Ch. 9.6  |
| 14 | 11/20 – 11/24  | Cascode Amplifier; **THANKSGIVING** | Ch. 8.5 |
| 15 | 11/27 – 12/01 | Frequency response and low frequency | Ch. 10.1 |
| 16 | 12/04 – 12/08 | High frequency response | Ch. 10.2-10.6 |