

SENIOR DESIGN INFO SESSION

INTRODUCTION TO ECE 477

George Hadley ©2014, Images Property of their Respective Owners.



COURSE OVERVIEW

- Course Description:

A structured approach to the development and integration of embedded hardware, software, and firmware that provides students with significant design experience applying microcontrollers to a wide range of embedded systems.

- Objective:

To provide practical experience developing integrated hardware and software which students will encounter in industry.

COURSE STAFF

- Phil Walter (philwalter@purdue.edu)
Course Coordination and Development
- Mithuna Thottethodi (mithuna@ecn.purdue.edu)
Fall Lecture Professor
- Joseph Bougher (bougher@purdue.edu)
Digital Systems Laboratory Engineer
- Mark Johnson (mcjohnso@purdue.edu)
Director of Instructional Labs

Additional details can be found under the website [About](#) → [Staff](#) tab

COURSE OVERVIEW

- Lectures:
 - Tuesdays and Thursdays
 - Topics include professional and design components
 - Lecture dates and topics listed on Course Calendar

- Mandatory Lab Hours:
 - Wednesdays (two sections available – all four team members must register for the same section)
 - Used to assess progress, provide feedback, and improve student/staff communication

COURSE OVERVIEW

- **Midterm Design Review and Final Presentation:**
 - Formal presentations given before classmates, course staff, and project sponsors
 - Opportunity to showcase prototyping progress and final deliverables
- **Weekly Progress Updates:**
 - Used to detail individual design activities and progress
 - Evaluated numerous times throughout the semester (one of the five course outcomes)
- **ECE Design Showcase:**
 - Opportunity to showcase completed projects to students, faculty, and project sponsors

DESIGN PROJECT

- Teams:
 - Four students each (no exceptions), self-selected
 - Established prior to submission of project proposal
- Projects:
 - Open-ended, team-specified and of *personal interest* to at least two team members
 - Tractable, yet “difficult enough”
 - Must satisfy five general and five project-specific success criteria
 - Opportunity to develop communication and teamwork skills that will be needed in industry

DESIGN PROJECT

- Basic Project Specifications:
 - Must utilize a **programmable chip** (Arduinos are acceptable for prototyping purposes but are not accepted for final project submissions)
 - May also utilize a CPLD or FPGA
 - May also utilize a “motherboard” (Atom / ARM / R-Pi)
 - Must interface to **multiple systems** (sensor, keypad, LCD, GPS, etc.) using **multiple standard interfaces** (USB, Ethernet, I2C, RS232, IR, RF, etc.)
 - Requires the design and fabrication of a **two-layer custom printed circuit board (PCB)**
 - Must be **neatly packaged and integrated**

COURSE SCHEDULE/CALENDAR

- Weeks 1-4: Concept Development:
 - Functional Project Proposal: *“Our idea seems sound... what do we need to get started?”*
 - Functional Analysis: *“How will our project be used? What are our project’s requirements?”*
 - Electrical and Software Overviews: *“At a high level, how will our project function?”*
 - Component Analysis and Bill of Materials: *“What parts does our project need to use?”*
 - Ordering/Acquisition of parts, tools, and prototyping hardware

COURSE SCHEDULE/CALENDAR

- Weeks 5-9: Design:
 - Mechanical Overview: *“What will our project look like? What form factor does it need to fit within?”*
 - Software Formalization: *“What software components will our design use? How will we verify and test the software?”*
 - Printed Circuit Board Layout
 - Midterm Design Review
 - PCB Submission and Verification

COURSE SCHEDULE/CALENDAR

- Weeks 10-15: Testing and Integration:
 - Legal Analysis: *“What steps must be taken to ensure our project can be legally sold to our customers?”*
 - Reliability and Safety Analysis: *“What risks are associated with use of our product? What parts are most likely to fail?”*
 - Ethical and Environmental Analysis: *“What resources does our project use? How can we responsibly manage our project’s life cycle? What ethical issues does our project present?”*
 - User Manual: Guide to your project for the end user
- Week 16: Success Criteria Demos, Final Presentations, and Final Documentation

COURSE WEBSITE

<https://engineering.purdue.edu/ece477>

- About – general course overview, staff information, history
- Course – assignments, lectures, documents, policies, processes
- Teams – information about current teams and links to their project websites
- Archive – information about past teams and links to websites
- Sponsors – information for corporate sponsors
- Incoming – registration information for prospective students
- Contact – course account email link for communications

Sample Past Projects

ECE 477 Digital Systems Senior Design Project – Spring 2007

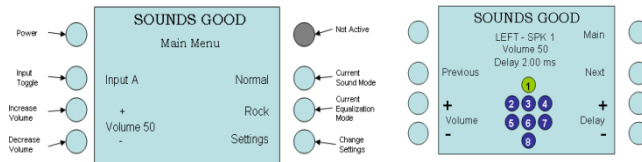
SOUNDS GOOD / DS³ Digital Steerable Sound System

Joe Land, Ben Fogle, James O'Carroll, Elizabeth Strehlow

PROJECT DESCRIPTION:

- Digitally Steerable Sound System, allows for non-ideal placement of speakers
- Six Preset Equalization Modes
- Wireless Control Interface

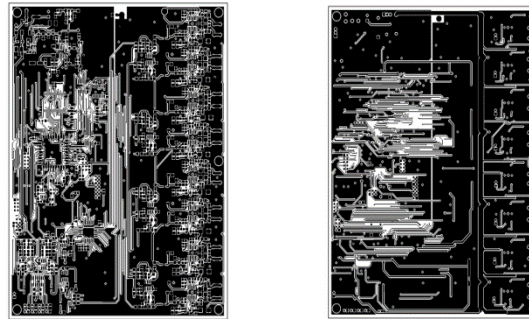
USER MENUS CONCEPT:



USER INTERFACE UNIT:



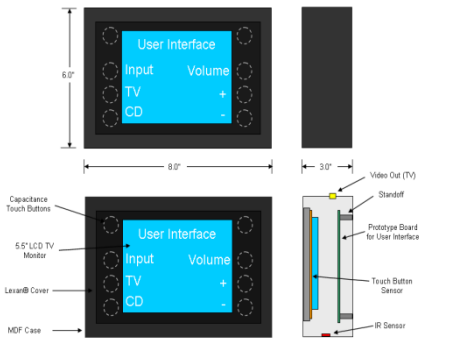
PCB LAYOUT:



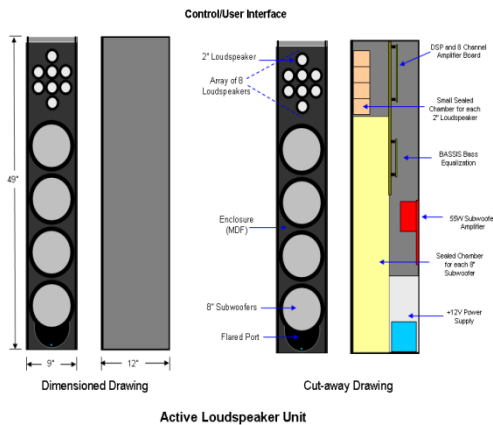
Top Copper

Bottom Copper

ILLUSTRATION OF CONCEPT:

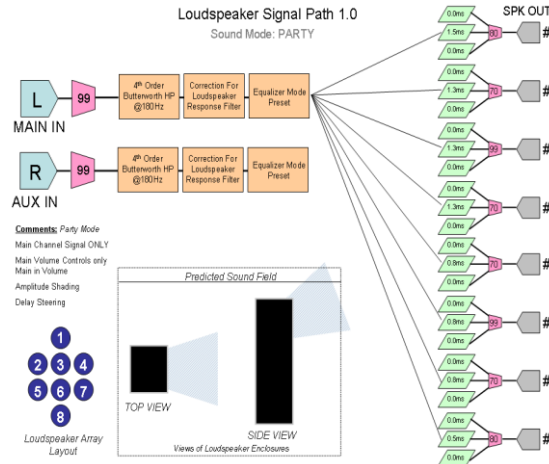


LOUDSPEAKER UNIT:



Active Loudspeaker Unit

SIGNAL PATH:



FRONT



BACK



Hooked on Harmonix is a learning tool that teaches the user valuable piano skills while providing an entertaining experience at the same time.



Final Printed Circuit Board (PCB)

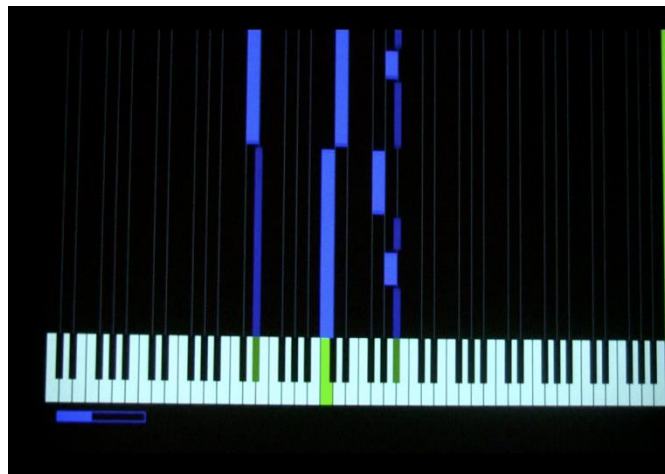


I/O Ports (from left): Audio Output, VGA output, MIDI input, power input



4-layer Printed Circuit Board (PCB)

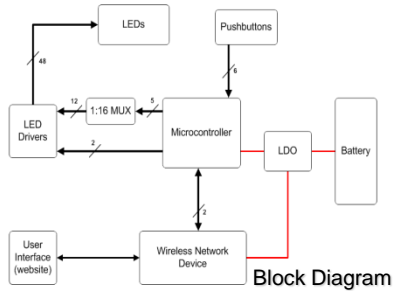
Synopsis: The user selects one of several MIDI tracks stored in Flash memory, and the corresponding file is parsed outputting bars of appropriate length to a standard computer monitor. Performance from a standard MIDI keyboard is judged and graded when the song is finished.



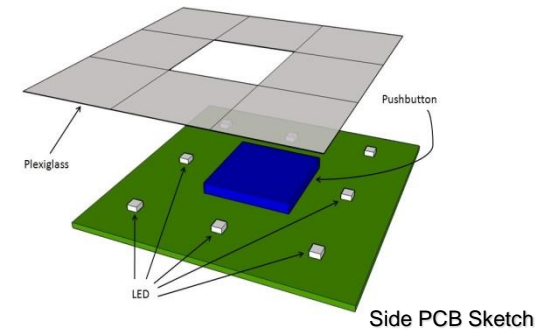
User input from standard MIDI keyboard is compared to stored MIDI song file



Design

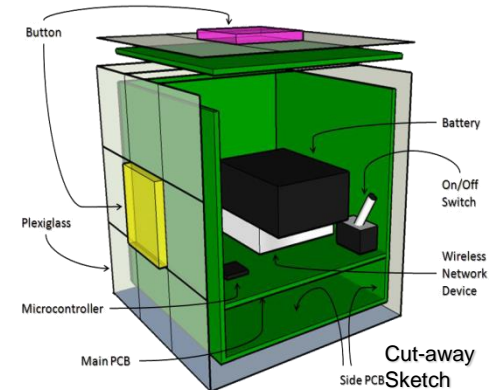
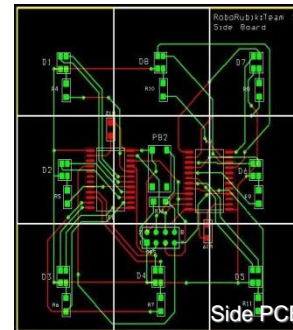
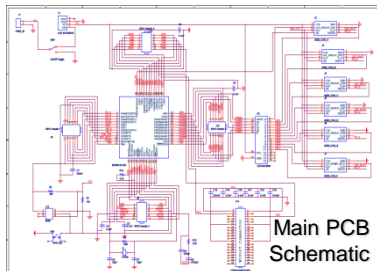
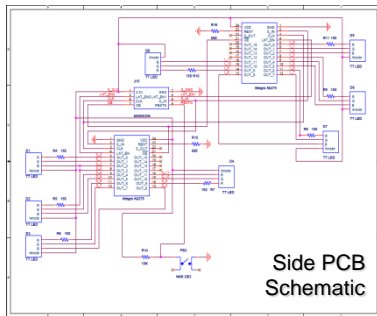


Device



RoboRubik is a self-contained automated Rubik's Cube solver. You can scramble and solve it just like a normal Rubik's Cube. If you get stuck, you can get hints as to what your next move should be. RoboRubik comes with an embedded user interface accessed through any device with wireless networking ability. It's a fun and simple way to learn about and play with one of the most beloved puzzles in the world.

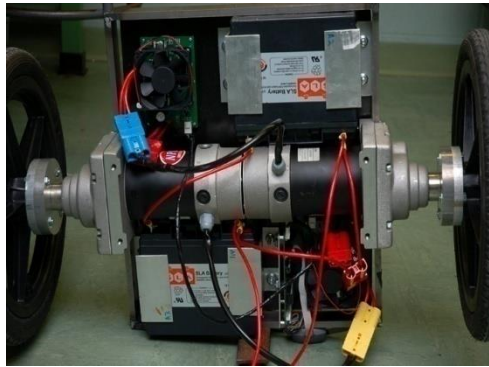
Development



ECE 477 Digital Systems Senior Design Project – Spring 2008

The Two Wheel Deal

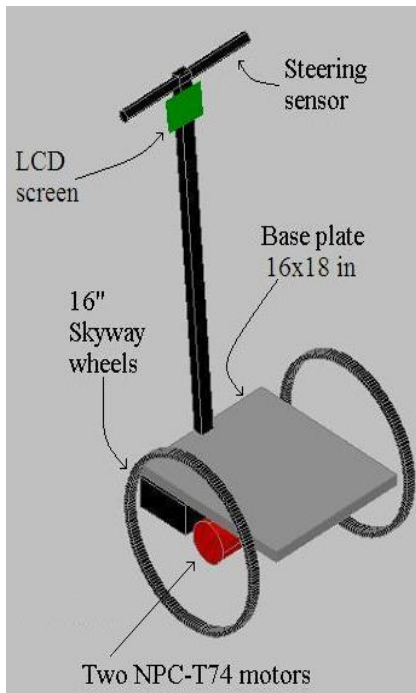
Packaging Layout



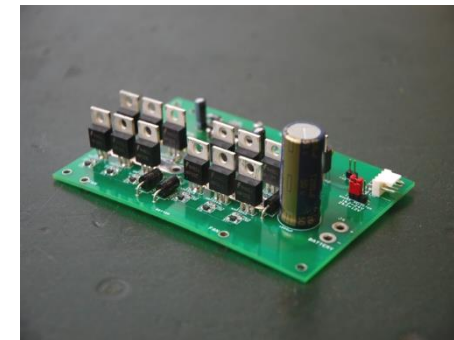
Wheel Hubs



Preliminary Chassis Design

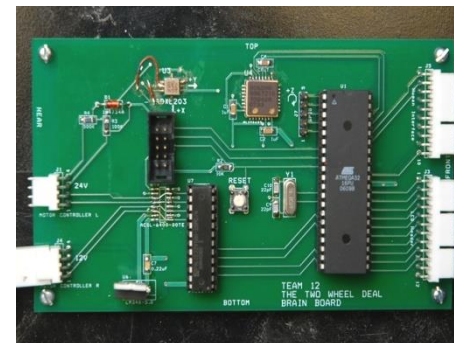


Motor Controller PCB



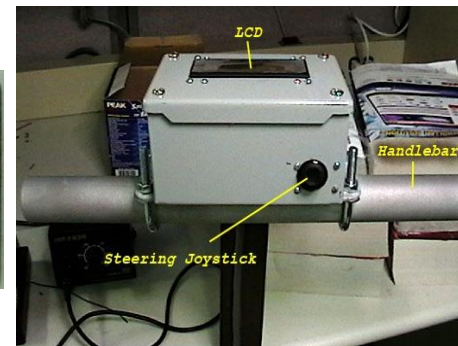
The Two Wheel Deal is a vehicle used for transporting a single rider on two wheels. The design uses an accelerometer and gyroscope to sense when the center of gravity is not directly over the axis of the wheels. It then drives the wheels in order to keep the vehicle balanced. The LCD screen displays battery life, speed, and tilt angle.

Microcontroller PCB



User Interface

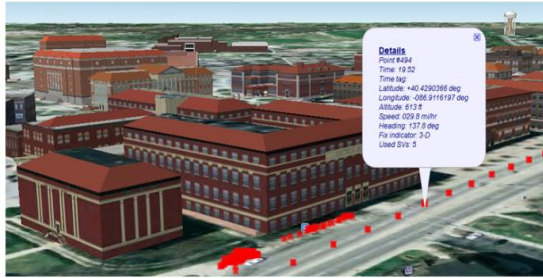
LCD Display



ECE 477 Digital Systems Senior Design Project – Fall 2011

The Incredible HUD – A helmet-based heads up display

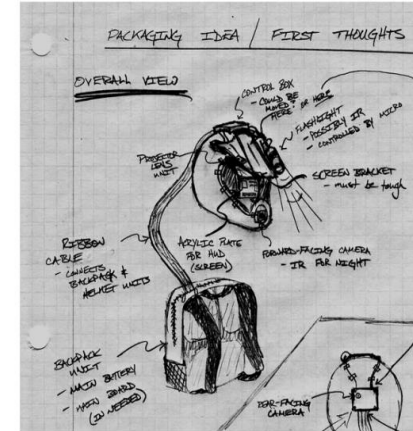
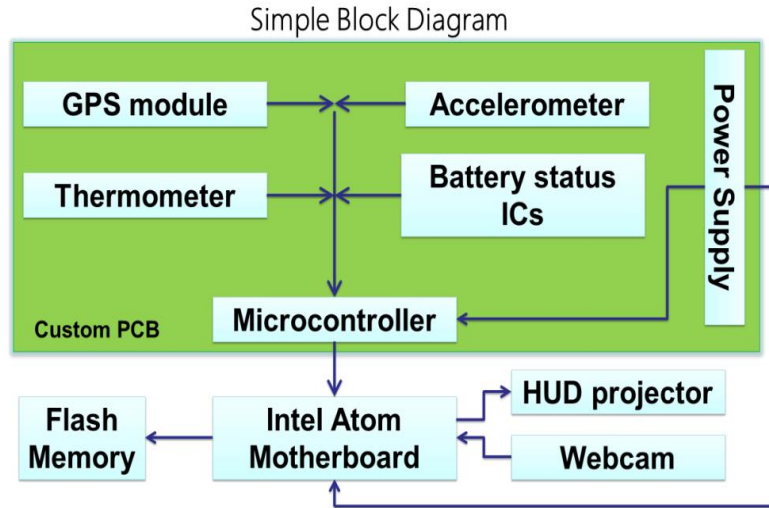
The Incredible HUD is a helmet-based augmented reality system designed for use in extreme sports such as motorcycling, skiing, or skydiving. The system consists of a helmet and a thin backpack unit for information processing. The system projects telemetry data such as GPS, acceleration, velocity, altitude and battery status, as well logging all the data generated. The logged data can be easily viewed using Google Earth.



Google Earth Data Log



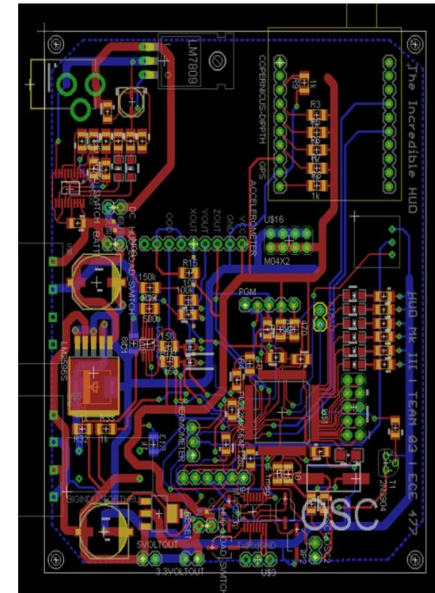
Helmet and Display Modes



Initial Concept



From left to right: Nikhil Sureshkumar, Marcelo Leone, Brandon Blaine Gardner, Aditya Balasubramanian



Final PCB Design

Special Thanks To:
 George Hadley and George Toh
 Professors Meyer and Dr. Johnson
 Chuck "The Wizard" Barnett
 Brian Bowman (Resident C# Guru)

THE HACKERS OF CATRON

The **Hackers of Catron** is an electronic board game based around the incredibly popular game *The Settlers of Catan*. The object of the game is to create the largest settlement on the island of Catron, by obtaining and trading various resources.

Features

- Generates a random arrangement of resource hexagons
- Automatically assigns resources to players after rolling the dice via the web app
- Resources are viewed, traded, and purchased in the web app
- Playable on any device with Wi-Fi and a modern browser
- No external wireless network necessary
- Enforces correct piece placement with visual feedback



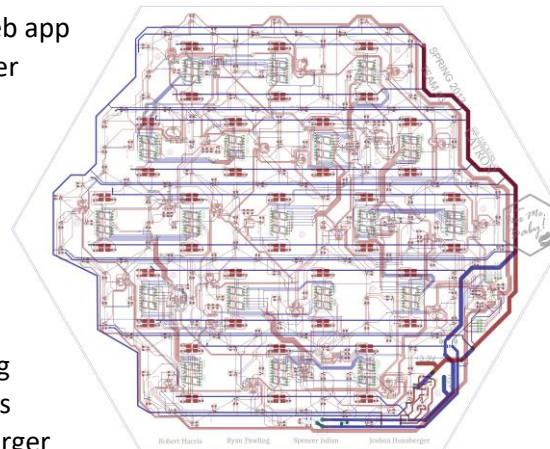
Ryan Pawling
Robert Harris
Josh Hunsberger
Spencer Julian



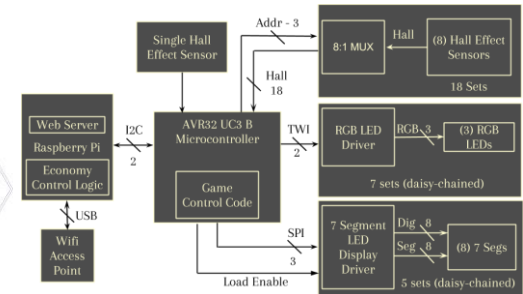
A Game in Progress



Web Application



PCB Layout



Block Diagram

REGISTRATION PROCEDURES

- Form a team of **four students** and designate a **team leader** (if you do not have a team, email ece477@ecn.purdue.edu to request inclusion on the posted “**free agent**” list)
- Visit [this form](#) to request a **preliminary team ID (PTID)** – should only be done by **team leader**
- Visit <https://engineering.purdue.edu/ece477>, click on the **Incoming** tab, then follow the **Registration** link; download the **Initial Project Proposal** skeleton file
- After completing your project proposal, return to the ECE 477 **Incoming** → **Registration** page and upload your proposal file (.docx only) via the **Submission** portal using your **assigned PTID** (done by **team leader** only)

REGISTRATION PROCEDURES

- Course staff will evaluate proposals in the order received; once reviewed, a “marked up” copy of the proposal will be emailed to the **team leader**
- ❖ If your proposal is **accepted**, your team will be assigned an “**official**” **team number** and added to the **Registered Teams** page (team members will receive an **override** to register for ECE 477 at this time)
- ❖ If your proposal is **rejected**, your team will have **one week** to **revise** the proposal and **resubmit** it
- ❖ If an acceptable proposal is not submitted within the allotted time, your priority in the incoming teams queue will be forfeit and other teams may sign up ahead of you