

ECE 477 – *Digital Systems Senior Design Project* – Fall 2018

COURSE CALENDAR

Wk	Monday	Tuesday	Wednesday	Thursday	Friday	Wk	Monday	Tuesday	Wednesday	Thursday	Friday
1	Aug 20	Aug 21 1 - Course Introduction 10:30-11:20 EE117	Aug 22 Mandatory Lab (as per lab section) EE007	Aug 23 2 - Defining Requirements 10:30-11:20 EE117	Aug 24 1 - Final Project Proposal	9	Oct 15 Midterm Peer Evaluation Due	Oct 16 10 - PCB Verification and Ordering EE117	Oct 17 Mandatory Lab (as per lab section) EE007	Oct 18 11-Hardware Debugging 10:30-11:20 EE117	Oct 19 PCB Verification & Submission
2	Aug 27	Aug 28 3 - Hardware Interfacing 10:30-11:20 EE117	Aug 29 Mandatory Lab (as per lab section) EE007	Aug 30 3 - Hardware Interfacing 10:30-11:20 EE117	Aug 31 2 - Functional Specification	10	Oct 22	Oct 23 12 - Legal and Regulatory 10:30-11:20 EE117	Oct 24 Mandatory Lab (as per lab section) EE007	Oct 25 12-Legal and Regulatory 10:30-11:20 EE117	Oct 26 9 – Legal Analysis
3	Sep 3 Labor Day	Sep 4 4 - Discrete Components 10:30-11:20 EE117	Sep 5 Mandatory Lab (as per lab section) EE007	Sep 6 4 - Discrete Components 10:30-11:20 EE117	Sep 7 3 - Software Overview, 4 - Component Analysis	11	Oct 29	Oct 30 13-Reliability and Safety 10:30-11:20 EE117	Oct 31 Mandatory Lab (as per lab section) EE007	Nov 1 13-Reliability and Safety 10:30-11:20 EE117	Nov 2 10 - Reliability and Safety Analysis
4	Sep 10	Sep 11 5 - Power Considerations 10:30-11:20 EE117	Sep 12 Mandatory Lab (as per lab section) EE007	Sep 13 5 - Power Considerations 10:30-11:20 EE117	Sep 14 5 - Bill of Materials, 6 - Electrical Overview	12	Nov 5	Nov 6 14-Ethical Considerations 10:30-11:20 EE117	Nov 7 Mandatory Lab (as per lab section) EE007	Nov 8 15- Environmental 10:30-11:20 EE117	Nov 9 11 – Ethical/ Environmental Impact Analysis
5	Sep 17	Sep 18 6 - Embedded Software 10:30-11:20 EE117	Sep 19 Mandatory Lab (as per lab section) EE007	Sep 20 7-Hardware Design Fund. 10:30-11:20 EE117	Sep 21 7 – Mechanical Overview	13	Nov 12	Nov 13 Case Studies 10:30-11:20 EE117	Nov 14 Mandatory Lab (as per lab section) EE007	Nov 15 Case Studies 10:30-11:20 EE117	Nov 16 12 - User Manual
6	Sep 24	Sep 25 8 - Hardware Design Techniques 10:30-11:20 EE117	Sep 26 Mandatory Lab (as per lab section) EE007	Sep 27 9 - Assembly and Soldering 10:30-11:20 EE117	Sep 28 Preliminary Schematic/ PCB footprint library development	14	Nov 19	Nov 20 Case Studies 10:30-11:20 EE117	Nov 21 Thanksgiving Break	Nov 22 Thanksgiving Break	Nov 23 Thanksgiving Break
7	Oct 1	Oct 2 9 - Assembly and Soldering 10:30-11:20 EE117	Oct 3 Mandatory Lab (as per lab section) EE007	Oct 4 Des. Rev. guidelines 10:30-11:20 EE117	Oct 5 Preliminary PCB Layout, 8 – Software Formalization	15	Nov 26	Nov 27 Case Studies 10:30-11:20 EE117	Nov 28 Mandatory Lab (as per lab section) EE007	Nov 29 16 - Next Steps 10:30-11:20 EE117	Nov 29 13 - ECE 477 Educational Report
8	Oct 8 October Break	Oct 9 October Break	Oct 10 Formal Design Reviews Location TBA	Oct 11 Formal Design Reviews Location TBA	Oct 12 Formal Design Reviews Location TBA	16	Dec 3	Dec 4	Dec 5	Dec 6	Dec 7 <i>Spark Challenge</i> Date/Time TBA

Formal Design Reviews for each team will be scheduled on 10/10 - 10/12

Final Presentation session will be on Dec 12. 9:30–1:30 PM (location TBA)

Final Confidential Peer Review, Final Project Archive, and Educational Report submitted on-line by 5:00 PM on Monday, Dec. 10th, 2018