ECE 477 – Digital Systems Senior Design Project – Fall 2018 COURSE CALENDAR

Wk	Monday	Tuesday	Wednesday	Thursday	Friday	Wk	Monday	Tuesday	Wednesday	Thursday	Friday
	Aug 20	Aug 21	Aug 22	Aug 23	Aug 24		Oct 15	Oct 16	Oct 17	Oct 18	Oct 19
	_	1 - Course	Mandatory	2 - Defining	_		Midterm	10 - PCB	Mandatory	11-Hardware	
1		Introduction	Lab (as per	Requirements	1 - Final	9	Peer	Verification	Lab (as per	Debugging	PCB Verification
		10:30-11:20	lab section)	10:30-11:20	Project		Evaluation	and Ordering	lab section)	10:30-11:20	& Submission
		EE117	EE007	EE117	Proposal		Due		EE007	EE117	
	Aug 27	Aug 28	Aug 29	Aug 30	Aug 31		Oct 22	Oct 23	Oct 24	Oct 25	Oct 26
		3 - Hardware	Mandatory	3 - Hardware				12 - Legal and	Mandatory	12-Legal and	9 – Legal
2		Interfacing	Lab (as per	Interfacing	2 - Functional	10		Regulatory	Lab (as per	Regulatory	Analysis
		10:30-11:20	lab section)	10:30-11:20	Specification			10:30-11:20	lab section)	10:30-11:20	
		EE117	EE007	EE117				EE117	EE007	EE117	
	Sep 3	Sep 4	Sep 5	Sep 6	Sep 7		Oct 29	Oct 30	Oct 31	Nov 1	Nov 2
		4 - Discrete	Mandatory	4 - Discrete	3 - Software			13-Reliability	Mandatory	13-Reliability	10 - Reliability
3	Labor Day	Components	Lab (as per	Components	Overview,	11		and Safety	Lab (as per	and Safety	and Safety
		10:30-11:20	lab section)	10:30-11:20	4 - Component			10:30-11:20	lab section)	10:30-11:20	Analysis
		EE117	EE007	EE117	Analysis			EE117	EE007	EE117	
	Sep 10	Sep 11	Sep 12	Sep 13	Sep 14		Nov 5	Nov 6	Nov 7	Nov 8	Nov 9
		5 - Power	Mandatory	5 - Power	5 - Bill of			14-Ethical	Mandatory	15-	11 – Ethical/
4		Consideration	Lab (as per	Considerations	Materials,	12		Considerations	Lab (as per	Environmental	Environmental
		S	lab section)	10:30-11:20	6 - Electrical	12		10:30-11:20	lab section)	10:30-11:20	Impact Analysis
		10:30-11:20	EE007	EE117	Overview			EE117	EE007	EE117	
		EE117			-						
	Sep 17	Sep 18	Sep 19	Sep 20	Sep 21		Nov 12	Nov 13	Nov 14	Nov 15	Nov 16
		6 - Embedded	Mandatory	7-Hardware	- M			0 0 1		0 0 "	40. 11
5		Software	Lab (as per	Design Fund.	7 – Mechanical	13		Case Studies	Mandatory	Case Studies	12 - User
		10:30-11:20	lab section)	10:30-11:20 EE117	Overview			10:30-11:20 EE117	Lab (as per	10:30-11:20 EE117	Manual
		EE117	EE007	EE117				EE117	lab section) EE007	EE117	
	Sep 24	Sep 25	Sep 26	Sep 27	Sep 28		Nov 19	Nov 20	Nov 21	Nov 22	Nov 23
	Sep 24	8 - Hardware	Mandatory	9 - Assembly and	Preliminary		1100 19	Case Studies	NOV Z I	NOV ZZ	NUV 23
		Design	Lab (as per	Soldering	Schematic/			10:30-11:20	Thanksgiving	Thanksgiving	Thanksgiving
6		Techniques	lab section)	10:30-11:20	PCB footprint	14		EE117	Break	Break	Break
		10:30-11:20	EE007	EE117	library			LL117	Dieak	Dieak	Dieak
		EE117	LLOO	LLIII	development						
	Oct 1	Oct 2	Oct 3	Oct 4	Oct 5		Nov 26	Nov 27	Nov 28	Nov 29	Nov 29
7	000.	9 - Assembly	Mandatory	Des. Rev.	Preliminary		1101 20	Case Studies	Mandatory	16 - Next	13 - ECE 477
		and Soldering	Lab (as per	guidelines	PCB Layout,	15		10:30-11:20	Lab (as per	Steps	Educational
•		10:30-11:20	lab section)	10:30-11:20	8 – Software	. 0		EE117	lab section)	10:30-11:20	Report
		EE117	EE007	EE117	Formalization				EE007	EE117	110,011
	Oct 8	Oct 9	Oct 10	Oct 11	Oct 12	j	Dec 3	Dec 4	Dec 5	Dec 6	Dec 7
			Formal	Formal	Formal						Spark
8	October	October	Design	Design	Design	16					Challenge
-	Break	Break	Reviews	Reviews	Reviews						Date/Time
			Location TBA	Location TBA	Location TBA						TBA
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Formal Design Reviews for each team will be scheduled on 10/10 - 10/12

Final Presentation session will be on Dec 12. 9:30–1:30 PM (location TBA)

Final Confidential Peer Review, Final Project Archive, and Educational Report submitted on-line by 5:00 PM on Monday, Dec. 10th, 2018