

To Do List: Week 6

1. Midterm Design Reviews

1. Design reviews are coming up in just a few short weeks; students are to provide their availability for midterm design reviews; an online form has been set up to collect student responses. Students should indicate their availability for midterm design reviews here: https://purdue.ca1.qualtrics.com/jfe/form/SV_0GoRaEpyVynWex
2. Students will need to be available for 3 design review slots (slot assignment will be performed based on availability):
 1. 1 presentation slot, in which they present their midterm design review with the rest of their team
 2. 2 review slots, in which they act as a peer reviewer for the design review of another team

2. Components

1. Components should be ordered by this time. If not, have one student from your team place a components order. ECE477 provides a \$425 reimbursement for parts ordered by the course which is described in detail here: <https://engineering.purdue.edu/ece477/Course/Process/ReimbursementProcess.pdf>
2. For each major part used by your team (excluding passive components), obtain a copy of that part's datasheet and host it on your project website.
3. For each part used by your team (including passive components), identify an acceptable component within Eagle CAD for use in an electrical schematic. Any parts which cannot be found within Eagle will have to be created. Develop a parts library and create all necessary Eagle footprints for your parts.

3. PCB Footprint Library and Electrical Schematics

1. By Mandatory Lab of this week, students are expected to have a completed Eagle parts library and a modestly-developed electrical schematic for their team's project.
2. Upcoming deadlines for the PCB Design and Layout (Post all completed items to student project websites):
 - **Week 6:** Eagle Tutorial Completed, Eagle Parts Library Completed, Electrical Schematic preliminary but substantial effort has been made
 - **Week 7:** Electrical schematic completed, PCB Layout preliminary but substantial effort has been made
 - **Week 8:** Midterm Design Reviews. PCB Layout completed.
 - **Week 9:** Final tweaks to Parts Library, Electrical Schematic, and PCB Layout made. PCBs submitted at end of week.

4. Software Prototyping

For individuals not directly involved with the hardware design process, prototyping progress is expected of students each week. Students should be able to demonstrate a prototyped system and describe progress made on the system to course staff.

5. Progress Reports

Progress Reports are due Fridays at midnight (same time as homework assignments). Progress reports should follow the course calendar (i.e. this week's entry should be listed as week 6). For any questions regarding the content to be included within a progress report, students may consult the Progress Report Policy, available here:

<https://engineering.purdue.edu/ece477/Course/Policies/ProgressReportPolicy.pdf>