To Do List: Week 8

1. **Midterm Design Reviews**

2. **Electrical Schematics and PCB Layout**
   1. By Mandatory Lab of this week, students are expected to have a completed their Eagle electrical schematics and a mostly-complete PCB layout for all of their team's circuit boards.
   2. Upcoming deadlines for the PCB Design and Layout (Post all completed items to student project websites):
      - **Week 7:** Electrical schematic completed, PCB Layout preliminary but substantial effort has been made
      - **Week 8:** Midterm Design Reviews. PCB Layout completed.
      - **Week 9:** Final tweaks to Parts Library, Electrical Schematic, and PCB Layout made. PCBs submitted at end of week.

3. **Software Prototyping**
   For individuals not directly involved with the hardware design process, prototyping progress is expected of students each week. Students should be able to demonstrate a prototyped system and describe progress made on the system to course staff.

4. **Progress Reports**
   Progress Reports are due Fridays at midnight (same time as homework assignments). Progress reports should follow the course calendar. For any questions regarding the content to be included within a progress report, students may consult the Progress Report Policy, available here: https://engineering.purdue.edu/ece477/Course/Policies/ProgressReportPolicy.pdf