To Do List: Week 15

1. **Assignment 13: ECE477 Educational Report**
   With the exception of the Final Project Archive, this is the final assignment of the semester. Per ABET accreditation requirements, students are to fill out a brief 2-3 page report on their ECE477 project and semester. This will be read by various faculty members of the greater ECE477 community, so please be sure to write professionally and follow exactly the formatting of the source document. The source document is available here: https://engineering.purdue.edu/ece477/Course/Assignments/Template/13_ECE477EducationalReport.docx

2. **Course Lectures**
   The final lecture of the semester will take place on the Friday prior to dead week. In addition to this, "Case Studies" lectures may be given, at the discretion of the ECE477 course staff. Some lecture sessions may be canceled; please see your ECE477 course staff for additional details.

3. **PSSC Check-offs**
   As hardware integration on student projects progresses and various pieces of functionality are completed, students will have the opportunity to demonstrate preliminary and final PSSCs to course staff. For additional information regarding preliminary and final PSSCs, please see the course PSSC policy, available here: https://engineering.purdue.edu/ece477/Course/Policies/PsscPolicy.pdf
   Students are recommended to print off a copy of the PSSC checkoff form, available here: https://engineering.purdue.edu/ece477/Course/Process/PSSCCheckoffForm.docx
   At least 3 preliminary PSSCs (2 PCB-related) must be demonstrated by the end of the semester to satisfy ABET course requirements. Early completion of PSSCs provides students with opportunities for bonus credit.

4. **Reimbursements**
   For those teams which haven't already done so, ECE477 offers a one-time reimbursement to help cover the costs of parts and materials accrued by team members over the semester. Please see our reimbursement process, https://engineering.purdue.edu/ece477/Course/Process/OrderingProcess.pdf, for further details.

5. **Hardware Integration**
   Hardware integration progress is expected at this point in the semester. Students should be able to integrate subsystems into their final PCB, and describe progress made on the system to course staff.

6. **Progress Reports**
   Progress Reports are due Fridays at midnight (same time as homework assignments). Progress report weeks should follow the course calendar. For any questions regarding the content to be included within a progress report, students may consult the Progress Report Policy, available here: https://engineering.purdue.edu/ece477/Course/Policies/ProgressReportPolicy.pdf