ECE 477 Digital Systems
Senior Design Project

Guideposts
Week 7

Last Week (Week 6)

- Homework 5: Theory of Operation and Hardware Design Narrative
  - Theory of Operation: Discussion of "how it works" (You've determined what you want to do and what you want to use, now tell us how you're going to do it.)
  - Hardware Design Narrative: Microcontroller-specific implementation details
  - Updated system block diagram
  - Updated electrical schematic
- Created PCB component library
- Initial Lab Notebook evaluations graded

This Week (Week 7)

- Homework 6: Printed Circuit Board Layout Narrative
  - Due Friday at 12 am (midnight)
  - Discussion of substantial PCB layout issues
  - Preliminary PCB layout due
  - Include the assignment AND relevant Eagle files in a .zip archive for homework submission
- Sign up for Midterm Design Reviews

This Week (Week 7)

- Mandatory Lab Session 5:
  - Wednesday at assigned TCSP session time in EE061/EE063 (8am and 12pm)
  - Discussion of overall design, circuitry issues, etc.
  - Showcase your printed circuit board (some progress on schematic is expected)
  - All students are expected to be on time and remain in lab for the entirety of their mandatory lab session
  - Students who are late will be marked as late

Next Week (Week 8)

- No lecture, no mandatory lab
- Midterm Design Reviews
  - Sign up using sheets provided by Professor Meyer
  - All students must present with team and attend 2 additional sessions to review other teams
  - Template file and presentation guidelines available on the Assignments page of the course website
  - Dress code is business semi-formal (what you would wear to an interview)
  - Preparing for the Design Review is strongly recommended

Questions?