

# ECE 477 Digital Systems Senior Design Project

Guideposts Week 6

#### Last Week (Week 5)

- Homework 4: Packaging Specs and Design
  - Analyzed packaging design of competing ideas
  - Determined project packaging requirements
  - Created CAD model of packaging design
- Design prototyping
- Ordered parts

#### This Week (Week 6)

- Homework 5: Theory of Operation and Hardware Design Narrative
  - Due Friday at 12am (midnight)
  - Theory of Operation: Discussion of "how it works" (You've determined what you want to do and what you want to use, now tell us how you're going to do it.)
  - Hardware Design Narrative: Microcontroller-specific implementation details
  - Updated system block diagram
  - Updated electrical schematic
- Create your PCB component library

## This Week (Week 6)

- Mandatory Lab Session 4:
  - Wednesday at assigned TCSP session time in EE061/EE063 (8am and 12pm)
  - Discussion of overall design, circuitry issues, etc.
  - Showcase your electrical schematic (some progress on schematic is expected)
  - All students are expected to be on time and remain in lab for the entirety of their mandatory lab session
  - Students who are late will be marked as late

## Next Week (Week 7)

- Homework 6: Printed Circuit Board Layout Narrative
  - Discussion of substantial PCB layout issues
  - Preliminary PCB layout due
- Begin preparing for Midterm Design Reviews

# Questions?