

It is possible to use "Converting Programming file" tool in QuartusII to generate a POF file for EPCS device which includes both of hardware configuration file and software image .

Please see steps below as your reference:

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Open "Converting Programming file" tool from QuartusII:

1. Choose right EPCS device;
2. Highlight on SOF Data(page\_0), Click "Properties", change the address mode to "start"; The start address remains 0x00000000; Then click "Add File", choose the right SOF file; Then highlight on this SOF file, click "Properties", select "Compression";
3. Click "Add Hex Data", choose the right HEX file, select "Absolute Address Mode";
4. Click "Generate", generate the output\_file.pof.

The above HEX file is obtained by the following steps:

1. Using flash programmer command to generate .flash file first, for example:  

```
sof2flash --epcs --input=whbiic.sof -- output=whbiic.flash
```

```
elf2flash --epcs --after=whbiic.flash -- input=hello_led_6.elf --output=epcs_controller.flash
```
2. Using the attached flash-to-absolute-hex tool to generate the absolute address HEX file, for example:  

```
./flash-to-absolute-hex epcs_controller 0x002dd84
```

Here, the absolute address 0x002dd84 is obtained by the following steps:

Open "Converting Programming file" tool from QuartusII:

1. Choose right EPCS device;
2. Highlight on SOF Data(page\_0), Click "Properties", change the address mode to "start"; The start address remains 0x00000000; Then click "Add File", choose the right SOF file; Then highlight on this SOF file, click "Properties", select "Compression";
3. Click "Generate", generate the output\_file.pof.

In default, there is an output\_file.map file generated. You can open this file and find the hardware configuration file occupies the address from the beginning of this page to 0x0002dd83.