

# Microcontrollers

Quarter 1, 2006  
SG1006Q12006 Rev 0

# About This Revision—Q1/2006

When new products are introduced, a summary of new products will be provided in this section. However, the New Product section will only appear on this page when new products have been introduced during the quarter.

In addition, a change bar appears in the left margin of every page in this selector guide that contains new or revised information.

If products are discontinued, a What's EOL? page is included at the end of this guide. The What's EOL? page lists end-of-life products along with their respective last order date, last ship date, and suggested possible replacement information.

## **NEW PRODUCT**

| Related Product | Page Number | Table Title           | Description   |
|-----------------|-------------|-----------------------|---|
| MC908QT1A       | SG1006-6    | HC08 Product Table    | Next generation of the popular low-end, low-pin count Q Family of HC08s.  |
| MC908QT2A       | SG1006-6    | HC08 Product Table    | Next generation of the popular low-end, low-pin count Q Family of HC08s.  |
| MC908QT4A       | SG1006-6    | HC08 Product Table    | Next generation of the popular low-end, low-pin count Q Family of HC08s.  |
| MC908QY1A       | SG1006-6    | HC08 Product Table    | Next generation of the popular low-end, low-pin count Q Family of HC08s.  |
| MC908QY2A       | SG1006-6    | HC08 Product Table    | Next generation of the popular low-end, low-pin count Q Family of HC08s.  |
| MC908QY4A       | SG1006-6    | HC08 Product Table    | Next generation of the popular low-end, low-pin count Q Family of HC08s.  |
| MC9S08GB60A     | SG1006-13   | HCS08 Product Table   | Latest addition to the high performance/high integration HCS08GB60 Family.  |
| MC9S08QG4       | SG1006-13   | HCS08 Product Table   | Latest addition to the popular HCS08 Family that combines an impressive set of integrated features along with low power and low-pin count packages. |
| MC9S08QG8       | SG1006-13   | HCS08 Product Table   | Latest addition to the popular HCS08 Family that combines an impressive set of integrated features along with low power and low-pin count packages. |
| MCF5327         | SG1006-27   | MCF5xxx Product Table | V3 ColdFire w/LDC   |
| MCF5328         | SG1006-27   | MCF5xxx Product Table | V3 ColdFire w/LDC   |
| MCF5329         | SG1006-27   | MCF5xxx Product Table | V3 ColdFire w/LDC   |
| MCF5372         | SG1006-27   | MCF5xxx Product Table | V3 ColdFire with Ethernet   |
| MCF5372L        | SG1006-27   | MCF5xxx Product Table | V3 ColdFire with USB host and USB otg   |
| MCF5373         | SG1006-27   | MCF5xxx Product Table | V3 ColdFire with Ethernet   |
| MCF5373L        | SG1006-27   | MCF5xxx Product Table | V3 ColdFire with USB host and USB otg   |

## HC08 FAMILY

### HC08 Product Table

For complete part number information and temperature definitions, refer to “Product Numbering System for 68HC08” on page SG1006-7.

| Product  | Flash Program Memory (Bytes) | RAM (Bytes) | 16-bit Timer            | I/O   | Communication                 | ADC          | Operating Voltage (V) | Bus Frequency (MHz) | Temperature Options | Packaging  | Development Tools           | Additional Information  |
|--|------------------------------|-------------|-------------------------|---|-------------------------------|--------------|-----------------------|---------------------|---------------------|--|-----------------------------|---|
| <b>AB Family—Embedded E<sup>2</sup></b>  |                              |             |                         |   |                               |              |                       |                     |                     |  |                             |   |
| MC68HC908AB32  | 32K                          | 1K          | Dual 4-CH, IC/OC or PWM | 51  | SCI, SPI                      | 8-CH, 10-bit | 5.0                   | 8.0                 | C, V, M             | 64-pin QFP (FU)                                    | FSICEKITAB32                | Programmable interrupt timer module   |
| <b>AS/AZ Family—Automotive/Industrial Communication Protocols J1850/CAN</b>                  |                              |             |                         |   |                               |              |                       |                     |                     |  |                             |   |
| MC908AZ32A   | 32K                          | 512         | 6-CH, IC/OC or PWM      | 50  | SCI, SPI                      | 15-CH, 8-bit | 5.0                   | 8.4                 | C, V, M             | 64-pin QFP (FU)                                    | FSICEKITASAZ                | www.freescale.com   |
| MC908AS32A   |                              |             |                         | 40  |                               |              |                       |                     |                     | 64-pin QFP (FU), 52-pin PLCC (FN)                  |                             |   |
| MC908AZ60A   | 60K                          | 1K          |                         | 52  |                               |              |                       |                     |                     | 64-pin QFP (FU)                                    |                             |   |
| MC908AS60A   |                              |             |                         | Up to 52  |                               |              |                       |                     |                     |  |                             |   |
| <b>AP Family—General Purpose 10-bit A/D, I<sup>2</sup>C, Pin Compatible from 8-60K Flash</b> |                              |             |                         |   |                               |              |                       |                     |                     |  |                             |   |
| MC68HC908AP8   | 8K                           | 1K          | Dual 2-CH, IC/OC or PWM | Up to 32  | 2 SCI, 1SPI, I <sup>2</sup> C | 8-CH, 10-bit | 3.0, 5.0              | 8.0                 | C, V, M             | 48-pin LQFP (FA), 44-pin QFP (FB), 42-pin SDIP (B) | DEMO908AP64<br>FSICEKITAP64 | 32 kHz PLL, RC oscillator, timebase module, low-voltage inhibit, up to 8 keyboard interrupts, 6 open-drain pins with 25 mA sink |
| MC68HC908AP16  | 16K                          |             |                         |   |                               |              |                       |                     |                     |  |                             |   |
| MC68HC908AP32  | 32K                          | 2K          |                         |   |                               |              |                       |                     |                     |  |                             |   |
| MC68HC908AP64  | 64K                          |             |                         |   |                               |              |                       |                     |                     |  |                             |   |
| <b>BD Family—Digital Monitors</b>  |                              |             |                         |   |                               |              |                       |                     |                     |  |                             |   |
| MC68HC908BD48  | 48K                          | 1K          | 2-CH, IC/OC or PWM      | 32  | I <sup>2</sup> C, DDC12AB     | 6-CH, 8-bit  | 5.0                   | 6.0                 | I                   | 44-pin QFP (FB), 42-pin SDIP (B)                   | M68EM08BD48                 | www.freescale.com   |
| <b>EY Family—Automotive/Industrial LIN</b>   |                              |             |                         |   |                               |              |                       |                     |                     |  |                             |   |
| MC68HC908EY8   | 8K                           | 384         | Dual 2-CH, IC/OC or PWM | 24  | ESCI, SPI                     | 8-CH, 10-bit | 3.0, 5.0              | 8.0                 | C, V, M             | 32-pin LQFP (FA)                                   | FSICEKITEY                  | ESCI is LIN ready   |
| MC68HC908EY16  | 16K                          | 512         |                         |   |                               |              |                       |                     |                     |  |                             |   |
| <b>GB/GT/GP Family—General Purpose</b>   |                              |             |                         |   |                               |              |                       |                     |                     |  |                             |   |
| MC68HC908GT8   | 8K                           | 512         | Dual 2-CH, IC/OC or PWM | 36  | SCI, SPI                      | 8-CH, 8-bit  | 3.0, 5.0              | 8.0                 | C                   | 44-pin QFP (FB), 42-pin DIP (B)                    | FSICEKITGPGT                | Internal clock generator; low-voltage inhibit   |
| MC68HC908GT16  | 16K                          |             |                         | 44-pin QFP (FB), 42-pin DIP (B), 40-pin DIP (P) |                               |              |                       |                     |                     | M68EV8908GP32<br>FSICEKITGPGT                      | www.freescale.com           |   |
| MC68HC908GP32  | 32K                          |             |                         |   |                               |              |                       |                     |                     |  |                             | Up to 33  |

## HC08 FAMILY

### HC08 FAMILY (continued)

### HC08 Product Table (continued)

For complete part number information and temperature definitions, refer to "Product Numbering System for 68HC08" on page SG1006-7.

| Product   | Flash Program Memory (Bytes) | RAM (Bytes) | 16-bit Timer               | I/O            | Communication    | ADC           | Operating Voltage (V) | Bus Frequency (MHz) | Temperature Options  | Packaging  | Development Tools   | Additional Information  |
|---|------------------------------|-------------|----------------------------|----------------|------------------|---------------|-----------------------|---------------------|--|--|---|---|
| <b>GR/GZ Family—General Purpose/Low Cost CAN</b>                      |                              |             |                            |                |                  |               |                       |                     |  |  |   |   |
| MC68HC908GR4  | 4K                           | 384         | 2-CH + 1-CH, IC/OC, or PWM | 21             | SCI, SPI         | 6-CH, 8-bit   | 3.0, 5.0              | 8.0                 | C  | 32-pin LQFP (FA)<br>28-pin SOIC (DW)<br>28-pin DIP (P) | FSICEKITGR8   | 32 kHz timebase module; two extra ADC channels on LQFP32                        |
| MC68HC908GR8  | 7.5K                         |             |                            |                |                  |               |                       |                     |  |  |   |   |
| MC68HC908GZ8  | 7.5K                         | 512         | Dual 2-CH, IC/OC or PWM    | Up to 37       | ESCI, SPI, CAN   | 8-CH, 10-bit  |                       |                     | C, V, M  | 32-pin LQFP (FJ)<br>48-pin LQFP (FA)                   | DEMO908GZ60<br>FSICEKITGRGZ   | www.freescale.com   |
| MC68HC908GR16   | 16K                          |             |                            |                |                  |               |                       |                     |  |  |   |   |
| MC68HC908GZ16   |                              | 1K          |                            |                |                  |               |                       |                     |  |  |   |   |
| MC68HC908GR16A  |                              |             |                            |                |                  |               |                       |                     |  |  |   |   |
| MC68HC908GR32A  | 32K                          | 1.5K        | 2-CH, 6-bit IC/OC or PWM   | Up to 50       | ESCI, SPI        | 24-CH, 10-bit |                       |                     | 32-pin LQFP (FJ)<br>48-pin LQFP (FA)<br>64-pin QFP (FU)                    | 1-8 MHz high-frequency oscillator                      |   |   |
| MC68HC908GZ32   |                              |             |                            |                |                  |               |                       |                     |  |  |   |   |
| MC68HC908GR48A  | 48K                          | 2K          | ESCI, SPI, CAN             | ESCI, SPI, CAN | ESCI, SPI        |               |                       |                     |  |  |   |   |
| MC68HC908GZ48   |                              |             |                            |                |                  |               |                       |                     |  |  |   |   |
| MC68HC908GR60A  | 60K                          | 2K          | ESCI, SPI, CAN             | ESCI, SPI, CAN | ESCI, SPI        |               |                       |                     |  |  |   |   |
| MC68HC908GZ60   |                              |             |                            |                |                  |               |                       |                     |  |  |   |   |
| <b>JB Family—Low Cost USB</b>   |                              |             |                            |                |                  |               |                       |                     |  |  |   |   |
| MC68HC908JB8  | 8K                           | 256         | 2-CH, IC/OC or PWM         | Up to 37       | USB 1.1          | —             | 3.0                   | —                   | 20-pin DIP (P)<br>28-pin SOIC (DW)<br>44-pin QFP (FB)<br>20-pin SOIC (JDW) | FSICEKITJB8  | Low-speed USB 1.1 compliant; on-chip 3.3V regulator                         |   |
| MC68HC908JB12   | 12K                          | 384         | Dual 2-CH, IC/OC or PWM    | Up to 21       | SCI, USB 1.0/1.1 | —             | 4.0-5.5               | —                   | 20-pin SOIC (JDW)<br>28-pin SOIC (DW)                                      | FSICEKITJB12   | Supports USB and PS/2; low-voltage reset, dual 27 MHz PLL; 6 LED drive I/Os |   |
| MC68HC908JB16   | 16K                          |             |                            |                |                  |               |                       |                     |  |  |   |   |
| <b>JK/JL Family—Low Cost, Pin Compatible, and Migratable with A/D</b> |                              |             |                            |                |                  |               |                       |                     |  |  |   |   |
| MC68HC908JK1E   | 1.5K                         | 128         | 2-CH, IC/OC or PWM         | 15             | —                | 12-CH, 8-bit  | 3.0, 5.0              | 8.0                 | C, M   | 20-pin DIP (P)<br>20-pin SOIC (DW)                     | FSICEKITJLK   | RC oscillator option available; LVR with selectable trip point; 6-pin LED drive |
| MC68HC908JK3E   | 4K                           |             |                            |                |                  |               |                       |                     |  |  |   |   |
| MC68HC908JK8  | 8K                           | 256         | Dual 2-CH, IC/OC or PWM    | Up to 23       | SCI              | 13-CH, 8-bit  |                       |                     |  |  |   |   |
| MC68HC908JL3E   | 4K                           | 128         | 2-CH, IC/OC or PWM         |                |                  | 12-CH, 8-bit  |                       |                     |  |  |   |   |
| MC68HC08JL8   | 8K                           | 256         | Dual 2-CH, IC/OC or PWM    | 13-CH, 8-bit   |                  |               |                       |                     |  |  |   |   |

HC08 FAMILY (continued)

HC08 Product Table (continued)

For complete part number information and temperature definitions, refer to "Product Numbering System for 68HC08" on page SG1006-7.

| Product  | Flash Program Memory (Bytes) | RAM (Bytes) | 16-bit Timer            | I/O                          | Communication                      | ADC          | Operating Voltage (V) | Bus Frequency (MHz) | Temperature Options | Packaging   | Development Tools             | Additional Information  |
|--|------------------------------|-------------|-------------------------|------------------------------|------------------------------------|--------------|-----------------------|---------------------|---------------------|---|-------------------------------|---|
| <b>KX Family—Low Pin Count with ICG</b>        |                              |             |                         |                              |                                    |              |                       |                     |                     |   |                               |   |
| MC68HC908KX2                                   | 2K                           | 128         | 2-CH, IC/OC or PWM      | 13                           | SCI                                | 4-CH, 8-bit  | 3.0, 5.0              | 8.0                 | C, V, M             | 16-pin DIP (P)<br>16-pin SOIC (DW)                      | FSICEKITKX                    | On-chip internal clock generator (ICG)  |
| MC68HC908KX8                                   | 8K                           |             |                         |                              |                                    |              |                       |                     |                     |   |                               |   |
| <b>LB Family—Lighting, High Resolution PWM</b> |                              |             |                         |                              |                                    |              |                       |                     |                     |   |                               |   |
| MC68HC908LB8                                   | 8K                           | 128         | 2-CH, IC/OC or PWM      | Up to 18                     | —                                  | 7-CH, 8-bit  | 5.0                   | 8.0                 | C, V, M             | 20-pin DIP (P)<br>20-pin SOIC (DW)                      | DEMO908LB8<br>FSICEKITLB8     | High resolution PWM   |
| <b>LD Family—Digital Monitoring</b>            |                              |             |                         |                              |                                    |              |                       |                     |                     |   |                               |   |
| MC68HC908LD60                                  | 60K                          | 1K          | 2-CH, IC/OC or PWM      | 39                           | i <sup>2</sup> C                   | 6-CH, 8-bit  | 3.3                   | 6.0                 | I                   | 64-pin QFP (FU)   | M68EML08LD64                  | www.freescale.com   |
| MC68HC908LD64                                  |                              | 2K          |                         |                              | USB 1.1, i <sup>2</sup> C          |              |                       |                     |                     |   |                               | Composite hub with embedded functions   |
| <b>LJ/LK Family—Low Cost LCD</b>               |                              |             |                         |                              |                                    |              |                       |                     |                     |   |                               |   |
| MC68HC908LJ12                                  | 12K                          | 512         | Dual 2-CH, IC/OC or PWM | Up to 32                     | SCI, SPI                           | 6-CH, 10-bit | 3.3, 5.0              | 8.0                 | C                   | 52-pin LQFP (FB)<br>64-pin QFP (FU)<br>64-pin LQFP (PB) | FSICEKITLJK                   | LCD driver with 4/3 backplanes and maximum 26 front planes; real-time clock             |
| MC68HC908LJ24                                  | 24K                          |             |                         | Up to 48                     | SCI, SPI, i <sup>2</sup> C         |              |                       |                     |                     | 64-pin QFP (FU)<br>64-pin LQFP (PB)<br>80-pin LQFP (PK) |                               | LCD driver with 4/3 backplanes and maximum 33 front planes; real-time clock; 32 kHz PLL |
| MC68HC908LK24                                  |                              |             |                         | IrSCI, SPI, i <sup>2</sup> C | 64-pin QFP (FU)<br>80-pin QFP (FQ) |              |                       |                     |                     |   |                               |   |
| <b>MR Family—Motor Control</b>                 |                              |             |                         |                              |                                    |              |                       |                     |                     |   |                               |   |
| MC68HC908MR8                                   | 256                          | 8K          | Dual 2-CH, IC/OC or PWM | 14                           | SCI                                | 7-CH, 10-bit | 5.0                   | 8.0                 | C, V, M             | 28-pin PDIP (P)<br>28-pin SOIC (DW)<br>32-pin LQFP (FA) | FSICEKITMR8                   | 6-CH, 12-bit PWM  |
| MC68HC908MR16                                  | 768                          |             |                         | 16K                          | 2-CH + 4-CH, IC/OC or PWM          |              |                       |                     |                     | 44  |                               | SCI, SPI  |
| MC68HC908MR32                                  |                              | 32K         |                         |                              |                                    |              |                       |                     |                     |   |                               |   |
| <b>QF Family—Integrated RF</b>                 |                              |             |                         |                              |                                    |              |                       |                     |                     |   |                               |   |
| MC68HC908QF4                                   | 4K                           | 128         | 2-CH, IC/OC or PWM      | 13                           | —                                  | 4-CH, 8-bit  | 2.2-3.6               | 2.0                 | C, V                | 32-pin LQFP (FJ)  | —                             | Key feature is integration of UHF RF transmitter  |
| <b>QL Family—Low Cost LIN</b>                  |                              |             |                         |                              |                                    |              |                       |                     |                     |   |                               |   |
| MC908QL2                                       | 2K                           | 128         | 2-CH, IC/OC or PWM      | 13                           | SLIC (LIN)                         | 6-CH, 10-bit | 3.0 to 5.0            | 8.0                 | C, V, M             | 16-pin TSSOP (DT)<br>16-pin SOIC (DW)                   | M68EVB908QL4<br>FSICEKITOBLTY | SLIC (Slave-LIN Interface Controller) featuring autobauding/auto synchronization        |
| MC908QL3                                       | 4K                           |             |                         |                              |                                    | —            |                       |                     |                     |   |                               |   |
| MC908QL4                                       |                              |             |                         |                              |                                    | 6-CH, 10-bit |                       |                     |                     |   |                               |   |

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# HC08 FAMILY

## HC08 FAMILY (continued)

### HC08 Product Table (continued)

For complete part number information and temperature definitions, refer to "Product Numbering System for 68HC08" on page SG1006-7.

| Product  | Flash Program Memory (Bytes) | RAM (Bytes) | 16-bit Timer   | I/O         | Communication         | ADC           | Operating Voltage (V) | Bus Frequency (MHz)                                      | Temperature Options         | Packaging  | Development Tools                                  | Additional Information   |   |            |
|--|------------------------------|-------------|--|-------------|-----------------------|---------------|-----------------------|--|-----------------------------|--|--|--|---|------------|
| <b>QB/QC/QT/QY Family—8/16 Pin Low Cost, Small Packages</b>            |                              |             |  |             |                       |               |                       |  |                             |  |  |  |   |            |
| MC908QB4   | 4K                           | 256         | 4-CH, IC/OC or PWM                                   | Up to 13    | ESCI, SPI             | 10-CH, 10-bit | 3.0, 5.0              | 8.0  | M                           | 16-pin TSSOP (DT)<br>16-pin SOIC (DW)<br>16-pin PDIP (P) | DEM0908QB8<br>FSICEKITQBLTY                        | Auto wakeup module, KBI  |   |            |
| MC908QB8   | 8K                           |             |  |             |                       |               |                       |  |                             |  | DEM0908QC16<br>FSICEKITQC16                        |  |   |            |
| MC908QC8   |                              |             |  |             |                       |               |                       |  |                             |  |  |  |   |            |
| MC908QC16  |                              |             |  |             |                       |               |                       |  |                             |  |  |  |   |            |
| MC908QT1A  | 1.5K                         | 128         | 2-CH Input Capture (IC) / Output Compare (OC) or PWM | 6           | —                     | —             | 3.0, 5.0              | 8.0  | C, V, M                     | 8-pin SOIC (DW)<br>8-pin PDIP (P)<br>8-pin DFN (FQ)      | DEM0908QB8<br>M68DEM0908QT4<br>FSICEKITQBLTY       | www.freescale.com  |   |            |
| MC908QT2A  | 2K                           |             |  |             |                       |               |                       |  |                             |  |  |  |   |            |
| MC908QT4A  | 4K                           |             |  |             |                       |               |                       |  |                             |  |  |  |   |            |
| MC908QY1A  | 1.5K                         |             |  |             |                       |               |                       |  |                             |  |  |  |   |            |
| MC908QY2A  | 2K                           |             |  |             |                       |               |                       |  |                             |  |  |  |   |            |
| MC908QY4A  | 4K                           |             |  |             |                       |               |                       |  |                             |  |  |  |   |            |
| MC68HC908QT1   | 1.5K                         |             |  | 6           | —                     | —             | 4-CH, 8-bit           | 3.0, 5.0   | 8.0                         | C  | 8-pin SOIC (DW)<br>8-pin DIP (P)<br>8-pin DFN (FQ) | DEM0908QB8<br>M68DEM0908QT4<br>FSICEKITQBLTY   | Trimmable (±25%) 3.2 MHz internal OSC, external RC, clock, or resonator/XTAL, selectable trip point LVI, auto wake up from stop, KBI, ROM available |            |
| MC68HLC908QT1  |                              |             |  |             |                       |               |                       |  |                             |  |  |  |   | 2.2 to 3.6 |
| MC68HC908QT2   | 2K                           |             |  |             |                       |               |                       |  |                             |  |  |  |   | 2.2 to 3.6 |
| MC68HLC908QT2  |                              |             |  |             |                       |               |                       |  |                             |  |  |  |   |            |
| MC68HC908QT4   | 4K                           |             |  |             |                       |               |                       |  |                             |  |  |  |   | 2.2 to 3.6 |
| MC68HLC908QT4  |                              |             |  |             |                       |               |                       |  |                             |  |  |  |   |            |
| MC68HC908QY1   | 1.5K                         | 14          | —  | 4-CH, 8-bit | 3.0, 5.0              | 8.0           | C                     | 16-pin SOIC (DW)<br>16-pin PDIP (P)<br>16-pin TSSOP (DT) | DEM0908QB8<br>FSICEKITQBLTY | Auto wakeup module, KBI                                  |  |  |   |            |
| MC68HLC908QY1  |                              |             |  |             |                       |               |                       |  |                             |  | 2.2 to 3.6   |  |   |            |
| MC68HC908QY2   | 2K                           |             |  |             |                       |               |                       |  |                             |  | 3.0, 5.0   |  |   |            |
| MC68HLC908QY2  |                              |             |  |             |                       |               |                       |  |                             |  |  | 2.2 to 3.6   |   |            |
| MC68HC908QY4   | 4K                           |             |  |             |                       |               |                       |  |                             |  | 3.0, 5.0   |  |   |            |
| MC68HLC908QY4  |                              |             |  |             |                       |               |                       |  |                             |  |  | 2.2 to 3.6   |   |            |
| MC68HC908QY8   | 8K                           | 256         | 4-CH, IC/OC or PWM                                   | Up to 13    | —                     | 10-CH, 10-bit | 3.0, 5.0              | 8.0  | M                           | 16-pin TSSOP (DT)  | DEM0908QB8<br>FSICEKITQBLTY                        | Auto wakeup module, KBI  |   |            |
| <b>RF Family—Integrated RF</b>   |                              |             |  |             |                       |               |                       |  |                             |  |  |  |   |            |
| MC68HC908RF2   | 2K                           | 128         | 2-CH, IC/OC or PWM                                   | 12          | —                     | —             | 1.8 to 3.6            | 4.0  | C, M                        | 32-pin LQFP (FA)   | FSICEKITKX   | Integrated RF transmitter  |   |            |
| <b>SR Family—Smart Battery, Temperature Sensor, and I<sup>2</sup>C</b> |                              |             |  |             |                       |               |                       |  |                             |  |  |  |   |            |
| MC68HC908SR12  | 12K                          | 512         | Dual 2-CH, IC/OC or PWM                              | Up to 31    | I <sup>2</sup> C, SCI | 14-CH, 8-bit  | 3.0, 5.0              | 8.0  | C, M                        | 48-pin LQFP (FA)<br>42-pin SDIP (B)                      | FSICEKITSR12                                       | RC oscillator, 32 kHz PLLI internal oscillator options; 8 KBI's, TBM, current detect |   |            |

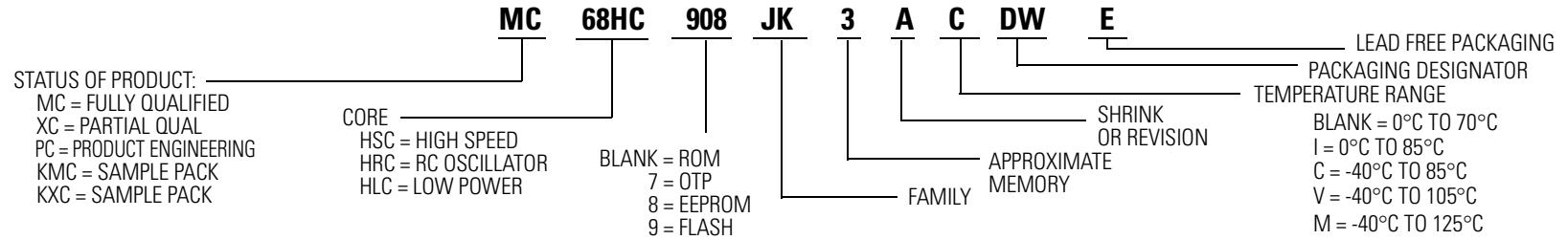
### 68HC08 Reference Manuals

CPU08RM, HC08 CPU Reference Manual  
TIM08RM, HC08 Timer Reference Manual

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HC08 FAMILY (continued)

Product Numbering System for 68HC08



### HC08 FAMILY (continued)

#### HC(S)08 DEVELOPMENT SOFTWARE

#### **CodeWarrior Development Studio for HC(S)08, Special Edition (Free-of-Charge)**

This comprehensive special edition toolset combines the best from Freescale Semiconductor and leading third-party developers and is bound to establish a new standard with the included components for fast and easy MCU development.

##### Features

- Industry-leading CodeWarrior Integrated Development Environment
  - Project manager for up to 32 files
  - Built-in “stationary” templates with example assembly and C code to help create new projects faster
- Full-chip simulation and Flash programming with P&E Microcomputer Systems technology
  - Start software development immediately using simulator without waiting for target hardware or requiring an evaluation board
- Assembler, linker, and assembly source level debugger supports all 68HC08 MCUs
- Highly optimized ANSI C compiler and C source level debugger for many of the most popular Freescale Semiconductor 68HC08 MCUs

Over 60 optimization strategies specifically designed to boost performance and reduce code size

- Processor Expert™ rapid application design tool from UNIS
  - Provides optimized and tested automatic C code generation for most 68HC08 on-chip peripherals to dramatically reduce development time and improve code quality
  - Helps to eliminate time spent on low-level details, which allows more time for adding value to system solution
  - Promotes reusability and easy system configuration
  - Verifies design based on actual MCU resources and timing contentions to help catch potential problems before you begin debugging
- Fully supports Freescale Semiconductor 68HC08 hardware development tools
- FSICE, MMDS, MMEVS, ICS, Cyclone, MON08 Multilink, and BDM Multilink for HCS08 devices
- Supports BDM Multilink and ROM monitor debugging for HCS08
- Takes full advantage of innovative on-chip HCS08 debug module

The special edition software is available on CD-ROM and for download from [www.freescale.com](http://www.freescale.com)

**The order number for this product is CWX-H08-SE.**

#### **Codewarrior C Compiler**

**(MSRP Starting at \$500)**

The special edition is a full-featured toolset with a 16K code size limited version of our highly optimized C compiler. You can upgrade to a 32K version or an unlimited 64K version to support all HC(S)08s. Both upgrades have project management for greater than 32 files.

The order number is CWS-H08-C32K-CX (32K version)

CWS-H08-C64K-CX (64K version)

CWS-H08-CC-CX (Unlimited version)

#### **Standard Edition**

**(MSRP \$2,394)**

The standard edition includes an unrestricted C compiler, project management for greater than 32 files, unlimited data visualization tools to ease debugging, and access to an additional 170-plus software-only objects for the Processor Expert design tool.

**The order number for this product is CWS-H08-STDED-CX.**

#### **Professional Edition**

**(MSRP \$4,794)**

The professional edition provides the following additional advanced tools to speed your design to market:

- Code coverage analysis allows the user to isolate unused or badly used portions of code
- Profiler/performance analysis to identify and optimize critical portions of code
- Software trace/logic analyzer, combined with breakpoints, to quickly isolate complex problems
- Additional advanced objects for the Processor Expert design tool, including support for complex communication modules such as CAN
- Processor Expert Wizard that allows design of custom beans
- OSEK awareness
- I-Logix Rhapsody in MicroC interface

**The order number for this product is CWS-H08-PROED-CX.**





## HC08 FAMILY (continued)

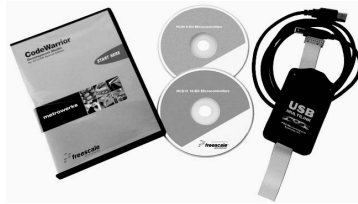
### MON08 Multilink

(MSRP \$99)

The MON08 Multilink is an easy to use low-cost development tool for Freescale Semiconductor 68HC08 FLASH MCUs. The MON08 Multilink provides in-circuit emulation, debugging, and programming through the 68HC08's standard MON08 serial debug/breakpoint interface.

Some of the features that help make the MON08 Multilink such a versatile, time-saving tool are:

- Universal development tool for all MON08 68HC08s
- Real-time in-circuit emulation and debug
- Fast in-circuit programming
- Auto-detects baud rate and frequency
- Provides optional overdrive clock to target
- Small unobtrusive size (approximately 3" x 2" x 3/4")
- Supports 2.0 V to 5.5 V 68HC08s
- Automatically cycles power for security checks (up to 125 mA)
- USB interface
- Includes CodeWarrior Development Studio for HC(S)08, Special Edition



The order number for this product is [USBMULTILINK08](#).

### HC08 Programming Adapters

(MSRP Starting at \$99)

The HC08 Programming adapter boards allow a P&E Cyclone or other compatible MON08 programmers to program loose HC08 MCUs. The programming adapter boards feature ZIF sockets, standard MON08 header, and MCU breakout headers.

#### M68CPA08 PART NUMBERS

- M68CPA08QF324448 supports 32-pin 0.8mm QFP packages, 44-pin 0.8mm QFP packages, and 48-pin 0.5mm QFP packages
- M68CPA08QF80 supports 80-pin 0.5mm QFP and 80-pin 0.65mm QFP packages
- M68CPA08QF5264 supports 52-pin 0.65mm QFP, 64-pin 0.5mm QFP, and 64-pin 0.8mm QFP packages
- M68CPA08W1628T20 supports 7.5mm SOIC packages up to 28 pins, 5.3mm SOIC packages up to 16 pins, TSSOP devices you must purchase and install socket U2 (Yamaichi part number IC51-0202-779)
- M68CPA08P40B56 supports DIP packages up to 40 pins and SDIP packages up to 56 pins



The order numbers for these products are [M68CPA08QP324448](#), [M68CPA08QP80](#), [M68CPA08QP5264](#), [M68CPA08W1628T20](#), and [M68CPA08P40B56](#).

### Cyclone Pro Universal Standalone In-Circuit Debugger/Programmer (MSRP \$499)



The Cyclone Pro provides all the capabilities of the BDM Multilink plus USB/Ethernet interfaces, the ability to function as a standalone programmer with push buttons and LEDs to control operation, and support for all MON08 HC08s and BDM HCS08s, HCS12s and HCS12Xs.

- Supports all HC(S)08s, HC(S)12s, and HC(S)12Xs
- Ethernet, USB, and Serial interfaces
- Fast in-circuit flash programming
- Scripting capability automates programming of test routines, test execution, erase, and final SW programming
- Auto-detects baud rate and frequency of target MU
- Provides optional overdrive clock to target MCU
- Automatically cycles power for security checks (up to 500 mA)
- Supports 1.8 V to 5.5 V

The order number for this product is [M68CYCLONEPRO](#).

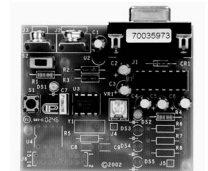
### MC68HC908QT4 Demonstration Board

(MSRP \$25)

The 68HC908QT4 demonstration kit contains everything a designer needs to develop and evaluate applications for the Q Family of M68HC08 Microcontrollers.

#### Features

- Demonstration board with a 4K FLASH 68HC908QT4 8-pin MCU
- Tutorial and demonstration code including A/D, timer, PWM, and keyboard interrupt routines to help you learn the 68HC908QT Family quickly
- Free Special Edition CodeWarrior Development Studio for HC(S)08, allowing you to:
  - Modify demo code or develop new code for the 68HC908QT4 in assembly or C
  - Program and debug code through DB9 serial port and included RS-232 serial cable



The order number for this product is [M68DEMO908QT4](#).

### HC08 FAMILY (continued)

#### MC68HC908AP64 Demonstration Board

The HC908AP64 demonstration board contains everything a designer needs to develop and evaluate 68HC908AP64 family of applications.

- Freescale 68HC908AP64 MCU in a 42 pin SDIP package
- Low cost SDIP socket
- Regulated +5V power supply
- RS-232 COM Serial Port w/ DB9 Connector
- MON08 circuit for debugging and FLASH programming via the COM Port. No other development hardware required.
- User Components for application development include: Full-duplex IrDA Interface (low speed), Reset push button, 2 input push buttons, light sensor and potentiometer for ATD input, 2 output LEDs
- Learn the 68HC908AP64 MCU quickly by stepping through the provided C demonstration code
- Program and debug code using free CodeWarrior Development Studio for HC(S)08, Special Edition through DB9 serial port and included RS-232 serial cable or optional USBMULTILINK08 cable

The order number for this product is [DEM0908AP64](#).

#### MC68HC908LB8 Demonstration Board

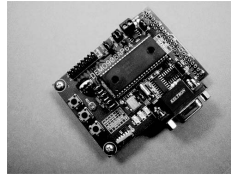
The MC68HC908LB8 demonstration board allows a designer to develop and evaluate the MC68HC908LB MCU family of applications.

##### Features

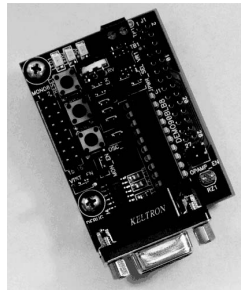
- Freescale M68HC908LB8 MCU
- Regulated 5 V power supply
- 9.8304 MHz oscillator w/Enable Jumper
- MON08 circuit for debugging and FLASH programming via the COM Port. No other development hardware required.
- User components for application development include: Reset push button, 2 input buttons, 2 output LEDs, and potentiometer
- Program and debug code using free CodeWarrior Development Studio for HC(S)08, Special Edition through DB9 serial port and included RS-232 serial cable or optional USBMULTILINK08 cable.

The order number for this product is [DEM0908LB8](#).

(MSRP \$49)



(MSRP \$49)



#### MC68HC908GZ60 Demonstration Board

(MSRP \$49.95)

The HC908GZ60 demonstration board contains everything a designer needs to develop and evaluate 68HC908GR/GZ family of applications.

- Freescale 68HC908GZ60 MCU in a 64 LQFP
- Regulated +5V power supply with ON/OFF slide Switch
- RS-232 COM Serial Port w/ DB9 Connector
- MON08 circuit for debugging and FLASH programming via the COM Port. No other development hardware required.
- User Components for application development include: RUN/LOAD slide switch, Reset push button, 2 input push buttons, light sensor and potentiometer for ATD input, 8 output LEDs
- Communication interfaces including single Fault-Tolerant Controller Area Network (CAN) and single Local Interconnect Network (LIN)
- Learn the 68HC908GZ60 MCU quickly by stepping through the provided C demonstration code
- Program and debug code using free CodeWarrior Development Studio for HC(S)08, Special Edition through DB9 serial port and included RS-232 serial cable or optional USBMULTILINK08 cable

The order number for this product is [DEM0908GZ60](#).

#### MC68HC908QL4 Evaluation Board

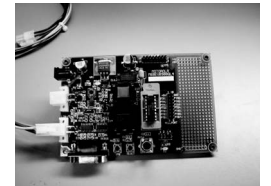
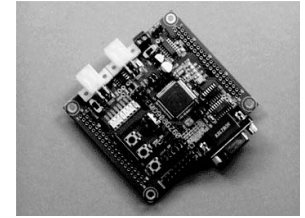
(MSRP \$199.10)

Freescale's advanced MC68HC908QL4 evaluation board, M68EVB908QL4, contains everything a designer needs to develop and evaluate the MC68HC908QL4 family applications. This comprehensive kit includes an evaluation board that utilizes the MC68HC908QL4 on chip slave LIN interface controller (SLIC) module.

The M68EVB908QL4 evaluation board is centered around the MC68HC908QL4 MCU and the enhanced LIN interface. The enhanced LIN interface consists of an MC33661 enhanced LIN transceiver and a 5 V regulator (an LT1121 chip). The board can be programmed either using the RS-232 MON08 interface or a Cyclone/MultiLink tool.

- Freescale 68HC908QL4 MCU
- ZIF sockets for TSSOP or DIP packages
- Regulated 5 V power supply
- LIN Ports with transceiver (MC33399)
- MON08 circuit for debugging and FLASH programming via the COM Port. No other development hardware required.
- User Components for application development include: prototype area, Reset push button, 2 input push buttons, LED, and potentiometer for ATD input
- Program and debug code using free CodeWarrior Development Studio for HC(S)08, Special Edition through DB9 serial port and included RS-232 serial cable or optional USBMULTILINK08 cable

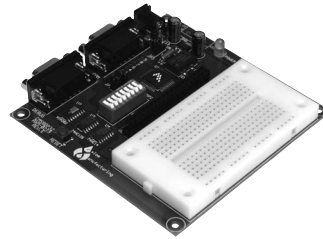
The order number for this product is [M68EVB908QL4](#).



## HC08 FAMILY (continued)

### MC68HC908GP32 Evaluation Board

(MSRP \$168.20)



Freescale's advanced MC68HC908GP32 evaluation board, M68EVB908GP32, contains everything a designer needs to develop and evaluate the MC68HC908GP32 family applications.

- Freescale MC68HC908GP32 FLASH MCU
- Regulated 5 V power supply
- Mode switch for easy operation change
- DB9 RS232 serial connector
- 9.8304 MHz oscillator with 32 kHz user option
- Universal power supply
- MON08 circuit for debugging and FLASH programming via the COM Port. No other development hardware required.
- User Components for application development include a large prototype area to support circuit designs.
- Program and debug code using free CodeWarrior Development Studio for HC(S)08, Special Edition through DB9 serial port and included RS232 serial cable or optional USBMULTILINK08 cable.

The order number for this product is [M68EVB908GP32](#).

### MC908QC16 Demonstration Board

(MSRP \$75)



The MC908QC16 demonstration board is a complete development system that allows you to develop and evaluate MC908QC16 family of applications. The DEMO908QC16 board has a built-in USB-to-MON08 interface for FLASH programming and debugging.

- Freescale MC908QC16 MCU
- Integrated USB-to-MON08 interface for FLASH programming and debugging
- RS-232 COM Serial Port w/DB9 Connector
- DB9 RS232 serial connector
- LIN Ports with transceiver (MC33399)
- Internal or External oscillator option
- User components for application development include: Reset push button, 2 input push buttons, light sensor and potentiometer for ATD input, 4 output LEDs
- Program and debug code using free CodeWarrior Development Studio for HC(S)08, Special Edition through the integrated USB-to-MON08 interface

The order number for this product is [DEMO908QC16](#).

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*HC08 FAMILY (continued)*

**Freescale In-circuit Emulator (FSICE) Kits**

**(MSRP starting at \$1495)**



The Freescale Semiconductor in-circuit emulator (FSICE) is a full-featured emulator system for developing embedded systems using HC(9)08 microcontrollers. The FSICE system consists of a platform board and an MCU emulator module (EM). Connected to your target system, the emulator replicates the actual target system MCU. The CodeWarrior development environment (IDE) interface allows for quick edits and changes to assembly code, which makes design, debug, and real-time evaluation of the target system as efficient as possible. Use this economical system to perform traditional debugging activities such as executing code in run or step mode, setting break points, monitoring or modifying CPU registers, memory and application variables, and creating log or script files to record test results or create test suites.

In addition to incorporating the debug features of the traditional emulators, FSICE adds advanced features such as a built-in USBMULTILINK08 cable for in-circuit Flash programming. Ethernet interface for remote debugging and application development, and real-time bus analyzer with 24 general-purpose logic inputs for capture user-defined bus cycles or events. The bus state analyzer can also help a designer debug the MCU support circuitry, verify nested or complex program flows using the full range of sequenced or logical event triggering and data capture modes, and ensure proper timing by using the custom time tag clock.

Since the FSICE maintains the module approach of the MMDS and MMEVS systems, the FSICE system is designed to support present and future HC(9)08 MCU families. Here is a summary of a complete FSICE system:

- 1) FSICE Base Station: FSICEBASE
- 2) HC(9)08 Emulator Module: M68EML08xxxx, M68EM08xxxx, EML08xxxx, or EM08xxxx
- 3) Target Cables: M68CBLxxxx or EMCBLxxxx
- 4) Target Head Adapters: M68TA08xxxx, M68TB08xxxx, M68TC08xxxx, M68TE08xxxx, or TH08xxxx
- 5) HC(9)08 Programming Adapter: M68CPA08xxxx

**To simplify the ordering process, Freescale offers device specific HC(9)08 FSICE Kits. These kits contain everything needed to begin developing for a HC(9)08 MCU family.**

**FSICE KITS INCLUDE:**

- FSICE Base Station (Part Number: FSICEBASE)
- Device-specific emulation module
- Device-specific target cable
- Device-specific target head adapters
- Package-specific Programming Adapters (M68CPA08xxxx)
- CodeWarrior™ Development Studio for HC(S)08, Special Edition

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| Freescale HC(9)08 MCUs  | MCU Emulator Module | Part Numbers for FSICE Kits |
|---|---------------------|-----------------------------|
| 908AB32   | M68EML08AB32        | FSICEKITAB32                |
| 908AP64, AP64A, AP32, AP32A, AP16, AP16A, AP8                 | M68EML08AP          | FSICEKITAP64                |
| 908AS60A, AS60, AS32A, AS32, AZ60A, AZ60, AZ32A, AZ32         | M68EM08AS/AZ60A     | FSICEKITASAZ                |
| 908EY8, 908EY16   | M68EML08EY          | FSICEKITEY                  |
| 908GP32, 908GT8, GT16   | M68EML08GPGT        | FSICEKITGPGT                |
| 908GR16, GR32A, GR48A, GR60A, GZ8, GZ16, GZ32, GZ48, GZ60     | M68EML08GZ          | FSICEKITGRGZ                |
| 908GR4, GR8   | M68EML08GPGT        | FSICEKITGR8                 |
| 908JB12, JB16, JG16   | M68EM08JB.JG        | FSICEKITJB.JG               |
| 908JB8  | M68EM08JB8          | FSICEKITJB8                 |
| 908JL3E, JL8, JK3E, JK1E, JK8                                 | M68EML08JLJK        | FSICEKITJLJK                |
| 908JW32   | M68EM08JW32         | FSICEKITJW32                |
| 908KX8, KX2, RF2  | M68EML08KX          | FSICEKITKX                  |
| 908LB8  | M68EML08LB8         | FSICEKITLB8                 |
| 908LJ12, LJ24, LK24   | M68EML08LJLK        | FSICEKITLJLK                |
| 908MR32, MR16   | M68EM08MR32         | FSICEKITMR32                |
| 908MR8, MR4   | M68EM08MR8          | FSICEKITMR8                 |
| 908QB8, QB4, QL4, QL3, QL2, QT4, QT2, QT1, QY4, QY2, QY1, QY8 | M68EML08QBLTY       | FSICEKITQBLTY               |
| 908QC16, QC8  | EML08QCBLTY         | FSICEKITQC16                |
| 908SR12   | M68EML08SR12        | FSICEKITSR12                |

## HCS08 FAMILY

### HCS08 Product Table

For complete part number information and temperature definitions, refer to "Product Numbering System for HCS08" on page SG1006-14.

| Product  | Flash Program Memory (bytes) | RAM (bytes) | 16-bit Timers                           | I/O      | Communication                  | ADC           | Operating Voltage (V) | Bus Frequency (max.) | Temperature Options | Packaging  | Development Tools               | Additional Information  |
|--|------------------------------|-------------|---|----------|--------------------------------|---------------|-----------------------|----------------------|---------------------|--|---------------------------------|---|
| <b>AW Family—Appliance Focus</b>                               |                              |             |   |          |                                |               |                       |                      |                     |  |                                 |   |
| MC9S08AW16   | 16K                          | 2K          | 2-CH + 6-CH, IC/OC or PWM               | Up to 50 | SPI, 2 SCI, I <sup>2</sup> C   | 16-CH, 10-bit | 3.0, 5.0              | 20.0                 | M                   | 44-pin LQFP (FG)<br>48-pin QFN (FD)<br>64-pin QFP (FU)   | DEMO9S08AW60                    | Low voltage inhibit;<br>Low voltage warming;<br>Highly accurate internal oscillator   |
| MC9S08AW32   | 32K                          |             |   |          |                                |               |                       |                      |                     |  |                                 |   |
| MC9S08AW48   | 48K                          |             |   |          |                                |               |                       |                      |                     |  |                                 |   |
| MC9S08AW60   | 60K                          |             |   |          | SPI, 2 ESCI, I <sup>2</sup> C  |               |                       |                      |                     |  |                                 |   |
| <b>G Family—High Performance, General Purpose, Low-Voltage</b> |                              |             |   |          |                                |               |                       |                      |                     |  |                                 |   |
| MC9S08GB32A  | 32K                          | 2K          | 3-CH + 5-CH, IC/OC or PWM               | 56       | 2 SCI, 1 SPI, I <sup>2</sup> C | 8-CH, 10-bit  | 1.8 to 3.6            | 20.0                 | C                   | 64-pin LQFP (FU)   | M68DEMO908GB60<br>M68EVB908GB60 | www.freescale.com   |
| MC9S08GT32A  |                              |             | Dual 2-CH, IC/OC or PWM                 | Up to 39 |                                |               |                       |                      |                     | 44-pin QFP (FB)<br>48-pin QFN (FD)   |                                 |   |
| MC9S08GB60A  | 60K                          | 4K          | 3-CH + 5-CH, IC/OC or PWM               | 56       | 2 SCI, SPI, I <sup>2</sup> C   | 8-CH, 10-bit  | 1.8 to 3.6            | 20.0                 | C                   | 64-pin LQFP (FU)   |                                 |   |
| MC9S08GT60A  |                              |             | Dual 2-CH, IC/OC or PWM                 | Up to 39 |                                |               |                       |                      |                     | 44-pin QFP (FB)<br>48-pin QFN (FD)   |                                 |   |
| MC9S08GB32   | 32K                          | 2K          | 8-CH, IC/OC or PWM                      | 56       | 2 SCI, SPI, I <sup>2</sup> C   | 8-CH, 10-bit  | 1.8 to 3.6            | 20.0                 | C                   | 64-pin QFP (FU)  | M68DEMO908GB60<br>M68EVB908GB60 | On-chip debug interface   |
| MC9S08GB60   | 60K                          | 4K          |   |          |                                |               |                       |                      |                     |  |                                 |   |
| MC9S08GT16   | 16K                          | 2K          | Dual 2-CH, IC/OC or PWM                 | Up to 36 | 2 SCI, SPI, I <sup>2</sup> C   | 8-CH, 10-bit  | 1.8 to 3.6            | 20.0                 | C                   | 42-pin SDIP (B)<br>44-pin QFP (FB)<br>48-pin QFN (FD)  |                                 |   |
| MC9S08GT32   | 32K                          |             |   |          |                                |               |                       |                      |                     |  |                                 |   |
| MC9S08GT60   | 60K                          |             |   |          |                                |               |                       |                      |                     | 4K   |                                 |   |
| <b>QG Family—General</b>                                       |                              |             |   |          |                                |               |                       |                      |                     |  |                                 |   |
| MC9S08QG4  | 4K                           | 256         | 2-CH, IC/OC or PWM + 8-bit modulo timer | Up to 12 | SPI, SCI, I <sup>2</sup> C     | 8-CH, 10-bit  | 1.8 to 3.6            | 10.0                 | C, M                | 16-pin PDIP (PB)<br>16-pin QFN (FF)<br>16-pin TSSOP (DT)<br>8-pin DFN (FQ)<br>8-pin PDIP (PA)<br>8-pin SOIC (DN) | DEMO9S08QG8                     | Temp. sensor, on-chip debug interface, internal clock source (ICS) containing a frequency-locked-loop (FLL), Analog Comparator (ACMP) |
| MC9S08QG8  | 8K                           | 512         |   |          |                                |               |                       |                      |                     |  |                                 |   |

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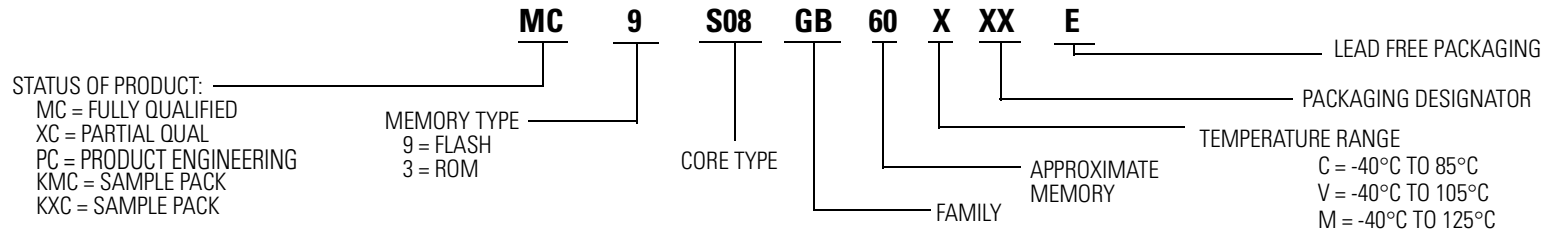
# HCS08 FAMILY

## HCS08 FAMILY (continued) HCS08 Product Table (continued)

For complete part number information and temperature definitions, refer to "Product Numbering System for HCS08" on page SG1006-14.

| Product  | Flash Program Memory (bytes) | RAM (bytes) | 16-bit Timers      | I/O      | Communication | ADC | Operating Voltage (V) | Bus Frequency (max.) | Temperature Options | Packaging                            | Development Tools | Additional Information                 |     |
|--|------------------------------|-------------|--------------------|----------|---------------|-----|-----------------------|----------------------|---------------------|--------------------------------------|-------------------|--|-----|
| <b>R Family—Low Power, High Performance for Battery Powered Applications</b> |                              |             |                    |          |               |     |                       |                      |                     |                                      |                   |  |     |
| MC9S08RC8  | 8K                           | 1K          | 2-CH, IC/OC or PWM | Up to 39 | —             | —   | 1.8 to 3.6            | 8.0                  | C                   | 32-pin LQFP (FJ)<br>44-pin LQFP (FG) | DEMO9S08RG60      | Analog Comparator, Low voltage warning |     |
| MC9S08RC16   | 16k                          |             |                    |          |               |     |                       |                      |                     |                                      |                   |  |     |
| MC9S08RC32   | 32K                          |             |                    |          |               |     |                       |                      |                     |                                      |                   |  |     |
| MC9S08RC60   | 60K                          |             |                    |          |               |     |                       |                      |                     |                                      |                   |  |     |
| MC9S08RD8  | 8K                           | 1K          |                    |          |               |     |                       |                      |                     |                                      |                   |  | SCI |
| MC9S08RD16   | 16K                          |             |                    |          |               |     |                       |                      |                     |                                      |                   |  |     |
| MC9S08RD32   | 32K                          | 2K          |                    |          |               |     |                       |                      |                     |                                      |                   |  |     |
| MC9S08RD60   | 60K                          |             |                    |          |               |     |                       |                      |                     |                                      |                   |  |     |
| MC9S08RE8  | 8K                           | 1K          |                    |          | SCI, SPI      |     |                       |                      |                     |                                      |                   |  |     |
| MC9S08RE16   | 16K                          |             |                    |          |               |     |                       |                      |                     |                                      |                   |  |     |
| MC9S08RE32   | 32K                          | 2K          |                    |          |               |     |                       |                      |                     |                                      |                   |  |     |
| MC9S08RE60   | 60K                          |             |                    |          |               |     |                       |                      |                     |                                      |                   |  |     |
| MC9S08RG32   | 32K                          | 1K          |                    |          |               |     |                       |                      |                     |                                      |                   |  |     |
| MC9S08RG60   | 60K                          | 2K          |                    |          |               |     |                       |                      |                     |                                      |                   |  |     |

### Product Numbering System for HCS08



## HCS08 FAMILY (continued)

### **BDM Multilink Universal In-Circuit Debugger/Programmer (MSRP \$99)**

The BDM Multilink is an easy-to-use, low-cost development tool for all HCS08, HCS12, and HCS12X MCUs.

- Real-time in-circuit debug through HCS08s BDM interface
- Fast in-circuit flash programming
- Small unobtrusive size (approximately 3" x 2" x 3/4")
- Supports 1.8 V to 5.5 V HC(S)08s
- Includes CodeWarrior Development Studio for HC(S)08 and HC(S)12, Special Edition
- USB interface



The order number for this product is [USBMULTILINKBDM](#).

### **Cyclone Pro Universal Standalone In-Circuit Debugger/Programmer (MSRP \$499)**

The Cyclone Pro provides all the capabilities of the BDM Multilink plus USB/Ethernet interfaces, the ability to function as a standalone programmer with push buttons and LEDs to control operation, and support for all MON08 HCO8s and BDM HS08s, HCS12s, and MCS12Xs.

- Supports all HC(S)08s, HC(S)12s, and HC(S)12Xs
- Ethernet, USB, and Serial interfaces
- Fast in-circuit flash programming
- Scripting capability automates programming of test routines, test execution, erase, and final SW programming
- Auto-detects baud rate and frequency of target MU
- Provides optional overdrive clock to target MCU
- Automatically cycles power for security checks (up to 500 mA)
- Supports 1.8 V to 5.5 V

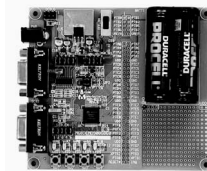


The order number for this product is [M68CYCLONEPRO](#).

### **MC9S08GB60 Demonstration Board (MSRP \$49)**

The GB60 demonstration kit contains everything a designer needs to develop and evaluate MC9S08GB or GT Family applications.

- Demonstration board with a 60K Flash MC9S08GB60 MCU, dual DB9 RS-232 serial ports, switches, LEDs, potentiometer, MCU pin-breakout header, and small prototype area
- Powered by 2 AA batteries (included) or optional external power supply
- Learn the HCS08 MCU Family quickly with demonstration code including A/D, timer, PWM, and keyboard interrupt routines
- Modify demo code or develop new code for the GB60 in assembly or C using free CodeWarrior Development Studio for HC(S)08, Special Edition
- Program and debug code using free CodeWarrior Development Studio for HC(S)08, Special Edition through DB9 serial port and included RS-232 serial cable or optional BDM Multilink

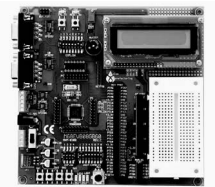


The order number for this product is [M68DEMO908GB60](#).

### **MC9S08GB60 Evaluation Board (MSRP \$168.20)**

The GB60 evaluation kit contains everything a designer needs to develop and evaluate MC9S08GB or GT Family applications.

- Evaluation board with a 60K flash MC9S08GB60 MCU, LCD display, dual DB9 RS-232 serial ports, switches, LEDs, and a breadboard area
- Modify demo code or develop new code for the GB60 in assembly or C using free CodeWarrior Development Studio for HC(S)08, Special Edition
- Program and debug code using free CodeWarrior Development Studio for HC(S)08, Special Edition through DB9 serial port and included RS-232 serial cable or optional BDM Multilink
- Universal power supply

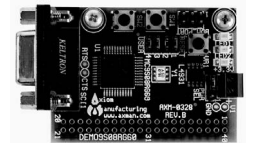


The order number for this product is [M68EVB908GB60](#).

### **MC9S08RG60 Demonstration Board (MSRP \$49.95)**

The RG60 demonstration board allows a designer to develop and evaluate MC9S08RG, RC, RD, or RE Family applications.

- Demonstration board with a 60K Flash MC9S08RG60 MCU, DB9, RS-232 serial port, switches, LEDs, Voltage Regulator, and MCU pin-breakout header
- Small Form Factor (approximately 5.5 cm x 4 cm)
- Innovative HCS08 Serial Monitor allows users to program, debug, and emulate application code via the RS-232 DB9 serial port
- Modify demo code or develop new code for the GB60 in assembly or C using free CodeWarrior Development Studio for HC(S)08, Special Edition



The order number for this product is [DEMO9S08RG60](#).

### **MC9S08QG8 Demonstration Board (MSRP \$50)**

The MC9S08QG8 demonstration board is a complete development system that allows you to develop and evaluate MC9S08QG8 family of applications. The DEMO9S08QG8 board has a built-in USB-to-DBM interface for FLASH programming and debugging.

- Freescale MC9S08QG8 MCU
- Integrated USB-to-DBM interface for FLASH programming and debugging
- Internal or external oscillator options
- RS-232 COM Serial Port w/DB9 Connector
- 8-pin DIP and 16-pin DIP sockets for easier evaluation of 9S08QG family devices
- User components for application development include: Reset push button, 2 input push buttons, light sensor and potentiometer for ATD input, 4 output LEDs
- Program and debug code using free CodeWarrior Development Studio for HC(S)08, Special Edition through the integrated USB-to-DBM interface



The order number for this product is [DEMO9S08QG8](#).

A change bar appears in the left margin to mark the location of new or revised information.

**HCS08 FAMILY (continued)****MC9S08AW60 Demonstration Board****(MSRP \$135)**

The MC9S08AW60 demonstration board is a complete development system that allows you to develop and evaluate MC9S08AW60 family of applications. The DEMO9S08AW60 board has a built-in USB-to-DBM interface for FLASH programming and debugging.

- Freescale MC9S08AW60 MCU
- Integrated USB-to-DBM interface for FLASH programming and debugging
- RS-232 COM Serial Port w/DB9 Connector
- 64-pin ZIF socket for easier evaluation of 9S08QG family devices
- User components for application development include: Reset push button, 8 output LED bar, 8 input switches, 4 slide switches, light sensor, potentiometer, and Freescale 2-axis accelerometer (MMA6260Q)
- Program and debug code using free CodeWarrior Development Studio for HC(S)08, Special Edition through the integrated USB-to-DBM interface

The order number for this product is **DEMO9S08AW60**.



## 68HC12 FAMILY

### 68HC12 Product Table *Note*

For complete part number information and temperature definitions, refer to "Product Numbering System for 68HC12" on page SG1006-18.

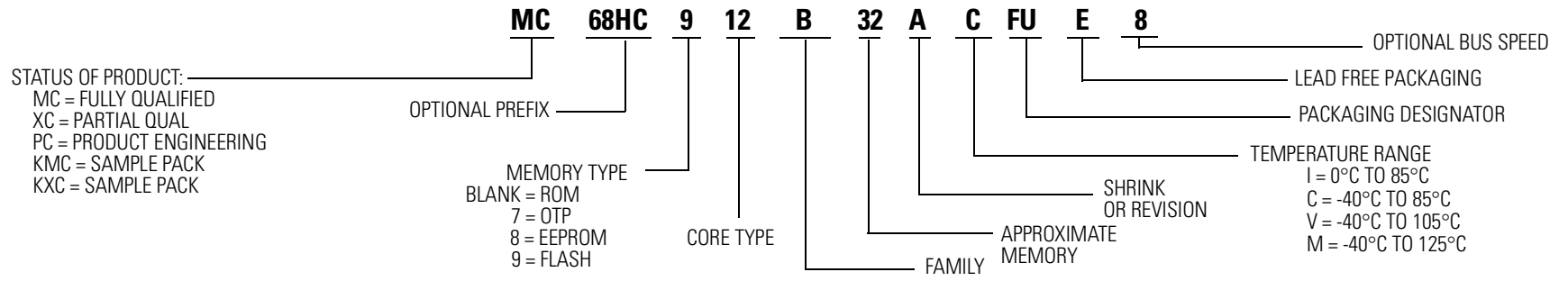
| Product              | ROM (KB)                                     | RAM (KB) | EEPROM (Bytes) | Flash (KB)                                   | Timer  | I/O                   | Serial   | A/D                   | PWM                           | Operating Voltage (V) | Max Bus Frequency (MHz) | Temp Options | Packaging                            | Status    | Additional Information  | Documentation  |
|----------------------|--|----------|----------------|--|--|-----------------------|--|-----------------------|-------------------------------|-----------------------|-------------------------|--------------|--------------------------------------|-----------|---|----------------|
| <b>HC12A Family</b>  |  |          |                |  |  |                       |  |                       |                               |                       |                         |              |                                      |           |   |                |
| MC68HC812A4          | n/a  | 1        | 4K             | n/a  | 8-CH, 16-bit IC or OC RTI, pulse accumulator | Up to 91              | Dual SCI SPI   | 8-CH, 8-bit           | n/a                           | 3.3, 5.0              | 8.0<br>5.0              | C            | 112-pin LQFP (PV)                    | Available | Non-muxed bus, 7 programmable chip selects, KBI (24 pins), PLL, BDM, 5M-byte external memory, 3.0–3.6 V, 5 MHz version (XC68C812A4) | MC68HC812A4    |
| <b>HC12B Family</b>  |  |          |                |  |  |                       |  |                       |                               |                       |                         |              |                                      |           |   |                |
| MC68HC912B32         | n/a  | 1        | 768            | 32   | 8-CH, 16-bit IC or OC RTI, pulse accumulator | Up to 63              | SCI, SPI J1850   | 8-CH, 10-bit          | 4-CH, 8-bit or 2-CH, 16-bit   | 5.0                   | 8.0                     | C, V, M      | 80-pin QFP (FU)                      | Available | J1850, muxed bus, BDM   | MC68HC912B     |
| MC68HC12BC32         | 32   |          |                | 8-CH, 16-bit                                 | 4-CH, 8-bit                                  |                       | 4.5 to 5.5   |                       | Part equipped with CAN 2.0A/B |                       |                         |              |                                      |           | MC68HC912B32TS  |                |
| XC912BC32            | n/a  |          |                | 8-CH, 16-bit IC or OC RTI, pulse accumulator | 4-CH, 8-bit or 2-CH, 16-bit                  |                       |  |                       | 5.0                           | MSCAN CAN 2.0B, BDM   |                         |              |                                      |           | MC68HC912B  |                |
| MC68HC12BE32         | 32   |          |                | n/a  | SCI, SPI J1850                               |                       | BDM, enhanced timer Evaluation product with on-chip monitor: XC12BE32DCFU8 |                       |                               |                       |                         |              |                                      |           |   |                |
| <b>HC12DG Family</b> |  |          |                |  |  |                       |  |                       |                               |                       |                         |              |                                      |           |   |                |
| XC68HC12D60          | 60   | 2        | 1K             | n/a  | 8-CH, 16-bit                                 | Up to 66 I/O and 18 i | Dual SCI SPI   | Dual 8-CH, 10-bit     | 4-CH, 8-bit or 2-CH, 16-bit   | 5.0                   | 8.0                     | C, V, M      | 80-pin QFP (FU)<br>112-pin LQFP (PV) | Available | Part equipped with CAN 2.0A/B   | MC68HC912D60   |
| MC912D60A            | n/a  |          |                | 60   | 8-CH, 16-bit IC or OC RTI, pulse accumulator |                       | Dual SCI SPI, CAN  | 8-CH, 10-bit          |                               |                       |                         |              |                                      |           | Replaces the XC68HC912D60 with 5 V Flash voltage and a different programming algorithm  |                |
| MC912DG128           | MC912DG128A is a pin-compatible replacement. |          |                |  |  |                       |  |                       |                               |                       |                         |              |                                      |           |   |                |
| MC912DG128A          | n/a  | 8        | 2K             | 128  | 8-CH, 16-bit IC or OC RTI, pulse accumulator | Up to 67 I/O and 18 i | Dual SCI SPI, CAN  | 8-CH or 16-CH, 10-bit | 4-CH, 8-bit or 2-CH, 16-bit   | 5.0                   | 8.0                     | C, V, M      | 112-pin LQFP (PV)                    | Available | Replaces the XC912DG128 with 5 V Flash voltage and a different programming algorithm  | MC68HC912DG128 |
| <b>HC12DT Family</b> |  |          |                |  |  |                       |  |                       |                               |                       |                         |              |                                      |           |   |                |
| MC68HC912DT128A      | n/a  | 8        | 2K             | 128  | 8-CH, 16-bit                                 | Up to 66 I/O and 18 i | Dual SCI, SPI  | Dual 8-CH, 10-bit     | 4-CH, 8-bit or 2-CH, 16-bit   | 5.0                   | 8.0                     | C, V, M      | 112-pin LQFP (PV)                    | Available | Part equipped with 3xCAN 2.0A/B   | MC68HC912DT128 |

Note: All 68HC12 MCUs incorporate a COP watchdog timer.

### 68HC12 Reference Manual

CPU12RM, HC12 CPU Reference Manual

**68HC12 FAMILY (continued)**  
**Product Numbering System for 68HC12**



## HCS12 FAMILY

### HCS12 Product Table

HCS12 Dx and A Family devices offer pin-for-pin compatibility.

For complete part number information and temperature definitions, refer to “Product Numbering System for HCS12” on page SG1006-22.

| Product   | ROM (Bytes) | RAM (KB) | Flash or OTP (KB) | EEPROM (KB) | Timer                       | I/O      | Serial                             | MUX | A/D                    | PWM                               | Operating Voltage (V) | Operating Frequency (MHz) | Temp Options | Packaging   | OTP or Flash Equiv. | Status    | Additional Information   | Documentation |
|---|-------------|----------|-------------------|-------------|-----------------------------|----------|------------------------------------|-----|------------------------|-----------------------------------|-----------------------|---------------------------|--------------|---|---------------------|-----------|--|---------------|
| <b>S12A Family—General Purpose with I<sup>2</sup>C</b>              |             |          |                   |             |                             |          |                                    |     |                        |                                   |                       |                           |              |   |                     |           |  |               |
| MC9S12A32   |             | 2        | 32 Flash          |             | 8-CH, 16-bit ECT            | Up to 59 | 2 SCI<br>1 SPI                     |     | 8-CH, 10-bit           | 7-CH, 8-bit or 3-CH, 16-bit       | 3.0, 5.0              |                           |              | 80-pin QFP (FU)                                       |                     |           | www.freescale.com  | MC9S12DP256   |
| MC9S12A64   |             | 4        | 64 Flash          | 1           |                             |          | Up to 2 SCI<br>1 SPI<br>IIC        |     |                        |                                   |                       | 25.0                      |              |   |                     |           | The 80 QFP differs from the 112 LQFP in that it offers only up to 59 I/O, has 1 x 8-CH A/D, and has 7-CH PWM | 9S12A64DGV1   |
| MC9S12A128  | n/a         | 8        | 128 Flash         | 2           | 8-CH, 16-bit IC, OC, PA     | Up to 91 | Up to 2 SCI<br>2 SPI<br>IIC        | n/a | Up to 2 x 8-CH, 10-bit | Up to 8-CH, 8-bit or 4-CH, 16-bit | 5.0                   |                           | C            | 80-pin QFP (FU)<br>112-pin LQFP (PV)                  | n/a                 | Available |  | 9S12A128DGV1  |
| MC9S12A256  |             | 12       | 256 Flash         |             |                             |          | Up to 2 SCI<br>3 SPI<br>IIC        |     |                        |                                   |                       |                           |              |   |                     |           | www.freescale.com  | 9S12A256DGV1  |
| MC9S12A512  |             | 14       | 512 Flash         | 4           | 8-CH, 16-bit ECT            |          | 2 SCI<br>3 SPI<br>I <sup>2</sup> C |     |                        | 8-CH, 8-bit or 4-CH, 16-bit       |                       | 25.0, 33.0                |              | 112-pin LQFP (PV)                                     |                     |           |  | MC9S12DP512   |
| <b>S12B Family—Automotive/Industrial with Intermediate Cost CAN</b> |             |          |                   |             |                             |          |                                    |     |                        |                                   |                       |                           |              |   |                     |           |  |               |
| MC9S12B128  |             | 4        | 128 Flash         |             | 8-CH, 16-bit IC, OC, or PWM | Up to 91 | SCI, SPI, I <sup>2</sup> C         | CAN | 8-CH, 16-bit           | See Timer                         | 3.0 to 5.0            | 25                        | C, V, M      | 112-pin LQFP (PV)<br>80-pin QFP (FU)                  | n/a                 | Available | www.freescale.com  |               |
| MC9S12B64   | n/a         | 2        | 64 Flash          | 1           |                             |          |                                    |     |                        |                                   |                       |                           |              |   |                     |           |  | 9S12B128DGV1  |
| <b>S12C Family—Low Pin Count, Low Cost CAN</b>                      |             |          |                   |             |                             |          |                                    |     |                        |                                   |                       |                           |              |   |                     |           |  |               |
| MC9S12C128  |             |          | 128 Flash         |             |                             |          |                                    |     |                        |                                   | 3.0 to 5.0            | 25                        | C, V, M      | 80-QFP, 52 LQFP, 48 LQFP                              |                     |           |  | 9S12C128DGV1  |
| MC9S12C96   |             | 4        | 96 Flash          | 0           | 8-CH, 16-bit IC, OC, or PWM | Up to 60 | SCI<br>SPI                         | CAN | 8-CH, 10-bit           | See Timer                         |                       |                           |              |   |                     |           |  |               |
| MC9S12C64   | n/a         |          | 64 Flash          |             |                             |          |                                    |     |                        |                                   | 3.15 to 5.5           | 16, 25                    | C, M         | 48-pin QFP (FA)<br>52-pin QFP (PB)<br>80-pin QFP (FU) | n/a                 | Available | www.freescale.com  | 9S12C32D6V1   |
| MC9S12C32   |             | 2        | 32 Flash          | n/a         |                             |          |                                    |     |                        |                                   |                       |                           |              |   |                     |           |  |               |

# HCS12 FAMILY

## HCS12 FAMILY (continued) HCS12 Product Table (continued)

HCS12 Dx and A Family devices offer pin-for-pin compatibility.

For complete part number information and temperature definitions, refer to "Product Numbering System for HCS12" on page SG1006-22.

| Product   | ROM (Bytes) | RAM (KB) | Flash or OTP (KB) | EEPROM (KB) | Timer                   | I/O      | Serial                             | MUX                              | A/D                    | PWM                               | Operating Voltage (V) | Operating Frequency (MHz) | Temp Options | Packaging                            | OTP or Flash Equiv. | Status    | Additional Information   | Documentation  |                |
|---|-------------|----------|-------------------|-------------|-------------------------|----------|------------------------------------|----------------------------------|------------------------|-----------------------------------|-----------------------|---------------------------|--------------|--------------------------------------|---------------------|-----------|--|----------------|----------------|
| <b>S12D Family—Automotive/Industrial with CAN</b> |             |          |                   |             |                         |          |                                    |                                  |                        |                                   |                       |                           |              |                                      |                     |           |  |                |                |
| MC9S12D32   |             | 2        | 32 Flash          |             | 8-CH, 16-bit ECT        | Up to 59 | 2 SCI<br>1 SPI                     | CAN                              | 8-CH, 10-bit           | 7-CH, 8-bit or 3-CH, 16-bit       | 5.0                   |                           |              | 80-pin QFP (FU)                      |                     |           | www.freescale.com  | MC9S12DP256    |                |
| MC9S12D64   |             | 4        | 64 Flash          | 1           |                         |          | Up to 2 SCI<br>1 SPI<br>IIC        | 1 CAN<br>2.0A/2.0B               |                        |                                   |                       |                           |              |                                      |                     |           | The 80 QFP differs from the 112 LQFP in that it offers only up to 59 I/O, has 1 x 8-CH A/D, and has 7-CH PWM | 9S12D64DGV1    |                |
| MC9S12DB128                                       |             |          |                   |             |                         |          | Up to 2 SCI<br>2 SPI               | 1 CAN<br>Byteflight              |                        |                                   |                       |                           |              | 80-pin QFP (FU)<br>112-pin LQFP (PV) |                     |           | www.freescale.com  |                |                |
| MC9S12DG128                                       |             | 8        | 128 Flash         | 2           | 8-CH, 16-bit IC, OC, PA | Up to 91 | Up to 2 SCI<br>2 SPI<br>IIC        | 2 CAN                            | Up to 2 x 8-CH, 10-bit | Up to 8-CH, 8-bit or 4-CH, 16-bit | 5.0                   |                           |              |                                      |                     |           | The 80 QFP differs from the 112 LQFP in that it offers only up to 59 I/O, has 1 x 8-CH A/D, and has 7-CH PWM | 9S12DT128BDGV1 |                |
| MC9S12DG256                                       |             | 12       | 256 Flash         | 4           |                         |          | 2 SCI<br>3 SPI<br>IIC              |                                  |                        |                                   |                       | 25.0                      |              | 112-pin LQFP (PV)                    |                     |           | www.freescale.com  | 9S12DP256BDGV2 |                |
| MC9S12DJ64  | n/a         | 4        | 64K Flash         | 1           |                         |          | Up to 2 SCI<br>1 SPI<br>IIC        | 1 CAN<br>2.0A/2.0B and 1 x J1850 |                        |                                   |                       |                           | C, V, M      |                                      | n/a                 | Available | The 80 QFP differs from the 112 LQFP in that it offers only up to 59 I/O, has 1 x 8-CH A/D, and has 7-CH PWM | 9S12DJ64DGV1   |                |
| MC9S12DJ128                                       |             | 8        | 128 Flash         | 2           | 8-CH, 16-bit IC, OC, PA |          | Up to 2 SCI<br>2 SPI<br>IIC        | 2 CAN and 1 x J1850              |                        |                                   |                       |                           |              | 80-pin QFP (FU)<br>112-pin LQFP (PV) |                     |           | The 80 QFP differs from the 112 LQFP in that it offers only up to 59 I/O, has 1 x 8-CH A/D, and has 7-CH PWM | 9S12DT128BDGV1 |                |
| MC9S12DJ256                                       |             |          |                   |             |                         |          | Up to 2 SCI<br>3 SPI<br>IIC        |                                  |                        |                                   |                       |                           |              |                                      |                     |           |  |                | 9S12DP256BDGV2 |
| MC9S12DP256                                       |             | 12       | 256 Flash         | 4           |                         |          |                                    |                                  |                        |                                   |                       |                           |              |                                      |                     |           |  |                |                |
| MC9S12DP512                                       |             | 14       | 512 Flash         | 4           | 8-CH, 16-bit ECT        | Up to 91 | 2 SCI<br>3 SPI<br>I <sup>2</sup> C | 5 CAN                            | Up to 2 x 8-CH, 10-bit | Up to 8-CH, 8-bit or 4-CH, 16-bit | 5.0                   | 25.0, 33.0                |              |                                      |                     |           |  |                | MC9S12DP512    |
| MC9S12DT128                                       |             | 8        | 128 Flash         | 2           |                         |          | 2 SCI<br>2 SPI<br>IIC              |                                  |                        |                                   |                       |                           |              | 112-pin LQFP (PV)                    |                     |           | www.freescale.com  | 9S12DT128BDGV1 |                |
| MC9S12DT256                                       |             | 12       | 256 Flash         | 4           | 8-CH, 16-bit IC, OC, PA |          | 2 SCI<br>3 SPI<br>IIC              | 3 CAN                            |                        |                                   |                       | 25.0                      |              |                                      |                     |           |  |                | 9S12DP256BDGV2 |

**HCS12 FAMILY (continued)**

**HCS12 Product Table (continued)**

HCS12 Dx and A Family devices offer pin-for-pin compatibility.

For complete part number information and temperature definitions, refer to "Product Numbering System for HCS12" on page SG1006-22.

| Product  | ROM (Bytes) | RAM (KB) | Flash or OTP (KB) | EEPROM (KB) | Timer                            | I/O               | Serial                     | MUX             | A/D           | PWM                         | Operating Voltage (V) | Operating Frequency (MHz) | Temp Options                           | Packaging                            | OTP or Flash Equiv. | Status    | Additional Information                                  | Documentation |
|--|-------------|----------|-------------------|-------------|----------------------------------|-------------------|----------------------------|-----------------|---------------|-----------------------------|-----------------------|---------------------------|--|--------------------------------------|---------------------|-----------|---|---------------|
| <b>S12E Family—General Purpose, 3 Volts with D/A</b> |             |          |                   |             |                                  |                   |                            |                 |               |                             |                       |                           |  |                                      |                     |           |   |               |
| MC9S12E64  | n/a         | 4        | 64 Flash          | n/a         | Three 4-CH, 16-bit IC, OC or PWM | Up to 90          | 3 SCI SPI I <sup>2</sup> C | n/a             | 16-CH, 10-bit | See Timer                   | 3.3 to 5.0            | 16.0, 25.0                | C, M                                   | 112-pin LQFP (PV)<br>80-pin QFP (FU) | n/a                 | Available | Two D/A Converters                                      | 9S12E128DGV1  |
| MC9S12E128   |             | 8        | 128 Flash         |             |                                  |                   |                            |                 |               |                             |                       |                           |  |                                      |                     |           |   |               |
| <b>S12GC Family—Low Cost, Low Pin Count</b>          |             |          |                   |             |                                  |                   |                            |                 |               |                             |                       |                           |  |                                      |                     |           |   |               |
| MC9S12GC128  | n/a         | 4        | 128 Flash         | 0           | 8-CH, 16-bit IC, OC, PWM         | Up to 60          | SCI SPI                    | n/a             | 8-CH, 10-bit  | See Timer                   | 3.0 to 5.0            | 25.0                      | C, V, M                                | 52-pin LQFP (PB)<br>48-pin LQFP (FA) | n/a                 | Available | www.freescale.com                                       | 9S12C128DGV1  |
| MC9S12GC96   |             |          | 96 Flash          |             |                                  |                   |                            |                 |               |                             |                       |                           |  |                                      |                     |           |   |               |
| MC9S12GC64   |             |          | 64 Flash          |             |                                  |                   |                            |                 |               |                             |                       |                           |  |                                      |                     |           |   |               |
| MC9S12GC32   |             | 2        | 32 Flash          |             |                                  |                   |                            |                 |               |                             |                       |                           |  |                                      |                     |           |   |               |
| MC9S12GC16   |             |          | 16 Flash          |             |                                  |                   |                            |                 |               |                             |                       |                           |  |                                      |                     |           |   |               |
| <b>S12H Family—LCD/H-Bridge Drivers with CAN</b>     |             |          |                   |             |                                  |                   |                            |                 |               |                             |                       |                           |  |                                      |                     |           |   |               |
| MC9S12H128B  | n/a         | 12       | 128 Flash         | 4           | 8-CH, 16-bit IC, OC, PA          | 99 plus 18 inputs | SCI SPI IIC                | 2 CAN 2.0A/2.0B | 16-CH, 10-bit | 6-CH, 8-bit or 3-CH, 16-bit | 5.0                   | 16.0                      | V                                      | 112-pin LQFP (PV)                    | n/a                 | Available | LCD driver module: up to 32 frontpanes and 4 backpanes. | 9S12H256BDGV1 |
| MC9S12H256B  |             |          | C, V, M           |             |                                  |                   |                            |                 |               |                             |                       |                           | 112-pin LQFP (PV)<br>144-pin LQFP (FV) |                                      |                     |           |   |               |

# HCS12 FAMILY

## HCS12 FAMILY (continued)

### HCS12 Product Table (continued)

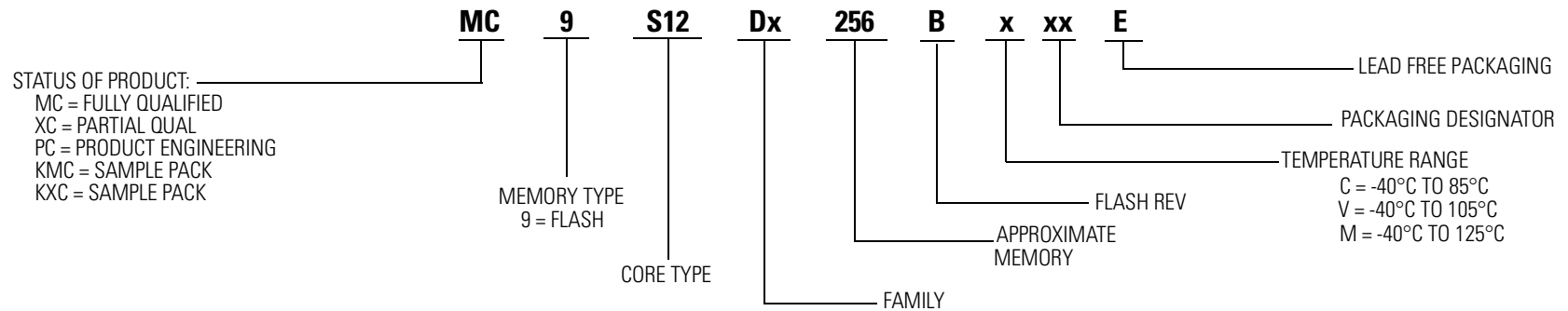
HCS12 Dx and A Family devices offer pin-for-pin compatibility.

For complete part number information and temperature definitions, refer to "Product Numbering System for HCS12" on page SG1006-22.

| Product  | ROM (Bytes) | RAM (KB)     | Flash or OTP (KB) | EEPROM (KB) | Timer                       | I/O      | Serial                           | MUX | A/D          | PWM                         | Operating Voltage (V) | Operating Frequency (MHz) | Temp Options         | Packaging                                | OTP or Flash Equiv. | Status    | Additional Information   | Documentation |  |
|--|-------------|--------------|-------------------|-------------|-----------------------------|----------|----------------------------------|-----|--------------|-----------------------------|-----------------------|---------------------------|----------------------|--|---------------------|-----------|--|---------------|--|
| <b>S12NE Family—Single Chip with 10/100 Base-T with Integrated MAC and PHY</b> |             |              |                   |             |                             |          |                                  |     |              |                             |                       |                           |                      |  |                     |           |  |               |  |
| MC9S12NE64   | n/a         | 8            | 64 Flash          | n/a         | 4-CH, 16-bit IC, OC or PWM  | Up to 70 | 2 SCI<br>SPI<br>I <sup>2</sup> C | n/a | 8-CH, 10-bit | See Timer                   | 3.0                   | 16.0, 25.0                | C (PV)<br>V (TU)     | 112-pin LQFP (PV)<br>80-pin TQFP-EP (TU) | n/a                 | Available | Integrated Media Access Controller (EMAC), 10/100 Ethernet PHY (EPHY)  | 9S12NE64BDUG  |  |
| <b>S12T Family—CALRAM with Fast BDM</b>  |             |              |                   |             |                             |          |                                  |     |              |                             |                       |                           |                      |  |                     |           |  |               |  |
| MC9S12T64  | n/a         | 2 + 2 CALRAM | 64 Flash          | n/a         | 8-CH, 16-bit IC, OC, PA     | 25       | 2 SCI<br>1 SPI                   | n/a | 8-CH, 10-bit | 8-CH, 8-bit or 4-CH, 16-bit | 5.0                   | 16.0                      | C, V, M <sup>1</sup> | 80-pin QFP (PK)                          | n/a                 | Available | FBDM (Fast Background Debug Mode)  | 9S12T64BDGV1  |  |
| <b>S12UF Family—USB 2.0</b>  |             |              |                   |             |                             |          |                                  |     |              |                             |                       |                           |                      |  |                     |           |  |               |  |
| MC9S12UF32   | n/a         | 3.5          | 32 Flash          | n/a         | 8-CH, 16-bit IC, OC, or PWM | Up to 75 | SCI<br>USB 2.0                   | n/a | n/a          | See Timer                   | 5.0                   | 30.0                      | 0°C to 70°C          | 100-pin LQFP (PU)<br>64-pin LQFP         | n/a                 | Available | Built-in host controller modules for ATA-5 interface, CompactFlash, Secure Digital/Multimedia Card, SmartMedia, and Memory Stick | 9S12UF32DGV1  |  |

<sup>1</sup> M temperature range limited to single-chip mode

### Product Numbering System for HCS12



## HCS12X FAMILY

### HCS12X Product Table

HCS12 Dx and A Family devices offer pin-for-pin compatibility.

For complete part number information and temperature definitions, refer to "Product Numbering System for HCS12X" on page SG1006-24.

| Product      | ROM (KB) | RAM (KB) | Flash (KB) Serial | EEPROM (KB) | Timer            | I/O | XGATE | Serial                               | MUX   | A/D               | PWM                         | Packaging    | Oper Voltage (V) | Oper Freq (MHz) | Temp Options | Flash or OTP | Status     | Additional Information | Documentation  |
|--------------|----------|----------|-------------------|-------------|------------------|-----|-------|--------------------------------------|-------|-------------------|-----------------------------|--------------|------------------|-----------------|--------------|--------------|------------|------------------------|----------------|
| MC9S12XDP512 | n/a      | 32       | 512               | 4           | 8-CH, 16-bit ECT | 91  | Yes   | 4 SCI<br>3 SPI<br>1 I <sup>2</sup> C | 5 CAN | 2 x 8-CH, 10-bit  | 8-CH, 8-bit or 4-CH, 16-bit | 112-pin LQFP | 3.3 to 5.5       | 40.0            | C, V, M      | n/a          | Production | —                      | 9S12XDP512DVG1 |
| MC9S12XDP512 |          |          |                   |             |                  | 119 |       | 6 SCI<br>3 SPI<br>2 I <sup>2</sup> C |       | 2 x 12-CH, 10-bit |                             | 144-pin LQFP |                  |                 |              |              |            |                        |                |
| MC9S12XDT512 | n/a      | 20       | 512               | 4           | 8-CH, 16-bit ECT | 59  | Yes   | 2 SCI<br>2 SPI<br>1 I <sup>2</sup> C | 3 CAN | 1 x 8-CH, 10-bit  | 7-CH, 8-bit or 3-CH, 16-bit | 80-pin QFP   | 3.3 to 5.5       | 40.0            | C, V, M      | n/a          | Production | —                      | 9S12XDP512DVG1 |
| MC9S12XDT512 |          |          |                   |             |                  | 91  |       | 4 SCI<br>3 SPI<br>1 I <sup>2</sup> C |       | 2 x 8-CH, 10-bit  | 8-CH, 8-bit or 4-CH, 16-bit | 112-pin LQFP |                  |                 |              |              |            |                        |                |
| MC9S12XDT512 |          |          |                   |             |                  | 119 |       | 6 SCI<br>3 SPI<br>1 I <sup>2</sup> C |       | 2 x 12-CH, 10-bit | 8-CH, 8-bit or 4-CH, 16-bit | 144-pin LQFP |                  |                 |              |              |            |                        |                |
| MC9S12XDT256 | n/a      | 16       | 256               | 4           | 8-CH, 16-bit ECT | 59  | Yes   | 2 SCI<br>2 SPI<br>1 I <sup>2</sup> C | 3 CAN | 1 x 8-CH, 10-bit  | 7-CH, 8-bit or 3-CH, 16-bit | 80-pin QFP   | 3.3 to 5.5       | 40.0            | C, V, M      | n/a          | Production | —                      | 9S12XDP512DVG1 |
| MC9S12XDT256 |          |          |                   |             |                  | 91  |       | 4 SCI<br>3 SPI<br>1 I <sup>2</sup> C |       | 2 x 8-CH, 10-bit  | 8-CH, 8-bit or 4-CH, 16-bit | 112-pin LQFP |                  |                 |              |              |            |                        |                |
| MC9S12XDT256 |          |          |                   |             |                  | 119 |       | 4 SCI<br>3 SPI<br>1 I <sup>2</sup> C |       | 2 x 12-CH, 10-bit | 8-CH, 8-bit or 4-CH, 16-bit | 144-pin LQFP |                  |                 |              |              |            |                        |                |
| MC9S12XD256  | n/a      | 14       | 256               | 4           | 8-CH, 16-bit ECT | 59  | Yes   | 2 SCI<br>2 SPI<br>1 I <sup>2</sup> C | 1 CAN | 1 x 8-CH, 10-bit  | 7-CH, 8-bit or 3-CH, 16-bit | 80-pin QFP   | 3.3 to 5.5       | 40.0            | C, V, M      | n/a          | Production | —                      | 9S12XDP512DVG1 |
| MC9S12XD256  |          |          |                   |             |                  | 91  |       | 4 SCI<br>2 SPI<br>1 I <sup>2</sup> C |       | 2 x 8-CH, 10-bit  | 8-CH, 8-bit or 4-CH, 16-bit | 112-pin LQFP |                  |                 |              |              |            |                        |                |
| MC9S12XD256  |          |          |                   |             |                  | 119 |       | 4 SCI<br>2 SPI<br>1 I <sup>2</sup> C |       | 2 x 12-CH, 10-bit | 8-CH, 8-bit or 4-CH, 16-bit | 144-pin LQFP |                  |                 |              |              |            |                        |                |

## HCS12X FAMILY

### HCS12X FAMILY (continued)

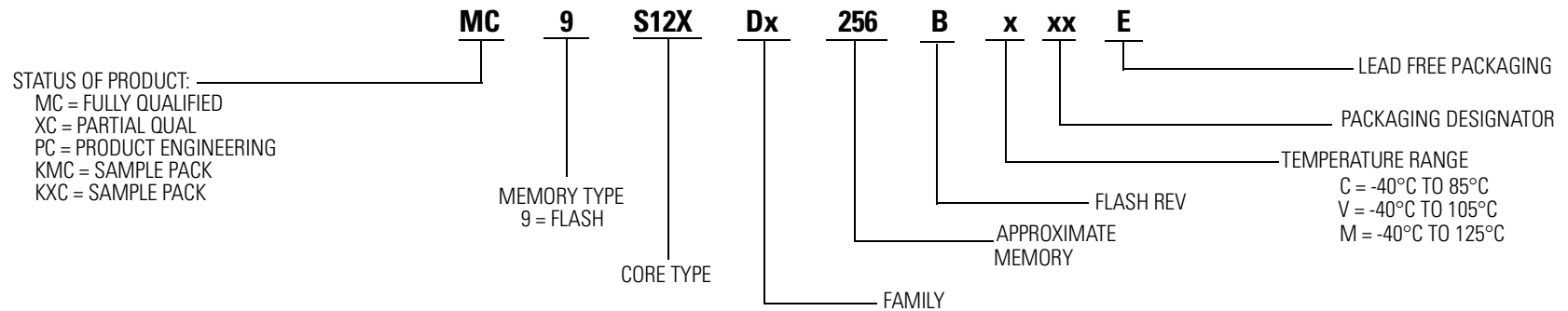
#### HCS12X Product Table (continued)

HCS12 Dx and A Family devices offer pin-for-pin compatibility.

For complete part number information and temperature definitions, refer to "Product Numbering System for HCS12X" on page SG1006-24.

| Product     | ROM (KB) | RAM (KB) | Flash (KB) Serial | EEPROM (KB) | Timer                    | I/O                                  | XGATE | Serial                               | MUX | A/D                         | PWM                         | Packaging  | Oper Voltage (V) | Oper Freq (MHz) | Temp Options | Flash or OTP | Status     | Additional Information | Documentation  |
|-------------|----------|----------|-------------------|-------------|--------------------------|--------------------------------------|-------|--------------------------------------|-----|-----------------------------|-----------------------------|------------|------------------|-----------------|--------------|--------------|------------|------------------------|----------------|
| MC9S12XA512 | n/a      | 32       | 512               | 4           | 8-CH, 16-bit IC, OC, PWM | 59                                   | Yes   | 2 SCI<br>2 SPI<br>1 I <sup>2</sup> C | n/a | 1 x 8-CH, 10-bit            | 7-CH, 8-bit or 3-CH, 16-bit | 80-pin QFP | 3.3 to 5.5       | 40.0            | C, V         | n/a          | Production | —                      | 9S12XDP512DVG1 |
| 91          |          |          |                   |             |                          | 4 SCI<br>3 SPI<br>1 I <sup>2</sup> C |       | 2 x 8-CH, 10-bit                     |     | 8-CH, 8-bit or 4-CH, 16-bit | 112-pin LQFP                |            |                  |                 |              |              |            |                        |                |
| 119         |          |          |                   |             |                          | 6 SCI<br>3 SPI<br>2 I <sup>2</sup> C |       | 2 x 12-CH, 10-bit                    |     | 8-CH, 8-bit or 4-CH, 16-bit | 144-pin LQFP                |            |                  |                 |              |              |            |                        |                |
| MC9S12XA256 | n/a      | 16       | 267               | 4           | 8-CH, 16-bit IC, OC, PWM | 59                                   | Yes   | 2 SCI<br>2 SPI<br>1 I <sup>2</sup> C | n/a | 1 x 8-CH, 10-bit            | 7-CH, 8-bit or 3-CH, 16-bit | 80-pin QFP | 3.3 to 5.5       | 40.0            | C, V         | n/a          | Production | —                      | 9S12XDP512DVG1 |
| 91          |          |          |                   |             |                          | 4 SCI<br>3 SPI<br>1 I <sup>2</sup> C |       | 2 x 8-CH, 10-bit                     |     | 8-CH, 8-bit or 4-CH, 16-bit | 112-pin LQFP                |            |                  |                 |              |              |            |                        |                |
| 119         |          |          |                   |             |                          | 4 SCI<br>3 SPI<br>1 I <sup>2</sup> C |       | 2 x 12-CH, 10-bit                    |     | 8-CH, 8-bit or 4-CH, 16-bit | 144-pin LQFP                |            |                  |                 |              |              |            |                        |                |

#### Product Numbering System for HCS12X





## MCF5xxx FAMILY

### MCF5xxx Product Table *Note*

For complete part number information and temperature definitions, refer to "MCF5xxx FAMILY (continued)" on page SG1006-28.

| Product  | Core | Dhrvs 2.1 MIPS @ max MHz | Processor Cache (Bytes) | Processor Flash (Kbytes) | Processor SRAM (Bytes)             | Serial Interface, UART    | Timers/ CS/ GPIO | DMA     | DRAM Controller | 10/100 Eth/ USB1.1 | Operating Voltage (V) | Operating Frequency (MHz)          | Temp Options  | Packaging          | Rev  | Additional Information |
|----------|------|--------------------------|-------------------------|--------------------------|------------------------------------|---------------------------|------------------|---------|-----------------|--------------------|-----------------------|------------------------------------|---|--------------------|--|------------------------|
| MCF5206  | V2   | 17                       | 512 I                   | n/a                      | 512K                               | 2 UARTs                   | 2/8/8            | n/a     | FPM, EDO        | n/a                | 5                     | 16, 25, 33                         | C   | 160-pin QFP        | A  | www.freescale.com.     |
| MCF5206E |      | 50                       | 4K I                    |                          | 8K                                 |                           |                  | 2-CH    |                 |                    | 3.3                   | 40, 54                             |   |                    | Enhanced pin-compatible version of 5206 with MAC, HW divide, BDM, I <sup>2</sup> C, 5V tolerant I/O. |                        |
| MCF5207  |      | 159                      | 8K Config. I/D          | 16K                      | 3 UARTs                            | 8/8/up to 30              | 16-CH            | DDR/SDR | One 10/100      | 1.5, 2.5, 3.3      | 166                   | 144-pin LQFP<br>144-ball<br>MAPBGA | 32x32 EMAC, QSPI, I <sup>2</sup> C.   |                    |  |                        |
| MCF5208  |      |                          |                         |                          |                                    | 8/8/up to 50              |                  |         |                 |                    |                       | 160-pin QFP<br>196-ball<br>MAPBGA  |   |                    |  |                        |
| MCF5211  |      | 76                       | n/a                     | 256                      | 32K                                | 16/0/up to 33             | 4-CH             | None    | n/a             | 3.3                | 66, 80                | 64-pin LQFP<br>81-ball<br>MAPBGA   | 32x32 EMAC, QSPI, I <sup>2</sup> C,<br>10-CH, 12-bit ADC.   |                    |  |                        |
| MCP5212  |      |                          |                         |                          |                                    | 16/0/up to 44             |                  |         |                 |                    |                       | 100-pin LQFP<br>81-ball<br>MAPBGA  |   |                    |  |                        |
| MCF5213  |      |                          |                         |                          |                                    | 16/0/up to 56             |                  |         |                 |                    |                       |                                    |   |                    |  |                        |
| MCF5214  |      | 66                       | 2K I                    | 64K                      | 3 UARTs,<br>1 PC, 1 CAN            | 8 + 4 DAM/7/<br>up to 150 | 4-CH             | SDRAM   | n/a             | 3.3, 5             | 80, 100, 150          | 256-ball<br>MAPBGA                 | 256 KB Flash.   |                    |  |                        |
| MCF5216  |      |                          |                         |                          |                                    | 24/8/up to 102            |                  |         |                 |                    |                       | 160-pin QFP<br>196-ball<br>MAPBGA  | 512 KB Flash.   |                    |  |                        |
| MCF5232  |      | 142                      | 8K Config.              | n/a                      | 96K                                | 3 UARTs                   | 24/8/up to 102   | 4-CH    | SDRAM           | n/a                | 1.5, 3.3              | 100, 150                           | 256-ball<br>MAPBGA  | 16-CH eTPU.        |  |                        |
| MCF5233  |      |                          |                         |                          |                                    |                           |                  |         |                 |                    |                       |                                    |   | 40/8/up to 142     | 32-CH eTPU.  |                        |
| MCF5234  |      |                          |                         |                          |                                    |                           |                  |         |                 |                    |                       |                                    |   | 24/8/up to 142     | 16-CH eTPU.  |                        |
| MCF5235  |      |                          |                         |                          |                                    |                           |                  |         |                 |                    |                       |                                    |   |                    | 16-CH eTPU, Crypto Enabled.  |                        |
| MCF5249  |      | 125                      | 8K I                    | 96K                      | 2 UARTs,<br>I <sup>2</sup> C, QSPI | 2/4/up to 47              | 4-CH             | SDRAM   | n/a             | 1.8, 3.3           | 140                   | 160-ball<br>MAPBGA                 | EMAC, HW divide, BDM, 12-bit ADC, CDROM block. CD text, hard disk drive, Memory stick interfaces. Audio decoders. |                    |  |                        |
| MCF5249L |      | 107                      |                         |                          |                                    | 2/3/up to 34              |                  |         |                 |                    | 120                   | 144-pin LQFP                       | EMAC, HW divide, BDM, 12-bit ADC, CDROM block. hard disk drive interface. Audio decoders.                         |                    |  |                        |
| MCF5270  |      | 144                      | 8K Config. I/D          | 64K                      | 3 UARTs                            | 8/8/up to 39              | 4-CH             | SDRAM   | n/a             | 1.5, 3.3           | 100                   | 160-pin QFP                        | 32x32 EMAC, QSPI, I <sup>2</sup> C.   |                    |  |                        |
| MCF5270  |      |                          |                         |                          |                                    | 8/8/up to 61              |                  |         |                 |                    |                       |                                    |   | 196-ball<br>MAPBGA |  |                        |

Note: Extended temperature products with minimum order requirements. All temperature/speed combinations may not be valid. Consult the factory to verify.

A change bar appears in the left margin to mark the location of new or revised information.

## MCF5xxx FAMILY (continued)

MCF5xxx Product Table <sup>Note</sup> (continued)

For complete part number information and temperature definitions, refer to "MCF5xxx FAMILY (continued)" on page SG1006-28.

| Product  | Core | Dhrvs 2.1 MIPS @ max MHz | Processor Cache (Bytes) | Processor Flash (Kbytes) | Processor SRAM (Bytes) | Serial Interface, UART                   | Timers/ CS/ GPIO                                     | DMA  | DRAM Controller | 10/100 Eth/ USB1.1                 | Operating Voltage (V) | Operating Frequency (MHz) | Temp Options | Packaging       | Rev | Additional Information  |
|----------|------|--------------------------|-------------------------|--------------------------|------------------------|--|--|------|-----------------|------------------------------------|-----------------------|---------------------------|--------------|-----------------|-----|---|
| MCF5271  | V2   | 144                      | 8K Config. I/D          | n/a                      | 64K                    | 3 UARTs                                  | 8/8/up to 39   | 4-CH | SDRAM           | One 10/100                         | 1.5, 3.3              | 100                       | C            | 160-pin QFP     | n/a | Hardware Encryption, 32x32 EMAC, QSPI, I <sup>2</sup> C.  |
| MCF5271  |      |                          |                         |                          |                        |  |  |      |                 | 8/8/up to 61                       |                       |                           | B            |                 |     |   |
| MCF5272  | V3   | 63                       | 1K I                    | n/a                      | 4K                     | 10/100 FEC, 2 UARTs, USB, QSPI           | 4/8/up to 32   | 2-CH | SDRAM           | MAC/ MAC+PHY                       | 3.3                   | 66                        | C            | 196-ball MAPBGA | n/a | MAC, HW divide, BDM, 4 TDM GCI/IDL ports, software HDCL module, QSPI, 3 PWMs, 5 V tolerant I/O.   |
| MCF5274L |      |                          |                         |                          |                        |  |  |      |                 | 8/8/up to 61                       |                       |                           | B            | 256-ball MAPBGA |     |   |
| MCF5274  | V3   | 159                      | 16K Config. I/D         | n/a                      | 64K                    | 3 UARTs                                  | 8/8/up to 69   | 4-CH | DDR             | Two 10/100, USB 2.0 Full-Sp Device | 1.5, 2.5, 3.3         | 166                       | B            | 196-ball MAPBGA | n/a | 32x32 EMAC, QSPI, I <sup>2</sup> C.   |
| MCF5275L |      |                          |                         |                          |                        |  |  |      |                 |                                    |                       |                           |              |                 |     |   |
| MCF5275  | V3   | 63                       | 2K I                    | n/a                      | 64K                    | 3 UARTs, I <sup>2</sup> C, QSPI, FlexCAN | 8/8/up to 69   | 4-CH | SDRAM           | Two 10/100, USB 2.0 Full-Sp Device | 3.3, 5.0              | 66, 80                    | C            | 256-ball MAPBGA | n/a | Enhanced CAN 2.0B controller. Flashless version of MCF5282.                                       |
| MCF5280  |      |                          |                         |                          |                        |  |  |      |                 |                                    |                       |                           |              |                 |     |   |
| MCF5281  | V3   | 54                       | 2K I                    | n/a                      | 64K                    | 3 UARTs, I <sup>2</sup> C, QSPI, FlexCAN | 4 Timers, +4 DMA Timers, 7 Chip Sel., Up to 150 I/Os | 4-CH | SDRAM           | MAC (FEC)/n/a                      | 3.3, 5.0              | 66, 80                    | C            | 256-ball MAPBGA | n/a | Enhanced CAN 2.0B controller. This product incorporates SuperFlash® technology licensed from SST. |
| MCF5282  |      |                          |                         |                          |                        |  |  |      |                 |                                    |                       |                           |              |                 |     |   |
| MCF5307  | V3   | 75                       | 8K I                    | n/a                      | 4K                     | 2 UARTs, I <sup>2</sup> C                | 2/8/16   |      | SDRAM, FPM, EDO | n/a                                | 3.3                   | 66, 90                    |              | 208-pin QFP     | B   | MAC, HW divide, BDM, PLL, I <sup>2</sup> C, 5 V tolerant I/O.                                     |

Note: Extended temperature products with minimum order requirements. All temperature/speed combinations may not be valid. Consult the factory to verify.

MCF5xxx FAMILY (continued)

**MCF5xxx Product Table** <sup>Note</sup> (continued)

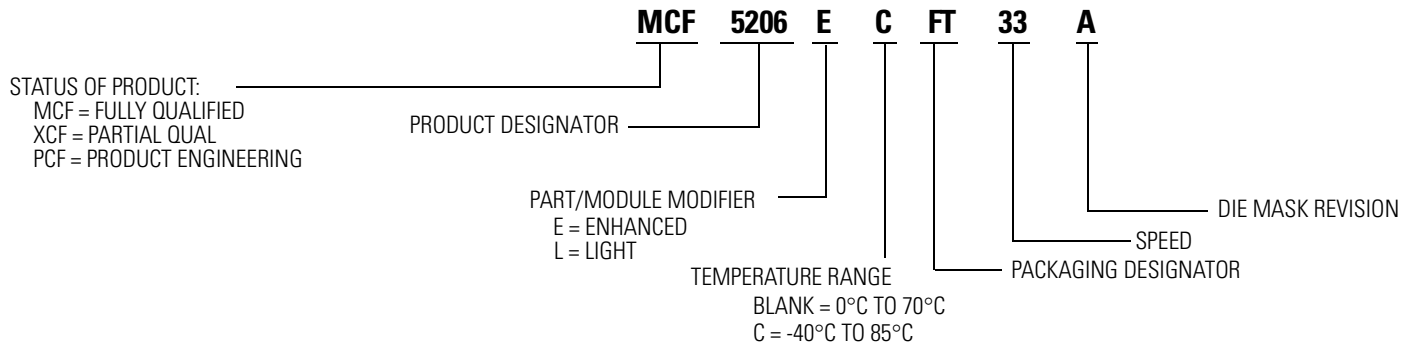
For complete part number information and temperature definitions, refer to "MCF5xxx FAMILY (continued)" on page SG1006-28.

| Product  | Core | Dhrys 2.1 MIPS @ max MHz | Processor Cache (Bytes)                               | Processor Flash (Kbytes) | Processor SRAM (Bytes) | Serial Interface, UART                                   | Timers/ CS/ GPIO                    | DMA   | DRAM Controller | 10/100 Eth/ USB1.1                                       | Operating Voltage (V) | Operating Frequency (MHz) | Temp Options      | Packaging  | Rev  | Additional Information   |
|----------|------|--------------------------|---|--------------------------|------------------------|--|-------------------------------------|-------|-----------------|--|-----------------------|---------------------------|-------------------|--|--|--|
| MCF5327  | V3   | 200                      | 16K Unified   | n/a                      | 32K                    | 3 UARTs  | 8/6/up to 94                        | 16-CH | DDR             | One USB 2.0 Full-SP Host<br>One USB 2.0 Full-SP Device   | 1.5, 3.3              | 240                       | -40 to +85 C      | 196-ball MAPBGA  | n/a  | 32x32 EMAC, QSPI, I <sup>2</sup> C.  |
| MCF5328  |      |                          |   |                          |                        |  |                                     |       |                 | 256-ball MAPBGA  |                       |                           |                   | Hardware Encryption, 32x32 EMAC, QSPI, I <sup>2</sup> C. |  |  |
| MCF5329  |      |                          |   |                          |                        |  |                                     |       |                 | 196-ball MAPBGA  |                       |                           |                   |  | 32x32 EMAC, QSPI, I <sup>2</sup> C.                      |  |
| MCF5372L |      |                          |   |                          |                        |  |                                     |       |                 |  |                       |                           |                   |  | Hardware Encryption, 32x32 EMAC, QSPI, I <sup>2</sup> C. |  |
| MCF5373L |      |                          |   |                          |                        |  |                                     |       |                 | Hardware Encryption, 32x32 EMAC, QSPI, I <sup>2</sup> C. |                       |                           |                   |  |  |  |
| MCF5372  |      | 150                      |   |                          | 180                    | 160-ball QFP   | 32x32 EMAC, QSPI, I <sup>2</sup> C. |       |                 |  |                       |                           |                   |  |  |  |
| MCF5373  |      |                          |   |                          |                        | Hardware Encryption, 32x32 EMAC, QSPI, I <sup>2</sup> C. |                                     |       |                 |  |                       |                           |                   |  |  |  |
| MCF5407  | V4   | 316                      | 16K I, 8K D   | n/a                      | 4K                     | UART, USART, I <sup>2</sup> C                            | 2/8/16                              | 4-CH  | SDRAM, FPM, EDO | n/a  | 1.8, 3.3              | 162, 220                  | C                 | 208-pin FOFP   | A  | Pin-compatible 5307 performance upgrade with MAC, HW divide, BDM, PLL, I <sup>2</sup> C, 3.3 V tolerant I/O. |
| MCF5470  | V4e  | 308                      | 32K I, 32K D  | n/a                      | 32K                    | 4 UARTs  | 6/6/up to 99                        | 16-CH | DDR/SDR         | Two 10/100, PCI  | 1.5, 2.5, 3.3         | 200                       | B                 | 388-ball TEPBGA  | n/a  | www.freescale.com  |
| MCF5471  |      |                          |   |                          |                        |  |                                     |       |                 | Crypto Enabled.  |                       |                           |                   |  |  |  |
| MCF5472  |      |                          |   |                          |                        |  |                                     |       |                 | www.freescale.com  |                       |                           |                   |  |  |  |
| MCF5473  |      |                          |   |                          |                        |  |                                     |       |                 | Crypto Enabled.  |                       |                           |                   |  |  |  |
| MCF5474  |      |                          |   |                          |                        |  |                                     |       |                 | www.freescale.com  |                       |                           |                   |  |  |  |
| MCF5475  |      | Crypto Enabled.          |   |                          |                        |  |                                     |       |                 |  |                       |                           |                   |  |  |  |
| MCF5480  |      | 410                      |   |                          |                        |  |                                     |       |                 | 266  |                       | Two 10/100, USB 2.0D, PCI | www.freescale.com |  |  |  |
| MCF5481  |      |                          |   |                          |                        |  |                                     |       |                 |  |                       | Crypto Enabled.           |                   |  |  |  |
| MCF5482  |      |                          |   |                          |                        |  |                                     |       |                 |  |                       | www.freescale.com         |                   |  |  |  |
| MCF5483  |      | 255                      |   |                          |                        |  |                                     |       |                 | 166  |                       | Two 10/100, Two CAN, PCI  | Crypto Enabled.   |  |  |  |
| MCF5484  |      |                          |   |                          |                        |  |                                     |       |                 |  |                       | www.freescale.com         |                   |  |  |  |
| MCF5485  |      |                          |   |                          |                        |  |                                     |       |                 |  |                       | Crypto Enabled.           |                   |  |  |  |
|          |      |                          |   |                          |                        |  |                                     |       |                 |  |                       | www.freescale.com         |                   |  |  |  |
| MCF5484  | 308  | 200                      | Two 10/100, USB 2.0D, Two CAN, PCI                    | www.freescale.com        |                        |  |                                     |       |                 |  |                       |                           |                   |  |  |  |
| MCF5485  |      |                          | Crypto Enabled. Contact Freescale for product status. |                          |                        |  |                                     |       |                 |  |                       |                           |                   |  |  |  |

Note: Extended temperature products with minimum order requirements. All temperature/speed combinations may not be valid. Consult the factory to verify.

A change bar appears in the left margin to mark the location of new or revised information.

**MCF5xxx FAMILY (continued)**  
**Product Numbering System for MCF5xxx Family**



## 56800 FAMILY

### 56F800 Series General Purpose 16-bit Fixed Point *Note*

| Product  | Performance       | Program ROM/RAM/Flash  | Data ROM/RAM/Flash  | Peripherals  | Packaging                     | Additional Information   |
|--|-------------------|--|---|--|-------------------------------|--|
| DSP56F801FA80<br>DSP56F801FA80E  | 80 MHz<br>40 MIPS | n/a/1K/8K<br>(words)   | n/a/1K/2K<br>(words)  | SCI, SPI, ADC, PWM,<br>Quad Timer  | 48-pin LQFP<br>48-pin LQFP*   | MCU-friendly instruction set, OnCE for debug, on-chip relaxation oscillator, 2K Boot Flash, up to 11 GPIO.       |
| DSP56F801FA60<br>DSP56F801FA60E  | 60 MHz<br>30 MIPS |  |   | MCU-friendly instruction set, OnCE for debug, on-chip relaxation oscillator, 2K Boot Flash, up to 11 GPIO. |                               |  |
| DSP56F802TA80<br>DSP56F802TA80E  | 80 MHz<br>40 MIPS |  |   | SCI, ADC, PWM, Quad Timer  | 32-pin LQFP<br>32-pin LQFP*   | MCU-friendly instruction set, OnCE for debug, on-chip relaxation oscillator, 2K Boot Flash, up to 4 GPIO.        |
| DSP56F802TA60<br>DSP56F802TA60E  | 60 MHz<br>30 MIPS |  |   | MCU-friendly instruction set, OnCE for debug, on-chip relaxation oscillator, 2K Boot Flash, up to 4 GPIO.  |                               |  |
| DSP56F803BU80<br>DSP56F803BU80E  | 80 MHz<br>40 MIPS | n/a/512K/32K<br>(words)  | n/a/2K/4K<br>(words)  | CAN, SCI, SPI, ADC, PWM,<br>Quadrature Decoder,<br>Quad Timer  | 100-pin LQFP<br>100-pin LQFP* | MCU-friendly instruction set, OnCE for debug, 2K Boot Flash, external memory expansion available, up to 16 GPIO. |
| DSP56F805FV80<br>DSP56F805FV80E  |                   |  |   |  | 144-pin LQFP<br>144-pin LQFP* | MCU-friendly instruction set, OnCE for debug, 2K Boot Flash, external memory expansion available, up to 32 GPIO. |
| DSP56F807PY80 (LQFP)<br>DSP56F807PY80E (LQFP)<br>DSP56F807VF80 (MAPBGA)<br>DSP56F807VF80E (MAPBGA) |                   | 160-pin LQFP<br>160-pin LQFP*<br>160-ball MAPBGA<br>160-ball MAPBGA* | MCU-friendly instruction set, OnCE for debug, 2K Boot Flash, external memory expansion available, up to 32 GPIO.<br>MOQ of 40 for LQFP. |  |                               |  |
| DSP56F826BU80<br>DSP56F826BU80E  |                   | n/a/512K/32K<br>(words)  | n/a/4K/2K<br>(words)  | SCI, SPI, SSI, TOD, Quad Timer   | 100-pin LQFP<br>100-pin LQFP* | MCU-friendly instruction set, OnCE for debug, 2K Boot Flash, external memory expansion available, up to 48 GPIO. |
| DSP56F827FG80<br>DSP56F827FG80E  |                   | n/a/1K/64K<br>(words)  | n/a/4K/4K<br>(words)  | SCI, SPI, SSI, TOD, ADC,<br>Quad Timer   | 128-pin LQFP<br>128-pin LQFP* | MCU-friendly instruction set, OnCE for debug, external memory expansion available, up to 52 GPIO.                |

Note: Contact your local Freescale Semiconductor Sales Office or authorized Freescale Semiconductor distributor for product availability.

\*This package is RoHS compliant.

## 56800E FAMILY

### 56800E FAMILY

#### 56850 Series General Purpose 16-bit Fixed Point *Note*

| Product  | Performance         | Boot ROM/<br>Program RAM<br>Data RAM | Off-Chip<br>Memory<br>Expansion<br>(EMI) | Peripherals  | Packaging  | Additional Information   |
|--|---------------------|--------------------------------------|--|--|--|--|
| DSP56852VF120<br>DSP56852VFE   | 120 MHz<br>120 MIPS | 1K/6K/4K<br>(words)                  | Up to 2M<br>program and<br>6M of data    | SCI, SPI, ISSI, EMI,<br>COP, Quad Timer                    | 81-ball MAPBGA<br>81-ball MAPBGA*                            | MCU-friendly instruction set, Enhanced OnCE for debug, up to four programmable chip select signals, and up to 11 GPIO.   |
| DSP56853FG120<br>DSP56853FGE   |                     | 1K/12K/4K<br>(words)                 | Up to 2M<br>program and<br>8M of data    | 2 SCI, SPI, ESSI, HI, EMI,<br>COP, DMA, TOD,<br>Quad Timer | 128-pin LQFP<br>128-pin LQFP*                                | MCU-friendly instruction set, Enhanced OnCE for debug, six channels of DMA, up to four programmable chip select signals, and up to 41 GPIO.                                |
| DSP56854FG120<br>DSP56854FGE   |                     | 1K/16K/16K<br>(words)                |  |  |  | MCU-friendly instruction set, Enhanced OnCE for debug, six channels of DMA, up to four programmable chip select signals, and up to 41 GPIO.                                |
| DSP56855BU120<br>DSP56855BUE   |                     | 1K/24K/24K<br>(words)                | n/a                                      | 2 SCI, ESSI, EMI, COP,<br>DMA, TOD, Quad Timer             | 100-pin LQFP<br>100-pin LQFP*                                | MCU-friendly instruction set, Enhanced OnCE for debug, six channels of DMA, on-chip relaxation oscillator, up to four programmable chip select signals, and up to 18 GPIO. |
| DSP56857BU120<br>DSP56857BUE   |                     | 1K/40K/24K<br>(words)                |  |  |  | MCU-friendly instruction set, Enhanced OnCE for debug, six channels of DMA, and up to 47 GPIO.   |
| DSP56858FV120 (LQFP)<br>DSP56858FVE (LQFP)<br>DSP56858VF120 (MAPBGA) |                     |                                      |  | Up to 2M<br>program and<br>8M of data                      | 2 SCI, SPI, 2 ESSI, HI, EMI,<br>COP, DMA, TOD,<br>Quad Timer | 144-pin LQFP<br>144-pin LQFP*<br>144-ball MAPBGA   |

Note: Contact your local Freescale Semiconductor Sales Office or authorized Freescale Semiconductor distributor for product availability.

\*This package is RoHS compliant.

56800E FAMILY (continued)

56F8300 Series General Purpose 16-bit Fixed Point *Note*

| Product  | Performance       | Flash/RAM (KB) | Off-Chip Memory Expansion (EMI) | Peripherals  | Packaging  | Additional Information   |
|--|-------------------|----------------|---------------------------------|--|--|--|
| <b>F832x Family</b>  |                   |                |                                 |  |  |  |
| MC56F8322MFA60<br>MC56F8322MFAE  | 60 MHz<br>60 MIPS | 48/12          | n/a                             | 2 SPI, 2 SCI, 2 ADC, PWM, COP, PLL, Decoder, 2 Quad Timers, FlexCAN      | 48-pin LQFP<br>48-pin LQFP*                      | Extended (-40°C to 125°C) MCU-friendly instruction set, Enhanced OnCE for debug, on-chip relaxation oscillator, temperature sensor, and up to 21 GPIOs.  |
| MC56F8322VFA60<br>MC56F8322VFAE  |                   |                |                                 |  | 48-pin LQFP<br>48-pin LQFP*                      | Industrial (-40°C to 105°C), MCU-friendly instruction set, Enhanced OnCE for debug, on-chip relaxation oscillator, temperature sensor and up to 21 GPIOs.  |
| MC56F8323MFB60<br>MC56F8323MFB60                                       |                   |                |                                 |  | 64-pin LQFP<br>64-pin LQFP*                      | Extended (-40°C to 125°C), MCU-friendly instruction set, Enhanced OnCE for debug, on-chip relaxation oscillator, temperature sensor, and up to 27 GPIOs.   |
| MC56F8323VFB60<br>MC56F8323VFB60                                       |                   |                |                                 |  | 64-pin LQFP<br>64-pin LQFP*                      | Industrial (-40°C to 105°C), MCU-friendly instruction set, Enhanced OnCE for debug, on-chip relaxation oscillator, temperature sensor, and up to 27 GPIOs.   |
| <b>F833x Family</b>  |                   |                |                                 |  |  |  |
| MC56F8335VFG60<br>MC56F8335MFG60                                       | 60 MHz<br>60 MIPS | 80/12          | n/a                             | 2 SPI, 2 SCI, 4ADC, PWM, COP, PLL, 2 Decoders, 4 Quad Timers, FlexCAN    | 128-pin LQFP*                                    | Industrial (-40°C to 105°C), MCU-friendly instruction set, Enhanced OnCE for debug, temperature sensor, and up to 49 GPIOs.<br>Industrial (-40°C to 105°C), MCU-friendly instruction set, Enhanced OnCE for debug, temperature sensor, and up to 49 GPIOs. |
| <b>F834x Family</b>  |                   |                |                                 |  |  |  |
| MC56F8345MFG60<br>MC56F8345MFG60                                       | 60 MHz<br>60 MIPS | 144/12         | n/a                             | 2 SPI, 2 SCI, 4 ADC, 2 PWM, COP, PLL, 2 Decoders, 4 Quad Timers, FlexCAN | 128-pin LQFP<br>128-pin LQFP*                    | Extended (-40°C to 125°C), MCU-friendly instruction set, Enhanced OnCE for debug, temperature sensor, and up to 49 GPIOs.  |
| MC56F8345VFG60<br>MC56F8345VFG60                                       |                   |                |                                 |  | 128-pin LQFP<br>128-pin LQFP*                    | Industrial (-40°C to 105°C), MCU-friendly instruction set, Enhanced OnCE for debug, temperature sensor, and up to 49 GPIOs.  |
| MC56F8346MVF60<br>MC56F8346MVF60                                       |                   |                | Yes                             |  | 144-pin LQFP<br>144-pin LQFP*                    | Extended (-40°C to 125°C), MCU-friendly instruction set, Enhanced OnCE for debug, temperature sensor, and up to 62 GPIOs.  |
| MC56F8346VVF60<br>MC56F8346VVF60                                       |                   |                |                                 |  | 144-pin LQFP<br>144-pin LQFP*                    | Industrial (-40°C to 105°C), MCU-friendly instruction set, Enhanced OnCE for debug, temperature sensor, and up to 62 GPIOs.  |
| MC56F8347MPY60<br>MC56F8347MPY60                                       |                   |                |                                 |  | 160-pin LQFP<br>160-pin LQFP*<br>160-pin MAPBGA* | Extended (-40°C to 125°C), MCU-friendly instruction set, Enhanced OnCE for debug, temperature sensor, and up to 76 GPIOs.  |
| MC56F8347VPY60 (LQFP)<br>MC56F8347VPYE (LQFP)<br>MC56F8347VVE (MAPBGA) |                   |                |                                 |  | 160-pin LQFP<br>160-pin LQFP*<br>160-pin MAPBGA* | Industrial (-40°C to 105°C), MCU-friendly instruction set, Enhanced OnCE for debug, temperature sensor, and up to 76 GPIOs.  |

Note: Contact your local Freescale Semiconductor Sales Office or authorized Freescale Semiconductor distributor for product availability.

\*This package is RoHS compliant.

## 56800E FAMILY (continued)

56F8300 Series General Purpose 16-bit Fixed Point<sup>Note</sup> (continued)

| Product  | Performance       | Flash/RAM (KB) | Off-Chip Memory Expansion (EMI) | Peripherals   | Packaging   | Additional Information  |
|--|-------------------|----------------|---------------------------------|---|---|---|
| <b>F835x Family</b>  |                   |                |                                 |   |   |   |
| MC56F8355MFG60<br>MC56F8355MFGE  | 60 MHz<br>60 MIPS | 280/20         | Yes                             | 2 SPI, 2 SCI, 4 ADC,<br>2 PWM, COP, PLL,<br>2 Decoders, 4 Quad Timers,<br>FlexCAN   | 128-pin LQFP<br>128-pin LQFP*                     | Extended (-40°C to 125°C), MCU-friendly instruction set, Enhanced OnCE for debug, temperature sensor, and up to 49 GPIOs.   |
| MC56F8355VFG60<br>MC56F8355VFGE  |                   |                | n/a                             |   | 128-pin LQFP<br>128-pin LQFP*                     | Industrial (-40°C to 105°C), MCU-friendly instruction set, Enhanced OnCE for debug, temperature sensor, and up to 49 GPIOs. |
| MC56F8356MFV60<br>MC56F8356MFVE  |                   |                | Yes                             |   | 144-pin LQFP<br>144-pin LQFP*                     | Extended (-40°C to 125°C), MCU-friendly instruction set, Enhanced OnCE for debug, temperature sensor, and up to 62 GPIOs.   |
| MC56F8356VFPV60<br>MC56F8356VFPVE                                      |                   |                | Yes                             |   | 144-pin LQFP<br>144-pin LQFP*                     | Industrial (-40°C to 105°C), MCU-friendly instruction set, Enhanced OnCE for debug, temperature sensor, and up to 62 GPIOs. |
| MC56F8357MPY60<br>MC56F8357MPYE  |                   |                | Yes                             |   | 160-pin LQFP<br>160-pin LQFP*                     | Extended (-40°C to 125°C), MCU-friendly instruction set, Enhanced OnCE for debug, temperature sensor, and up to 76 GPIOs.   |
| MC56F8357VPY60 (LQFP)<br>MC56F8357VPYE (LQFP)<br>MC56F8357VVE (MAPBGA) |                   |                | Yes                             |   | 160-pin LQFP<br>160-pin LQFP*<br>160-pin MAPBGA*  | Industrial (-40°C to 105°C), MCU-friendly instruction set, Enhanced OnCE for debug, temperature sensor, and up to 76 GPIOs. |
| <b>F836x Family</b>  |                   |                |                                 |   |   |   |
| MC56F8365VFG60<br>MC56F8365VFGE  | 60 MHz<br>60 MIPS | 576/36         | n/a                             | 2 SPI, 2 SCI, 4 ADC,<br>2 PWM, COP, PLL,<br>2 Decoders, 4 Quad Timers,<br>2 FlexCAN | 128-pin LQFP<br>128-pin LQFP*                     | Industrial (-40°C to 105°C), MCU-friendly instruction set, Enhanced OnCE for debug, temperature sensor, and up to 49 GPIOs. |
| MC56F8365MFG60<br>MC56F8365MFGE  |                   |                | n/a                             |   | 128-pin LQFP<br>128-pin LQFP*                     | Extended (-40°C to 125°C), MCU-friendly instruction set, Enhanced OnCE for debug, temperature sensor, and up to 49 GPIOs.   |
| MC56F8366VFPV60<br>MC56F8366VFPVE                                      |                   |                | Yes                             |   | 144-pin LQFP<br>144-pin LQFP*                     | Industrial (-40°C to 105°C), MCU-friendly instruction set, Enhanced OnCE for debug, temperature sensor, and up to 62 GPIOs. |
| MC56F8366MFV60<br>MC56F8366MFVE  |                   |                | Yes                             |   | 144-pin LQFP<br>144-pin LQFP*                     | Extended (-40°C to 125°C), MCU-friendly instruction set, Enhanced OnCE for debug, temperature sensor, and up to 62 GPIOs.   |
| MC56F8367VPY60 (LQFP)<br>MC56F8367VPYE (LQFP)<br>MC56F8367VVE (MAPBGA) |                   |                | Yes                             |   | 160-pin LQFP<br>160-pin LQFP*<br>160-ball MAPBGA* | Industrial (-40°C to 105°C), MCU-friendly instruction set, Enhanced OnCE for debug, temperature sensor, and up to 76 GPIOs. |
| MC56F8367MPY60<br>MC56F8367MPYE (LQFP)                                 |                   |                | Yes                             |   | 160-pin LQFP<br>160-pin LQFP*                     | Extended (-40°C to 125°C), MCU-friendly instruction set, Enhanced OnCE for debug, temperature sensor, and up to 76 GPIOs.   |

Note: Contact your local Freescale Semiconductor Sales Office or authorized Freescale Semiconductor distributor for product availability.

\*This package is RoHS compliant.



56800E FAMILY (continued)

**56F8000 Series General Purpose 16-bit Fixed Point** *Note*

| Product  | Performance       | Flash/RAM (KB)  | Peripherals  | Packaging   | Additional Information  |
|--|-------------------|---|--|---|---|
| MC56F8013VFAE  | 32 MHz<br>32 MIPS | 16/4  | 6-CH PWM, Quad Timer, SPI, SCI with LIN slave, PLL, dual 3-CH, 12-bit ADCs, COP, POR, I <sup>2</sup> C, On-Chip oscillator | 32-pin LQFP   | Industrial (-40°C to 105°C), MCU-friendly instruction set, Enhanced OnCE for debug, temperature sensor, and up to 26 GPIOs. |
| MC56F8014VFAE  |                   |   | 5-CH PWM, Quad Timer, SPI, SCI with LIN slave, PLL, dual 4-CH, 12-bit ADCs, COP, POR, I <sup>2</sup> C, On-Chip oscillator |   | Industrial (-40°C to 105°C), MCU-friendly instruction set, Enhanced OnCE for debug, temperature sensor, and up to 26 GPIOs. |
| Applications   |                   | Development Tools                                       |  | Benefits  |   |
| <ul style="list-style-type: none"> <li>• Smart sensors</li> <li>• Industrial motor control</li> <li>• Dimming lamp ballast</li> <li>• Switched-mode power supply</li> <li>• Soft-switching PFC</li> <li>• Appliance motor control</li> <li>• DC-DC power supplies</li> </ul> |                   | Refer to Development Tools beginning on page SG1004-23. |  | Because of its low cost, configuration flexibility, and compact program code, the 56F8013 is well suited for many applications. The 56800E core is based on a Harvard architecture consisting of three execution units operating in parallel, allowing as many as six operations per instruction cycle. The microprocessor-style programming model and optimized instruction set allow straightforward generation of efficient, compact code for both DSP and MCU applications. |   |

Note: Contact your local Freescale Semiconductor Sales Office or authorized Freescale Semiconductor distributor for product availability.

## 56800E FAMILY

### 56800E FAMILY (continued)

### 56F8100 Series General Purpose 16-Bit Fixed Point <sup>Note</sup>

| Product                       | Performance       | Flash/RAM (KB)                | Off-Chip Memory Expansion (EMI) | Peripherals  | Packaging  | Additional Information   |
|-------------------------------|-------------------|-------------------------------|---------------------------------|--|--|--|
| MC56F8122VFA<br>MC56F8122VFAE | 40 MHz<br>40 MIPS | 40/8                          | n/a                             | 2 SPI, 2 SCI, 2 ADC, COP, PLL,<br>Quad Timer   | 48-pin LQFP<br>48-pin LQFP*  | Industrial (-40°C to 105°C), MCU-friendly instruction set, Enhanced OnCE for debug, on-chip relaxation oscillator, and up to 21 GPIOs. |
| MC56F8123VFB<br>MC56F8123VFBE |                   |                               |                                 |  | 64-pin LQFP<br>64-pin LQFP*  | Industrial (-40°C to 105°C), MCU-friendly instruction set, Enhanced OnCE for debug, on-chip relaxation oscillator, and up to 27 GPIOs. |
| MC56F8135VFG                  |                   | 72/8                          |                                 |  | 128-pin LQFP*  | Industrial (-40°C to 105°C), MCU-friendly instruction set, Enhanced OnCE for debug, and up to 49 GPIOs.                                |
| MC56F8145VFG<br>MC56F8145VFG  |                   | 136/8                         |                                 |  |  | 128-pin LQFP<br>128-pin LQFP*  |
| MC56F8146VFB<br>MC56F8146VFE  |                   |                               | Yes                             | 144-pin LQFP<br>144-pin LQFP*  | Industrial (-40°C to 105°C), MCU-friendly instruction set, Enhanced OnCE for debug and up to 62 GPIOs. |  |
| MC56F8147VPY<br>MC56F8147VPYE |                   | n/a                           |                                 | 160-pin LQFP<br>160-pin LQFP*  | Industrial (-40°C to 105°C), MCU-friendly instruction set, Enhanced OnCE for debug and up to 76 GPIOs. |  |
| MC56F8155VFG<br>MC56F8155VFG  |                   |                               | Yes                             | 2 SPI, 2 SCI, 4ADC, PWM,<br>COP, PLL, Decoder, 2 Quad<br>Timers  | 128-pin LQFP<br>128-pin LQFP*  | Industrial (-40°C to 105°C), MCU-friendly instruction set, Enhanced OnCE for debug and up to 49 GPIOs.                                 |
| MC56F8156VFB<br>MC56F8156VFE  |                   | 144-pin LQFP<br>144-pin LQFP* |                                 |  | Industrial (-40°C to 105°C), MCU-friendly instruction set, Enhanced OnCE for debug and up to 62 GPIOs. |  |
| MC56F8157VPY<br>MC56F8157VPYE |                   | n/a                           | 160-pin LQFP<br>160-pin LQFP*   | Industrial (-40°C to 105°C), MCU-friendly instruction set, Enhanced OnCE for debug and up to 76 GPIOs. |  |  |
| MC56F8165VFG<br>MC56F8165VFG  |                   |                               |                                 | 128-pin LQFP<br>128-pin LQFP*  | Industrial (-40°C to 105°C), MCU-friendly instruction set, Enhanced OnCE for debug and up to 49 GPIOs. |  |
| MC56F8166VFB<br>MC56F8166VFE  |                   | Yes                           | 544/32                          |  | 144-pin LQFP<br>144-pin LQFP*  | Industrial (-40°C to 105°C), MCU-friendly instruction set, Enhanced OnCE for debug and up to 62 GPIOs.                                 |
| MC56F8167VPY<br>MC56F8167VPYE |                   |                               |                                 | 160-pin LQFP<br>160-pin LQFP*  | Industrial (-40°C to 105°C), MCU-friendly instruction set, Enhanced OnCE for debug and up to 76 GPIOs. |  |

Note: Contact your local Freescale Semiconductor Sales Office or authorized Freescale Semiconductor distributor for product availability.

\*This package is RoHS compliant.

## 68HC16 FAMILY

### 68HC16 Product Table

| Product    | ROM (KB) | RAM (KB) | Flash (KB) | Product Integration | Timer | Serial          | Analog      | Operating Voltage (V) | Operating Frequency (MHz) | Temp Options | Packaging                    | Flash | Status    | Additional Information   | Documentation |
|------------|----------|----------|------------|---------------------|-------|-----------------|-------------|-----------------------|---------------------------|--------------|------------------------------|-------|-----------|--|---------------|
| MC68HC16Z1 | 0        | 1        | 0          | SIM                 | GPT   | SCI, queued SPI | 8-CH 10-bit | 5.0<br>2.7 to 3.6     | 16, 20, 25                | C, V, M      | 132-pin PQFP<br>144-pin LQFP | n/a   | Available | 2.7 V to 3.6 V, 16 MHz version<br>MC68CK16Z1 with 32kHz crystal in 144-pin LQFP package only;<br>MC68CM16Z1 with 4MHz crystal in 144-pin LQFP package only | MC68HC16ZUM   |
| MC68HC16Z3 | 8        | 4        |            |                     |       |                 |             | 5.0                   | 16, 25                    | C, V         |                              |       |           | www.freescale.com  |               |

Note: All package, speed, and temperature combinations may not be valid. Consult factory to verify.

### 68HC16 Reference Manuals

CPU16RM, HC16 CPU Reference Manual

SIMRM, System Integration Module Reference Manual

TPURM, Timer Processor Unit Reference Manual

GPTRM, General-Purpose Timer Reference Manual

QSMRM, Queued Serial Module Reference Manual

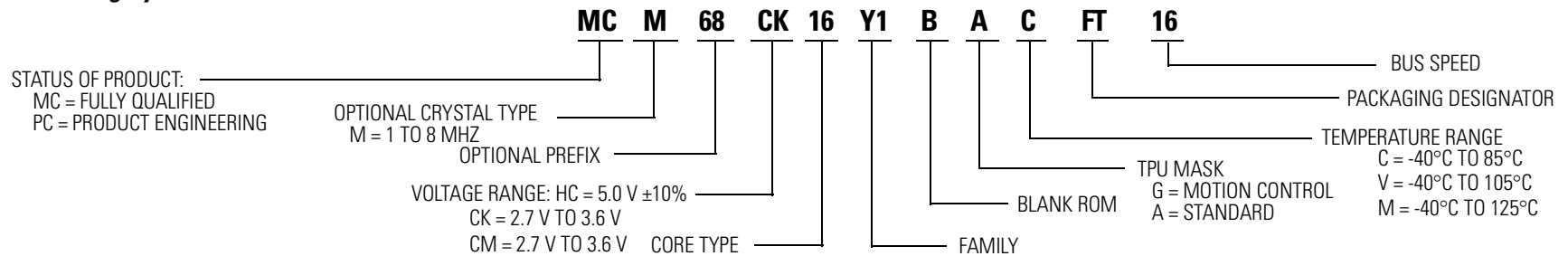
ADCRM, Analog-to-Digital Converter Reference Manual

CTMRM, Configurable Timer Module Reference Manual

MCCIRM, Multi-Channel Communication Interface Reference Manual

SCIMRM, Single-Chip Integration Module Reference Manual

### Product Numbering System for 68HC16



**683xx FAMILY**

**683xx Product Table**

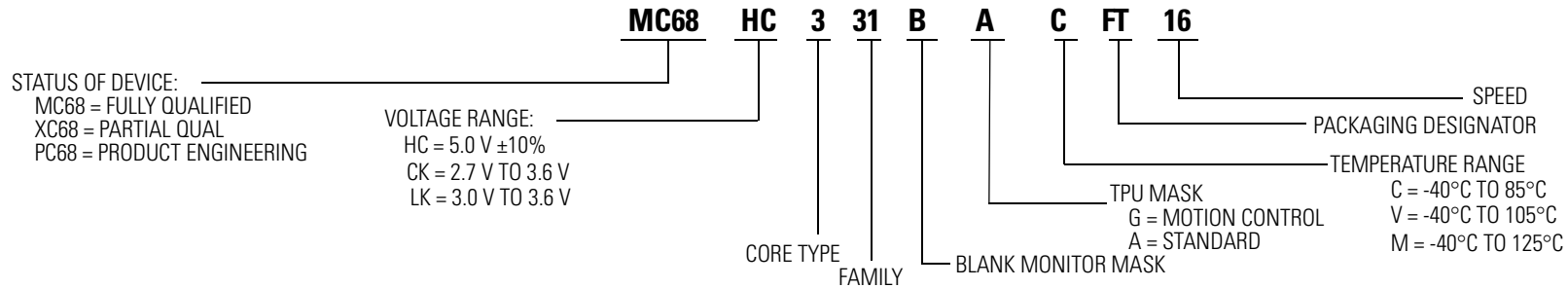
| Product | ROM (KB) | RAM (KB) | Flash (KB) | Device Integration | Timer                                      | Serial          | A/D                     | Operating Voltage (V) | Operating Frequency (MHz) | Temp Options | Packaging                    | Status    | Additional Information   | Documentation                  |
|---------|----------|----------|------------|--------------------|--|-----------------|-------------------------|-----------------------|---------------------------|--------------|------------------------------|-----------|--|--------------------------------|
| MC68331 | 0        | 0        | 0          | SIM                | GPT  | SCI, queued SPI | n/a                     | 5.0                   | 16, 20, 25                | C, V, M      | 132-pin PQFP<br>144-pin LQFP | Available | 2.7 V to 3.6 V, 16 MHz version (MC68CK331).<br>MC68CK331 is on end of life | MC68331UM<br>MC68CK331EC16     |
| MC68332 |          | 2        |            |                    | 3.0 V to 3.6 V, 16 MHz version (MC68LK332) |                 |                         |                       |                           |              |                              |           | MC68332UM<br>MC68LK332EC16   |                                |
| MC68336 |          | 4 + 3.5  |            |                    | Queued<br>16-CH<br>10-bit                  | TPU<br>CTM4     | CAN, SCI,<br>queued SPI |                       | 20, 25                    |              | 160-pin QFP                  |           | www.freescale.com  | MC68336/376PP<br>MC68336/376UM |
| MC68376 | 8        |          |            |                    |  |                 |                         |                       |                           |              |                              |           |  |                                |

Note: All package, speed, and temperature combinations may not be valid. Consult factory to verify.

**683xx Reference Manuals**

- CPU32RM, CPU32 Reference Manual
- SIMRM, System Integration Module Reference Manual
- TPURM, Timer Processor Unit Reference Manual
- GPTRM, General-Purpose Timer Reference Manual
- QSMRM, Queued Serial Module Reference Manual
- ADCRM, Analog-to-Digital Converter Reference Manual
- CTMRM, Configurable Timer Module Reference Manual

**Product Numbering System for 683xx Family**

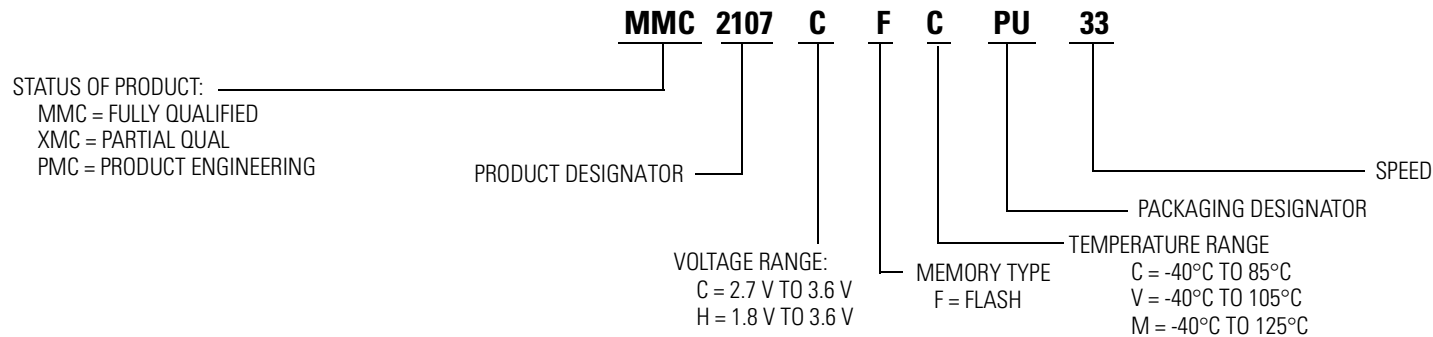


## MMC2100 FAMILY

### MMC2100 Product Table

| Product | ROM (KB) | RAM (KB) | Flash (KB) | Timer   | PWM         | Serial                 | A/D                | Operating Voltage (V)                           | Operating Frequency (MHz) | Temp Options | Packaging  | Status            | Additional Information  | Documentation        |
|---------|----------|----------|------------|---|-------------|------------------------|--------------------|---|---------------------------|--------------|--|-------------------|---|----------------------|
| MMC2001 | 256      | 32       | 0          | Time-of-day, periodic interrupt timer, COP                      | 6-CH 10-bit | Dual UART Interval SPI | n/a                | 1.8 to 3.6                                      | 33                        | C            | 144-pin LQFP   | Samples Available | ROM includes debugger, peripheral product drivers, and a monitor; external bus interface with 22 address/16 data and 4 chip selects, OnCE debug module, KBI (16 pins).<br>Sample part number: KMMC2001HCPV33B | MMC2001RM<br>MCOEREM |
| MMC2107 | 0        | 8        | 128        | Dual 4-channel 16-bit capture/compare, PWM capability, watchdog | See Timer   | Dual SCI, SPI          | Queued 8-CH 10-bit | 2.7 to 3.6                                      |                           |              | 100-pin LQFP<br>144-pin LQFP   | Available         | PLL clock, 32 source interrupt controller, periodic interrupt timer, external bus interface with 23 address, 16/32 data and 4 chip select lines, OnCE debug module.   | MMC2107<br>MCOEREM   |
| MMC2113 |          | 32       | 256        |   |             |                        |                    | 100-pin LQFP<br>144-pin LQFP<br>196-ball MAPBGA |                           |              | PLL clock, 32 source interrupt controller, periodic interrupt timer, external bus interface with 23 address, 16/32 data and 4 chip select lines, OnCE debug module, Offers Flash Security<br>This product incorporates SuperFlash® technology licensed From SST. |                   | MMC2114   |                      |
| MMC2114 |          |          |            |   |             |                        |                    |   |                           |              |  |                   |   |                      |

### Product Numbering System for MMC2100



**MPC500 FAMILY**

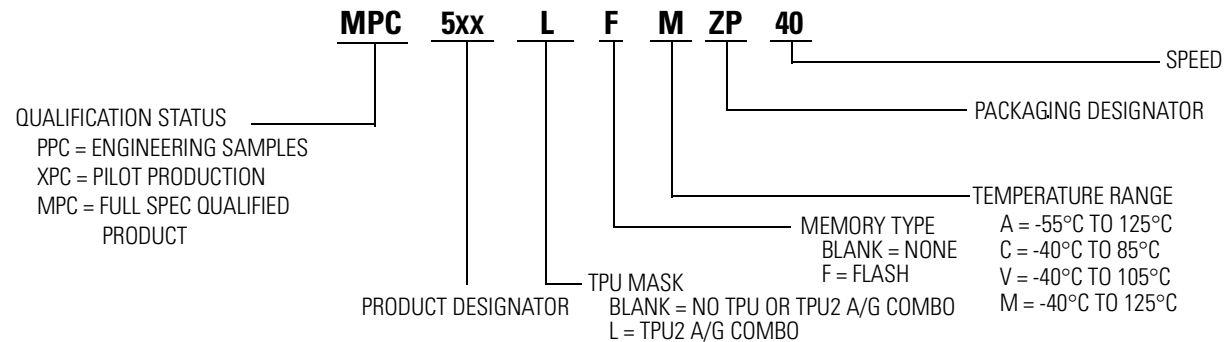
**MPC500 Product Table** *Note*

For complete part number information and temperature definitions, refer to “Product Numbering System for MPC500” on page SG1006-38.

| Product | ROM (KB) | RAM (KB)                       | Flash (KB) | Product Integration | Timer                                   | Serial                                   | MUX                                   | A/D  | PWM  | Operating Voltage                   | Operating Frequency (MHz)                 | Temp Options  | Packaging         | Status    | Additional Information | Documentation           |                       |
|---------|----------|--------------------------------|------------|---------------------|---|--|---------------------------------------|--|--|-------------------------------------|---|---------------|-------------------|-----------|------------------------|-------------------------|-----------------------|
| MPC533  | 0        | 32                             | 512        | USIU                | 22-channel timer system; MIOS14         | QSMCM (2 SCI + QSPI) +1 TouCAN           | 1 x TouCAN                            | 1 QADC (10-bit A/D with 64 result registers) 32 channels on chip | 12 x PWM   | 2.6, 5.0                            | 40  | C             | 388-ball PBGA     | Available | www.freescale.com      | MPC533UM MPC533PB       |                       |
| MPC534  |          |                                |            |                     |   |  |                                       | Offers code compression  |  |                                     |   |               |                   |           |                        |                         |                       |
| MPC535  |          | 40                             | 1M         |                     |   |  |                                       | 1 QADC (10-bit A/D with 64 result registers) 40 channels on chip |  |                                     |   |               |                   |           | www.freescale.com      | MPC535UM MPC535PB       |                       |
| MPC536  |          |                                |            |                     |   |  |                                       | Offers code compression  |  |                                     |   |               |                   |           |                        |                         |                       |
| MPC555  |          | 26 + 6 for TPU                 | 448        |                     | 50-channel timer system; 2 TPU3 + MIOS1 | QSMCM (2 SCI + QSPI) + 2 TouCAN          | 2 x TouCAN                            | 2 QADC (10-bit A/D with 64 result register) 32 channels on chip  | 8 x PWM  | 3.3 Vdc for core, 5.0 Vdc for Flash | A, C, M                                   | 272-ball PBGA | www.freescale.com |           | MPC555UM TPURM RCPURM  |                         |                       |
| MPC561  |          | 32 + 8 for TPU + 2 for DECRAM  | 0          |                     | 512                                     | 54-channel timer system; 2 TPU3 + MIOS14 | QSMCM (2 SCI + 1 QSPI) + 3 TouCAN     | 3 x TouCAN   | 2 QADC (10-bit A/D with 64 result register) 32 channels on chip  | 12 x PWM                            | 2.6 Vdc for core, 5.0 Vdc for A/D and I/O | 40, 56, 66    | C, M              |           | 388-ball PBGA          | Offers code compression | MPC561RM TPURM RCPURM |
| MPC562  |          |                                |            |                     |   |  |                                       |  |  |                                     |   |               |                   |           |                        | www.freescale.com       | MPC563RM TPURM RCPURM |
| MPC563  |          | 36 + 10 for TPU + 4 for DECRAM | 1M         |                     | 1M                                      | 70-channel timer system; 3 TPU3 + MIOS14 | QSMCM x 2 (4 SCI + 2 QSPI) + 3 TouCAN | 3 x TouCAN 1 x J1850   | 2 QADC (10-bit A/D with 64 result registers) 40 channels on chip | 12 x PWM                            | 2.6 Vdc for core, 5.0 Vdc for A/D and I/O | 40 or 56      | A, C, M           |           | 388-ball PBGA          | www.freescale.com       | MPC566UM TPURM RCPURM |
| MPC564  |          |                                |            |                     |   |  |                                       |  |  |                                     |   |               |                   |           |                        | Offers code compression |                       |
| MPC565  |          |                                |            |                     |   |  |                                       |  |  |                                     |   |               |                   |           |                        | Offers code compression |                       |
| MPC566  |          |                                |            |                     |   |  |                                       |  |  |                                     |   |               |                   |           |                        | Offers code compression |                       |

Note: Extended temperature products with minimum order requirements. All package/speed/temperature combinations may not be valid - consult factory to verify.

**Product Numbering System for MPC500**



## MPC5500 FAMILY

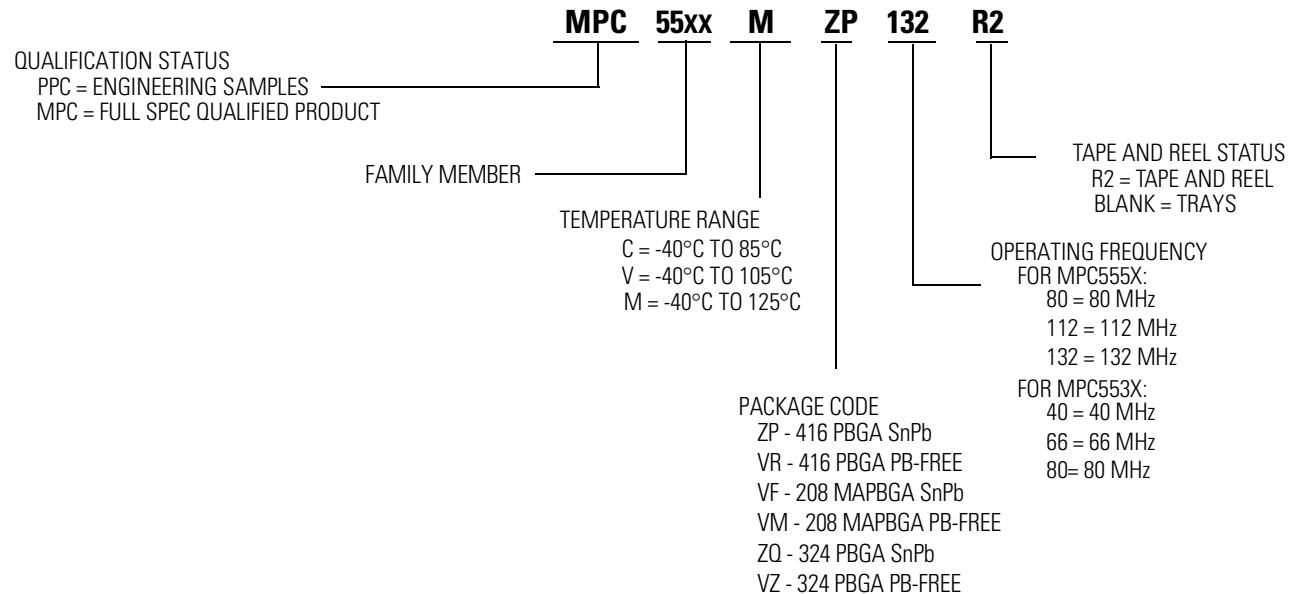
### MPC5500 Family Comparison

| Device  | PowerPC Core | Variable Length Instruction Support | Cache       | Memory Mngt Unit | Crossbar | Core Nexus        | SRAM | FLASH Main Array  | External Bus (EBI) Data Bus | External Bus (EBI) Address Bus | DMA   | DMA Nexus | Serial | Controller Area Network (CAN) | SPI | eMIOS | eTPU  | Code Memory | Parameter RAM | Nexus Class | Interrupt Controller | Analog to Digital Converter (eQADC) | Fast Ethernet Controller | PLL | VRC |
|---------|--------------|-------------------------------------|-------------|------------------|----------|-------------------|------|-------------------|-----------------------------|--------------------------------|-------|-----------|--------|-------------------------------|-----|-------|-------|-------------|---------------|-------------|----------------------|-------------------------------------|--------------------------|-----|-----|
| MPC5534 | e200z3       | Yes                                 | None        | 16 entry         | 4x5      | Class 3 + (NZ3C3) | 64k  | 1M <sup>1</sup>   | 16-bit                      | 24                             | 32-CH | None      | 2      | 2 (64 buf)                    | 3   | 24-CH | 32-CH | 12K         | 2.5K          | 3           | 210-CH               | 40-CH                               | No                       | FM  | Yes |
| MPC5553 | e200z6       | No                                  | 8K Unified  | 32 entry         | 4x4      | Class 3+ (NZ3C3)  | 64k  | 1.5M <sup>2</sup> | 16-bit                      | 24                             | 32-CH | Class 3   | 2      | 2 (64 buf)                    | 2   | 24-CH | 32-CH | 12K         | 2.5K          | 3           | 210-CH               | 40-CH                               | Yes <sup>3</sup>         | FM  | Yes |
| MPC5554 | e200z6       | No                                  | 32K Unified | 32 entry         | 4x5      | Class 3+ (NZ3C3)  | 64k  | 2M <sup>2</sup>   | 32-bit                      | 24                             | 64-CH | Class 3   | 2      | 3 (64 buf)                    | 3   | 24-CH | 64-CH | 16K         | 3K            | 3           | 300-CH               | 40-CH                               | No                       | FM  | Yes |

Notes:

1. 16-Byte flash page size for programming
2. 32-Byte flash page size for programming.
3. The FEC signals are shared with Data Bus pins DATA[16:31].

### Product Numbering System for the MPC5500 Family



A change bar appears in the left margin to mark the location of new or revised information.

## CAN MCUs

### CONTROLLER AREA NETWORK MICROCONTROLLERS

#### 68HC08 Family CAN MCUs

| Product       | ROM (KB) | RAM (KB) | Flash or OTP (KB) | EEPROM (Bytes) | Timer                              | I/O                                 | Serial            | A/D                  | PWM          | COP | Operating Voltage (V) | Max Bus Frequency (MHz) | Temp <sup>1</sup> | Packaging   | OTP or Flash Equiv. | Status    | Additional Information  | Documentation    |
|---------------|----------|----------|-------------------|----------------|------------------------------------|-------------------------------------|-------------------|----------------------|--------------|-----|-----------------------|-------------------------|-------------------|---|---------------------|-----------|---|------------------|
| XC68HC08AZ32  | 32       | 1        | n/a               | 512            | 4-CH + 2-CH, 16-bit IC, OC, or PWM | 40/50                               | SCI<br>SPI<br>CAN | 8-CH or 15-CH, 8-bit | See<br>Timer | Y   | 5.0                   | 8.4                     | C, V, M           | 64-pin QFP (FU)<br>52-pin PLCC (FN)                     | 908AZ60A            | Available | CAN 2.0A and 2.0B   | MC68HC08AZ32/D   |
| MC908AZ60A    | n/a      | 2        | 60 Flash          | 1K             | 6-CH + 2-CH, 16-bit IC, OC, or PWM | 50                                  |                   | 15-CH, 8-bit         |              |     |                       |                         |                   | 64-pin QFP (FU)   | n/a                 |           | MC908AZ60A is pin-for-pin compatible replacement for MC68HC908AZ60. CAN 2.0A and 2.0B | MC68HC908AZ60A/D |
| MC68HC08AZ60  | 60       |          | n/a               |                | 48                                 | 32-pin QFP (FJ)<br>48-pin LQFP (FA) |                   | 908AZ60              |              |     |                       |                         |                   | CAN 2.0A and 2.0B                                       | MC68HC08AZ60/D      |           |   |                  |
| MC68HC908GZ8  | n/a      | 1        | 8 Flash           | n/a            | Dual 2-CH, 16-bit IC, OC, or PWM   | Up to 37                            | ESCI<br>SPI       | 8-CH, 10-bit         | See<br>Timer | Y   | 3.0, 5.0              | 8.0                     | C, V, M           | 32-pin QFP (FJ)<br>48-pin LQFP (FA)                     | n/a                 | Available | MSCAN 2.0   | MC68HC908GZ16/D  |
| MC68HC908GZ16 |          |          | 16 Flash          |                |                                    |                                     |                   |                      |              |     |                       |                         |                   |   |                     |           |   | MC68HC908GZ16/D  |
| MC68HC908GZ32 | n/a      | 1.5      | 32 Flash          | n/a            | 2-CH + 6-CH, 16-bit IC, OC, or PWM | Up to 50                            | 1 SPI<br>1 ESCI   | 24-CH, 10-bit        | See<br>Timer | Y   | 3.0, 5.0              | 8.0                     | C, V, M           | 32-pin LQFP (FJ)<br>48-pin LQFP (FA)<br>64-pin QFP (FU) | n/a                 | Available | 1 to 8 MHz high frequency oscillator  | MC68HC908GZ32/D  |
| MC68HC908GZ48 |          |          | 48 Flash          |                |                                    |                                     |                   |                      |              |     |                       |                         |                   |   |                     |           |   | MC68HC908GZ48/D  |
| MC68HC908GZ60 |          |          | 60 Flash          |                |                                    |                                     |                   |                      |              |     |                       |                         |                   |   |                     |           |   | MC68HC908GZ60/D  |

<sup>1</sup>C = -40°C to 85°C, M = -40°C to 125°C, and V = 85°C to 105°C.



## CONTROLLER AREA NETWORK MICROCONTROLLERS (continued)

### 68HC12 Family CAN MCUs (continued)

| Product     | ROM (Bytes) | RAM (KB) | Flash (KB) | EEPROM (Bytes) | Timer <sup>1</sup>                           | I/O                   | Serial               | A/D          | PWM                            | Operating Voltage (V)                | Max Bus Frequency (MHz) | Temp <sup>2</sup> | Packaging  | Status    | Additional Information  | Documentation  |
|-------------|-------------|----------|------------|----------------|--|-----------------------|----------------------|--------------|--------------------------------|--------------------------------------|-------------------------|-------------------|--|-----------|---|----------------|
| XC912BC32   | n/a         | 1        | 32         | 768            | 8-CH, 16-bit IC or OC RTI, pulse accumulator | Up to 63              | SCI, SPI<br>CAN      | 8-CH, 10-bit | 4-CH, 8-bit or<br>2-CH, 16-bit | 4.5 to 5.5                           | 8.0                     | C, V, M           | 80-pin QFP (FU)  | Available | MSCAN CAN 2.0B, BDM<br>Sample pack part number:<br>KXC912BC32CFU8 | MC68HC912B     |
| MC912D60A   |             | 2        | 60         | 1K             |  | Up to 66 I/O and 18 i | Dual SCI<br>SPI, CAN |              |                                | 80-pin QFP (FU)<br>112-pin LQFP (PV) |                         |                   | Replaces the XC68HC912D60 with 5 V Flash voltage and a different programming algorithm |           |   | MC68HC912D60   |
| MC912DG128A |             | 8        | 128        | 2K             |  | 8-CH or 16-CH, 10-bit |                      |              |                                | 112-pin LQFP (PV)                    |                         |                   | Replaces the XC912DG128 with 5 V Flash voltage and a different programming algorithm   |           |   | MC68HC912DG128 |

<sup>1</sup>All 68HC12 MCUs incorporate a COP watchdog timer.

<sup>2</sup>C = -40°C to 85°C, M = -40°C to 125°C, and V = -40°C to 105°C.

### HCS12 Family CAN MCUs

HCS12 Dx and A Family devices offer pin-for-pin compatibility.

For complete part number information and temperature definitions, refer to "Product Numbering System for HCS12" on page SG1006-22.

| Product      | ROM (Bytes) | RAM (KB)            | Flash or OTP (KB) | EEPROM (KB) | Timer                                | I/O                     | Serial   | MUX                   | A/D              | PWM                            | Operating Voltage (V)                | Operating Frequency (MHz) | Temp <sup>1</sup>  | Packaging   | OTP or Flash Equiv. | Status   | Additional Information         | Documentation                  |                   |                  |                   |  |                                |           |        |      |                 |                             |
|--------------|-------------|---------------------|-------------------|-------------|--------------------------------------|-------------------------|--|-----------------------|------------------|--------------------------------|--------------------------------------|---------------------------|--|---|---------------------|--|--------------------------------|--------------------------------|-------------------|------------------|-------------------|--|--------------------------------|-----------|--------|------|-----------------|-----------------------------|
| MC9S12C128   | n/a         | 4                   | 128 Flash         | 0           | 8-CH, 16-Bit IC, OC or PWM           | Up to 60                | SCI<br>SPI   | CAN                   | 8-CH, 10-Bit     | See Timer                      | 3.0-5.0                              | 25                        | C, V, M  | 48-pin QFP (FA)<br>52-pin QFP (FB)<br>80-pin QFP (FU) | n/a                 | Available  | www.freescale.com              | 9S12C128DGV1                   |                   |                  |                   |  |                                |           |        |      |                 |                             |
| MC9S12C96    |             |                     | 96 Flash          |             |                                      |                         |  |                       |                  |                                |                                      |                           |  |   |                     |  |                                | 32 Flash                       | 1                 | 8-CH, 16-bit ECT | Up to 59          | 2 SCI<br>1 SPI   | 7-CH, 8-bit or<br>3-CH, 16-bit | 3.15, 5.5 | 16, 25 | C, M | 80-pin QFP (FU) | MC9S12DP256/D<br>CPU12RM/AD |
| MC9S12C64    |             |                     | 64 Flash          |             |                                      |                         |  |                       |                  |                                |                                      |                           |  |   |                     |  |                                |                                |                   |                  |                   |  |                                |           |        |      |                 |                             |
| MC9S12C32    |             | n/a                 | 8                 | 128 Flash   | 2                                    | 8-CH, 16-bit IC, OC, PA | Up to 91   | 2 SCI<br>2 SPI<br>IIC | 2 x 8-CH, 10-bit | 8-CH, 8-bit or<br>4-CH, 16-bit | 25.0                                 | C, V, M                   | 80-pin QFP (FU)<br>112-pin LQFP (PV)   | Samples Available                                     |                     | The 80 QFP differs from the 112 LQFP in that it offers only up to 59 I/O, has 1 x 8-CH A/D, and has 7-CH PWM | 9S12DT128BDGV1/D<br>CPU12RM/AD |                                |                   |                  |                   |  |                                |           |        |      |                 |                             |
| MC9S12D32    |             | 1                   |                   |             |                                      |                         |  |                       |                  |                                |                                      |                           |  |   |                     |  |                                | 2 CAN                          | 5.0               | C, V, M          | 80-pin QFP (FU)   | The 80 QFP differs from the 112 LQFP in that it offers only up to 59 I/O, has 1 x 8-CH A/D, and has 7-CH PWM |                                |           |        |      |                 |                             |
| MC9S12DB128B |             | 2 CAN               | 25.0              | C, V, M     | 80-pin QFP (FU)<br>112-pin LQFP (PV) | Samples Available       | The 80 QFP differs from the 112 LQFP in that it offers only up to 59 I/O, has 1 x 8-CH A/D, and has 7-CH PWM |                       |                  |                                |                                      |                           |  |   |                     |  |                                |                                |                   |                  |                   |  |                                |           |        |      |                 |                             |
| MC9S12DG128B |             |                     |                   |             |                                      |                         |  | 2 CAN                 | 25.0             | C, V, M                        | 80-pin QFP (FU)<br>112-pin LQFP (PV) | Samples Available         | The 80 QFP differs from the 112 LQFP in that it offers only up to 59 I/O, has 1 x 8-CH A/D, and has 7-CH PWM |   |                     |  |                                |                                |                   |                  |                   |  |                                |           |        |      |                 |                             |
| MC9S12DJ128B |             | 2 CAN and 1 x J1850 | 25.0              | C, V, M     | 80-pin QFP (FU)<br>112-pin LQFP (PV) | Samples Available       | The 80 QFP differs from the 112 LQFP in that it offers only up to 59 I/O, has 1 x 8-CH A/D, and has 7-CH PWM |                       |                  |                                |                                      |                           |  |   |                     |  |                                |                                |                   |                  |                   |  |                                |           |        |      |                 |                             |
| MC9S12DP512  |             |                     |                   |             |                                      |                         |  | 14                    | 512 Flash        | 4                              | 8-CH, 16-bit ECT                     | Up to 91                  | 2 SCI<br>3 SPI<br>I <sup>2</sup> C   | 5 CAN   |                     | 2 x 8-CH, 10-bit   | 8-CH, 8-bit or<br>4-CH, 16-bit | 25, 33                         | 112-pin LQFP (PV) | Available        | www.freescale.com | MC9S12DP512/D<br>CPU12RM/AD  |                                |           |        |      |                 |                             |
| MC9S12DT128B |             | 8                   | 128 Flash         | 2           | 8-CH, 16-bit IC, OC, PA              | Up to 91                | 2 SCI<br>2 SPI<br>IIC  | 3 CAN                 | 2 x 8-CH, 10-bit | 8-CH, 8-bit or<br>4-CH, 16-bit | 25.0                                 | 25.0                      | C, V, M  | 80-pin QFP (FU)<br>112-pin LQFP (PV)                  |                     | Available  | www.freescale.com              | 9S12DT128BDGV1/D<br>CPU12RM/AD |                   |                  |                   |  |                                |           |        |      |                 |                             |

<sup>1</sup>C = -40°C to 85°C, M = -40°C to 125°C, and V = 85°C to 105°C.

## CAN MCUs

### CONTROLLER AREA NETWORK MICROCONTROLLERS (continued)

#### HCS12 Family CAN MCUs (continued)

HCS12 Dx and A Family devices offer pin-for-pin compatibility.

For complete part number information and temperature definitions, refer to "Product Numbering System for HCS12" on page SG1006-22.

| Product      | ROM (Bytes) | RAM (KB) | Flash or OTP (KB) | EEPROM (KB) | Timer                   | I/O      | Serial                | MUX                    | A/D                 | PWM                            | Operating Voltage (V) | Operating Frequency (MHz) | Temp <sup>1</sup> | Packaging                            | OTP or Flash Equiv. | Status            | Additional Information | Documentation  |                                |                   |                    |                  |                                |      |              |                   |                               |
|--------------|-------------|----------|-------------------|-------------|-------------------------|----------|-----------------------|------------------------|---------------------|--------------------------------|-----------------------|---------------------------|-------------------|--------------------------------------|---------------------|-------------------|------------------------|--|--------------------------------|-------------------|--------------------|------------------|--------------------------------|------|--------------|-------------------|-------------------------------|
| MC9S12DG256B | n/a         | 12       | 256 Flash         | 4           | 8-CH, 16-bit IC, OC, PA | Up to 91 | 2 SCI<br>3 SPI<br>IIC | 2 CAN                  | 2 x 8-CH,<br>10-bit | 8-CH, 8-bit or<br>4-CH, 16-bit | 5.0                   | 25.0                      | C, V, M           | 112-pin LQFP (PV)                    | n/a                 | Samples Available | www.freescale.com      | The 80 QFP differs from the 112 LQFP in that it offers only up to 59 I/O, has 1 x 8-CH A/D, and has 7-CH PWM | 9S12DP256BDGV2/D<br>CPU12RM/AD |                   |                    |                  |                                |      |              |                   |                               |
| MC9S12DJ256B |             |          |                   |             |                         |          |                       | 2 CAN and<br>1 x J1850 |                     |                                |                       |                           |                   | 80-pin QFP (FU)<br>112-pin LQFP (PV) |                     |                   |                        |  |                                |                   |                    |                  |                                |      |              |                   |                               |
| MC9S12DP256B |             |          |                   |             |                         |          |                       | 5 CAN                  |                     |                                |                       |                           |                   | 112-pin LQFP (PV)                    |                     |                   |                        |  |                                |                   |                    |                  |                                |      |              |                   |                               |
| MC9S12DT256B |             |          |                   |             |                         |          |                       | 3 CAN                  |                     |                                |                       |                           |                   |                                      |                     |                   |                        |  |                                |                   |                    |                  |                                |      |              |                   |                               |
| MC9S12H128B  |             |          |                   |             |                         |          |                       | 128 Flash              |                     |                                |                       |                           |                   | 99<br>plus 18<br>inputs              |                     |                   |                        |  |                                | SCI<br>SPI<br>IIC | 2 CAN<br>2.0A/2.0B | 16-CH,<br>10-bit | 6-CH, 8-bit or<br>3-CH, 16-bit | 16.0 | V<br>C, V, M | www.freescale.com | 9S12H256BDGV1/D<br>CPU12RM/AD |
| MC9S12H256B  |             |          |                   |             |                         |          |                       | 256 Flash              |                     |                                |                       |                           |                   |                                      |                     |                   |                        |  |                                |                   |                    |                  |                                |      |              |                   |                               |

<sup>1</sup>M = -40°C to 125°C, C = -40°C to 85°C, V = -40°C to 105°C.

#### 683xxx Family CAN MCUs

| Product | ROM (KB) | RAM (KB) | Flash (Bytes) | Product Integration | Timer       | Serial                        | A/D                       | Operating Voltage (V) | Operating Frequency (MHz) | Temp <sup>1</sup> | Packaging   | Status    | Additional Information | Documentation                  |
|---------|----------|----------|---------------|---------------------|-------------|-------------------------------|---------------------------|-----------------------|---------------------------|-------------------|-------------|-----------|------------------------|--------------------------------|
| MC68376 | 8        | 4 + 3.5  | 0             | SIM                 | TPU<br>CTM4 | TouCAN,<br>SCI,<br>queued SPI | Queued<br>16-CH<br>10-bit | 5.0                   | 20, 25                    | C, V, M           | 160-pin QFP | Available | www.freescale.com      | MC68336/376PP<br>MC68336/376UM |

<sup>1</sup>M = -40°C to 125°C, C = -40°C to 85°C, V = -40°C to 105°C.

**CONTROLLER AREA NETWORK MICROCONTROLLERS (continued)**  
**MPC500 Family CAN MCUs**

| Product | ROM (Bytes)             | RAM (KB)                       | Flash (Bytes) | Product Integration | Timer                                    | Serial                                | MUX                     | A/D  | PWM  | Operating Voltage (V)                     | Operating Frequency (MHz) | Temp <sup>1</sup>       | Packaging         | Status    | Additional Information      | Documentation               |
|---------|-------------------------|--------------------------------|---------------|---------------------|--|---------------------------------------|-------------------------|--|--|---|---------------------------|-------------------------|-------------------|-----------|-----------------------------|-----------------------------|
| MPC533  | 0                       | 32                             | 512K          | USIU                | 22-channel timer system; MIOS14          | QSMCM (2SCI + QSPI) +1 TouCAN         | 1 x TouCAN              | 1 QADC (10-bit A/D with 64 result registers) 32 channels on chip | 12 x PWM   | 2.6, 5.0                                  | 40                        | C                       | 388-ball PBGA     | Available | www.freescale.com           | MPC533UM<br>MPC533PB        |
| MPC534  |                         |                                |               |                     |  |                                       |                         | Offers code compression  |  |   |                           |                         |                   |           |                             |                             |
| MPC535  |                         |                                |               |                     |  |                                       |                         | www.freescale.com  |  |   |                           |                         |                   |           |                             |                             |
| MPC536  |                         |                                |               |                     |  |                                       |                         | Offers code compression  |  |   |                           |                         |                   |           |                             |                             |
| MPC555  |                         | 26 + 6 for TPU                 | 448K          |                     | 50-channel timer system; 2 TPU3 + MIOS1  | QSMCM (2 SCI + QSPI) + 2 TouCAN       | 2 x TouCAN              | 2 QADC (10-bit A/D with 64 result registers) 32 channels on chip | 8 x PWM  | 3.3 Vdc for core, 5.0 Vdc for Flash       | A, C, M                   | 272-ball PBGA           | www.freescale.com |           | MPC555UM<br>TPURM<br>RCPURM |                             |
| MPC561  |                         | 32 + 8 for TPU + 2 for DEGRAM  | 0             |                     | 54-channel timer system; 2 TPU3 + MIOS14 | QSMCM (2 SCI + 1 QSPI) + 3 TouCAN     | 3 x TouCAN              |  | 12 x PWM   | 2.6 Vdc for core, 5.0 Vdc for A/D and I/O | 40, 56, 66                | C, M                    | 388-ball PBGA     |           | Offers code compression     | MPC561RM<br>TPURM<br>RCPURM |
| MPC562  |                         |                                |               |                     |  |                                       |                         | www.freescale.com  |  |   |                           |                         |                   |           | MPC563RM<br>TPURM<br>RCPURM |                             |
| MPC563  |                         |                                |               |                     |  |                                       |                         |  |  |   |                           |                         |                   |           |                             | Offers code compression     |
| MPC564  |                         |                                |               |                     |  |                                       |                         | www.freescale.com  |  |   |                           |                         |                   |           | MPC566UM<br>TPURM<br>RCPURM |                             |
| MPC565  |                         | 36 + 10 for TPU + 4 for DEGRAM | 1M            |                     | 70-channel timer system; 3 TPU3 + MIOS14 | QSMCM x 2 (4 SCI + 2 QSPI) + 3 TouCAN | 3 x TouCAN<br>1 x J1850 |  | 2 QADC (10-bit A/D with 64 result registers) 40 channels on chip | 40 or 56                                  | A, C, M                   | Offers code compression |                   |           |                             |                             |
| MPC566  | Offers code compression |                                |               |                     |  |                                       |                         |  |  |   |                           |                         |                   |           |                             |                             |

<sup>1</sup>A = -55°C to 125°C, C = -40°C to 85°C, and M = -40°C to 125°C.

## CAN MCUs

### CONTROLLER AREA NETWORK MICROCONTROLLERS (continued)

#### 56800 Family CAN MCUs

| Product  | Performance | Program ROM/RAM/Flash | Data ROM/RAM/Flash | Peripherals   | Packaging                       | Additional Information   |
|--|-------------|-----------------------|--------------------|---|---------------------------------|--|
| <b>F80X Family</b>                             |             |                       |                    |   |                                 |  |
| DSP56F803BU80                                  | 80 MHz      | n/a/512/32K           | n/a/2K/4K          | CAN, SCI, SPI, ADC, PWM, Quadrature Decoder, Quad Timer | 100-pin LQFP                    | MCU-friendly instruction set, OnCE for debug, 2K Boot Flash, external memory expansion available, up to 16 GPIO. Order two-unit sample pack as SPAK56F803BU80. S, MOQ of 90.                 |
| DSP56F805FV80                                  |             |                       |                    |   | 144-pin LQFP                    | MCU-friendly instruction set, OnCE for debug, 2K Boot Flash, external memory expansion available, up to 32 GPIO. SPAK56F805FV80. S, MOQ of 60.   |
| DSP56F807PY80 (LQFP)<br>DSP56F807VF80 (MAPBGA) |             | n/a/2K/60K            | n/a/2K/8K          |   | 160-pin LQFP<br>160-ball MAPBGA | MCU-friendly instruction set, OnCE for debug, 2K Boot Flash, external memory expansion available, up to 32 GPIO. MOQ of 60 for LQFP. SPAK56F807PY80 or SPAK56F807VF80. MOQ of 24 for MAPBGA. |

#### 56F8300 Family CAN MCUs <sup>Note</sup>

| Product                          | Performance       | Flash/RAM (KB) | Off-Chip Memory Expansion (EMI) | Peripherals   | Packaging                   | Additional Information   |
|----------------------------------|-------------------|----------------|---------------------------------|---|-----------------------------|--|
| <b>F832x Family</b>              |                   |                |                                 |   |                             |  |
| MC56F8322MFA60<br>MC56F8322MFAE  | 60 MHz<br>60 MIPS | 48/12          | n/a                             | 2 SPI, 2 SCI, 2 ADC, PWM, COP, PLL, Decoder, 2 Quad Timers, FlexCAN   | 48-pin LQFP<br>48-pin LQFP* | Extended (-40°C to 125°C) MCU-friendly instruction set, Enhanced OnCE for debug, on-chip relaxation oscillator, temperature sensor, and up to 21 GPIOs.  |
| MC56F8322VFA60<br>MC56F8322VFAE  |                   |                |                                 |   | 48-pin LQFP<br>48-pin LQFP* | Industrial (-40°C to 105°C), MCU-friendly instruction set, Enhanced OnCE for debug, on-chip relaxation oscillator, temperature sensor and up to 21 GPIOs.  |
| MC56F8323MFB60<br>MC56F8323MFB6E |                   |                |                                 |   | 64-pin LQFP<br>64-pin LQFP* | Extended (-40°C to 125°C), MCU-friendly instruction set, Enhanced OnCE for debug, on-chip relaxation oscillator, temperature sensor, and up to 27 GPIOs.   |
| MC56F8323VFB60<br>MC56F8323VFB6E |                   |                |                                 |   | 64-pin LQFP<br>64-pin LQFP* | Industrial (-40°C to 105°C), MCU-friendly instruction set, Enhanced OnCE for debug, on-chip relaxation oscillator, temperature sensor, and up to 27 GPIOs.   |
| <b>F833x Family</b>              |                   |                |                                 |   |                             |  |
| MC56F8335VFG60<br>MC56F8335MFG6E | 60 MHz<br>60 MIPS | 80/12          | n/a                             | 2 SPI, 2 SCI, 4ADC, PWM, COP, PLL, 2 Decoders, 4 Quad Timers, FlexCAN | 128-pin LQFP*               | Industrial (-40°C to 105°C), MCU-friendly instruction set, Enhanced OnCE for debug, temperature sensor, and up to 49 GPIOs.<br>Industrial (-40°C to 105°C), MCU-friendly instruction set, Enhanced OnCE for debug, temperature sensor, and up to 49 GPIOs. |

Note: Contact your local Freescale Semiconductor Sales Office or authorized Freescale Semiconductor distributor for product availability.

\*This package is RoHS compliant.

**CONTROLLER AREA NETWORK MICROCONTROLLERS (continued)**  
**56F8300 Family CAN MCUs<sup>Note</sup> (continued)**

| Product  | Performance       | Flash/RAM (KB) | Off-Chip Memory Expansion (EMI)                  | Peripherals   | Packaging   | Additional Information  |
|--|-------------------|----------------|--|---|---|---|
| <b>F834x Family</b>  |                   |                |  |   |   |   |
| MC56F8345MFG60<br>MC56F8345MFGE  | 60 MHz<br>60 MIPS | 144/12         | n/a  | 2 SPI, 2 SCI, 4 ADC,<br>2 PWM, COP, PLL,<br>2 Decoders, 4 Quad Timers,<br>FlexCAN | 128-pin LQFP<br>128-pin LQFP*   | Extended (-40°C to 125°C), MCU-friendly instruction set, Enhanced OnCE for debug, temperature sensor, and up to 49 GPIOs.   |
| MC56F8345VFG60<br>MC56F8345VFGE  |                   |                |  |   | 128-pin LQFP<br>128-pin LQFP*   | Industrial (-40°C to 105°C), MCU-friendly instruction set, Enhanced OnCE for debug, temperature sensor, and up to 49 GPIOs. |
| MC56F8346MVF60<br>MC56F8346MFVE  |                   |                | Yes  |   | 144-pin LQFP<br>144-pin LQFP*   | Extended (-40°C to 125°C), MCU-friendly instruction set, Enhanced OnCE for debug, temperature sensor, and up to 62 GPIOs.   |
| MC56F8346VVF60<br>MC56F8346VFVE  |                   |                |  |   | 144-pin LQFP<br>144-pin LQFP*   | Industrial (-40°C to 105°C), MCU-friendly instruction set, Enhanced OnCE for debug, temperature sensor, and up to 62 GPIOs. |
| MC56F8347MPY60<br>MC56F8347MPYE  |                   |                | 160-pin LQFP<br>160-pin LQFP*<br>160-pin MAPBGA* |   | Extended (-40°C to 125°C), MCU-friendly instruction set, Enhanced OnCE for debug, temperature sensor, and up to 76 GPIOs.   |   |
| MC56F8347VPY60 (LQFP)<br>MC56F8347VPYE (LQFP)<br>MC56F8347VVE (MAPBGA) |                   |                |  |   | Industrial (-40°C to 105°C), MCU-friendly instruction set, Enhanced OnCE for debug, temperature sensor, and up to 76 GPIOs. |   |
| <b>F835x Family</b>  |                   |                |  |   |   |   |
| MC56F8355MFG60<br>MC56F8355MFGE  | 60 MHz<br>60 MIPS | 280/20         | Yes  | 2 SPI, 2 SCI, 4 ADC,<br>2 PWM, COP, PLL,<br>2 Decoders, 4 Quad Timers,<br>FlexCAN | 128-pin LQFP<br>128-pin LQFP*   | Extended (-40°C to 125°C), MCU-friendly instruction set, Enhanced OnCE for debug, temperature sensor, and up to 49 GPIOs.   |
| MC56F8355VFG60<br>MC56F8355VFGE  |                   |                | n/a  |   | 128-pin LQFP<br>128-pin LQFP*   | Industrial (-40°C to 105°C), MCU-friendly instruction set, Enhanced OnCE for debug, temperature sensor, and up to 49 GPIOs. |
| MC56F8356MVF60<br>MC56F8356MFVE  |                   |                | Yes  |   | 144-pin LQFP<br>144-pin LQFP*   | Extended (-40°C to 125°C), MCU-friendly instruction set, Enhanced OnCE for debug, temperature sensor, and up to 62 GPIOs.   |
| MC56F8356VVF60<br>MC56F8356VFVE  |                   |                |  |   | 144-pin LQFP<br>144-pin LQFP*   | Industrial (-40°C to 105°C), MCU-friendly instruction set, Enhanced OnCE for debug, temperature sensor, and up to 62 GPIOs. |
| MC56F8357MPY60<br>MC56F8357MPYE  |                   |                | 160-pin LQFP<br>160-pin LQFP*                    |   | Extended (-40°C to 125°C), MCU-friendly instruction set, Enhanced OnCE for debug, temperature sensor, and up to 76 GPIOs.   |   |
| MC56F8357VPY60 (LQFP)<br>MC56F8357VPYE (LQFP)<br>MC56F8357VVE (MAPBGA) |                   |                |  |   | 160-pin LQFP<br>160-pin LQFP*<br>160-pin MAPBGA*  | Industrial (-40°C to 105°C), MCU-friendly instruction set, Enhanced OnCE for debug, temperature sensor, and up to 76 GPIOs. |

Note: Contact your local Freescale Semiconductor Sales Office or authorized Freescale Semiconductor distributor for product availability.

\*This package is RoHS compliant.

**CONTROLLER AREA NETWORK MICROCONTROLLERS (continued)**

**56F8300 Family CAN MCUs<sup>Note</sup> (continued)**

| Product  | Performance       | Flash/RAM (KB) | Off-Chip Memory Expansion (EMI)   | Peripherals   | Packaging   | Additional Information  |
|--|-------------------|----------------|---|---|---|---|
| <b>F836x Family</b>  |                   |                |   |   |   |   |
| MC56F8365VFG60<br>MC56F8365VFG60   | 60 MHz<br>60 MIPS | 576/36         | n/a   | 2 SPI, 2 SCI, 4 ADC,<br>2 PWM, COP, PLL,<br>2 Decoders, 4 Quad Timers,<br>2 FlexCAN | 128-pin LQFP<br>128-pin LQFP*   | Industrial (-40°C to 105°C), MCU-friendly instruction set, Enhanced OnCE for debug, temperature sensor, and up to 49 GPIOs. |
| MC56F8365MFG60<br>MC56F8365MFG60   |                   |                | Extended (-40°C to 125°C), MCU-friendly instruction set, Enhanced OnCE for debug, temperature sensor, and up to 49 GPIOs. |   |   |   |
| MC56F8366VVF60<br>MC56F8366VVF60   |                   |                | Yes   |   | 144-pin LQFP<br>144-pin LQFP*   | Industrial (-40°C to 105°C), MCU-friendly instruction set, Enhanced OnCE for debug, temperature sensor, and up to 62 GPIOs. |
| MC56F8366MFV60<br>MC56F8366MFV60   |                   |                |   |   | Extended (-40°C to 125°C), MCU-friendly instruction set, Enhanced OnCE for debug, temperature sensor, and up to 62 GPIOs.   |   |
| MC56F8367VPY60 (LQFP)<br>MC56F8367VPY60 (LQFP)<br>MC56F8367VVFE (MAPBGA) |                   |                | 160-pin LQFP<br>160-pin LQFP*<br>160-ball MAPBGA*   |   | Industrial (-40°C to 105°C), MCU-friendly instruction set, Enhanced OnCE for debug, temperature sensor, and up to 76 GPIOs. |   |
| MC56F8367MPY60<br>MC56F8367MPY60 (LQFP)                                  |                   |                | Extended (-40°C to 125°C), MCU-friendly instruction set, Enhanced OnCE for debug, temperature sensor, and up to 76 GPIOs. |   |   |   |

Note: Contact your local Freescale Semiconductor Sales Office or authorized Freescale Semiconductor distributor for product availability.  
\*This package is RoHS compliant.

## LOCAL AREA NETWORK MICROCONTROLLERS

### LIN Slave MCUs

| Product       | ROM (KB) | RAM (Bytes) | Flash or OTP (KB) | EEPROM (Bytes) | Timer                              | I/O          | Serial       | A/D          | PWM  | COP  | Operating Voltage (V) | Max Bus Freq (MHz) | Temp <sup>1</sup> | Packaging  | OTP or Flash Equiv.                  | Status                     | Additional Information               | Documentation   |             |              |              |   |          |            |         |     |            |
|---------------|----------|-------------|-------------------|----------------|------------------------------------|--------------|--------------|--------------|------|--|-----------------------|--------------------|-------------------|--|--------------------------------------|----------------------------|--------------------------------------|-----------------|-------------|--------------|--------------|---|----------|------------|---------|-----|------------|
| MC68HC08AB16A | 16       | 512         | n/a               | 512            | 4-CH + 4-CH, 16-bit IC, OC, or PWM | 51           | SCI<br>SPI   | 8-CH, 8-bit  |      |  | 5.0                   | 8.0                | C, M              | 64-pin QFP (FU)  | 908AB32                              | Available                  | Programmable interrupt timer module. | MC68HC08AB16A/D |             |              |              |   |          |            |         |     |            |
| MC68HC908AB32 | n/a      | 1K          | 32 Flash          |                |                                    |              |              |              |      |  |                       |                    | n/a               |  | 2-CH + 2-CH, 16-bit I/C, O/C, or PWM |                            |                                      | 24              | ESCI<br>SPI | 8-CH, 10-bit | See<br>Timer | Y | 3.0, 5.0 | 8.0<br>Max | C, V, M | n/a | Production |
| MC68HC908EY16 |          | 512         | 16 Flash          | n/a            | 2-CH, 16-bit IC, OC, or PWM        | 23           | n/a          | 12-CH, 8-bit | C, M | 28-pin DIP (P)<br>28-pin SOIC (DW)<br>48-pin LQFP (FA) | 908JL3                | Available          |                   | RC oscillator option, LVR with selectable trip points, 6-pin LED drive. Sample pack part numbers: KMC908JL3CP, KMC908JL3CDW, KMCR908JL3CP, KMCR908JL3CDW |                                      | MC68HC908EY16/D            |                                      |                 |             |              |              |   |          |            |         |     |            |
| MC68HC908JL3  |          | 128         | 4 Flash           |                |                                    |              |              |              |      |  |                       |                    |                   |  |                                      | 2-CH, 16-bit IC, OC or PWM | 13                                   |                 |             |              |              |   |          |            |         |     |            |
| MC68HC08JL3   | 4        |             | n/a               | n/a            | MC68HC908QL4                       | MC68HC908QL3 | MC68HC908QL2 |              |      |  |                       |                    |                   |  |                                      |                            |                                      |                 |             |              |              |   |          |            |         |     |            |
| MC908QL4      | n/a      |             | 4                 |                |                                    |              |              |              |      |  |                       |                    |                   |  |                                      |                            |                                      |                 |             |              |              |   |          |            |         |     |            |
| MC908QL3      |          | 2           |                   |                |                                    |              |              |              |      |  |                       |                    |                   |  |                                      |                            |                                      |                 |             |              |              |   |          |            |         |     |            |

<sup>1</sup>C = -40°C to 85°C, M = -40°C to 125°C, and V = -40°C to 105°C.

### 56F8000 LIN Slave MCUs *Note*

| Product       | Performance       | Flash/RAM (KB) | Peripherals  | Packaging   | Additional Information  |
|---------------|-------------------|----------------|--|-------------|---|
| MC56F8013VFAE | 32 MHz<br>32 MIPS | 16/4           | 6-CH PWM, Quad Timer, SPI, SCI with LIN slave, PLL, dual 3-CH, 12-bit ADCs, COP, POR, I <sup>2</sup> C, On-Chip oscillator | 32-pin LQFP | Industrial (-40°C to 105°C), MCU-friendly instruction set, Enhanced OnCE for debug, temperature sensor, and up to 26 GPIOs. |
| MC56F8014VFAE |                   |                | 5-CH PWM, Quad Timer, SPI, SCI with LIN slave, PLL, dual 4-CH, 12-bit ADCs, COP, POR, I <sup>2</sup> C, On-Chip oscillator |             | Industrial (-40°C to 105°C), MCU-friendly instruction set, Enhanced OnCE for debug, temperature sensor, and up to 26 GPIOs. |

Note: Contact your local Freescale Semiconductor Sales Office or authorized Freescale Semiconductor distributor for product availability.

LOCAL AREA NETWORK MICROCONTROLLERS (continued)

**68HC08 LIN Master MCUs**

| Product      | ROM (KB) | RAM (KB) | Flash or OTP (KB) | EEPROM (Bytes) | Timer                              | I/O   | Serial            | A/D                  | PWM       | COP | Operating Voltage (V) | Max Bus Freq (MHz) | Temp <sup>1</sup> | Packaging                           | OTP or Flash Equiv. | Status    | Additional Information | Documentation |
|--------------|----------|----------|-------------------|----------------|------------------------------------|-------|-------------------|----------------------|-----------|-----|-----------------------|--------------------|-------------------|-------------------------------------|---------------------|-----------|------------------------|---------------|
| XC68HC08AZ32 | 32       | 1        | n/a               | 512            | 4-CH + 2-CH, 16-bit IC, OC, or PWM | 40/50 | SCI<br>SPI<br>CAN | 8-CH or 15-CH, 8-bit | See Timer | Y   | 5.0                   | 8.4                | C, V, M           | 64-pin QFP (FU)<br>52-pin PLCC (FN) | 908AZ60A            | Available | CAN 2.0A and 2.0B      | MC68HC08AZ32  |
| MC908AZ60A   | n/a      | 2        | 60 Flash          | 1K             | 6-CH + 2-CH, 16-bit IC, OC, or PWM | 50    |                   | 15-CH, 8-bit         |           |     |                       |                    |                   | 64-pin QFP (FU)                     | n/a                 |           |                        |               |

<sup>1</sup>C = -40°C to 85°C, M = -40°C to 125°C, and V = -40°C to 105°C.

**68HC12 LIN Master MCUs**

| Product         | ROM (KB) | RAM (KB) | Flash (KB) | EEPROM (Bytes) | Timer  | I/O                   | Serial                         | A/D                   | PWM                         | Operating Voltage (V) | Max Bus Frequency (MHz) | Temp <sup>1</sup> | Packaging                            | Status    | Additional Information  | Documentation    |
|-----------------|----------|----------|------------|----------------|--|-----------------------|--------------------------------|-----------------------|-----------------------------|-----------------------|-------------------------|-------------------|--------------------------------------|-----------|---|------------------|
| MC68HC912B32    | n/a      | 1        | 32         | 768            | 8-CH, 16-bit IC or OC RTI, pulse accumulator | Up to 63              | SCI, SPI J1850                 | 8-CH, 10-bit          | 4-CH, 8-bit or 2-CH, 16-bit | 5.0                   | 8.0                     | C, V, M           | 80-pin QFP (FU)                      | Available | J1850, muxed bus, BDM. Sample pack part numbers: KMC912B32CFU/VFU/MFU                   | MC68HC912B/D     |
| MC68HC12BE32    | 32       |          | n/a        |                |  |                       |                                |                       |                             |                       |                         |                   |                                      |           | KXC12BE32DCFU8. Sample pack part number: KXC12BE32DCFU8                                 |                  |
| MC912D60A       | n/a      | 2        | 60         | 1K             | 8-CH, 16-Bit                                 | Up to 66 I/O and 18 i | Dual SCI SPI, CAN              | Dual 8-CH, 10-Bit     | 4-CH, 8-bit or 2-CH, 16-bit | 5.0                   | 8.0                     | C, V, M           | 80-pin QFP (FU)<br>112-pin LQFP (PV) | Available | Replaces the XC68HC912D60 with 5 V Flash voltage and a different programming algorithm. | MC68HC912D60/D   |
| XC68HC12D60     | 60       |          | n/a        |                |  |                       | Part equipped with CAN 2.0A/B. |                       |                             |                       |                         |                   |                                      |           |   |                  |
| MC912DG128A     | n/a      | 8        | 128        | 2K             | 8-CH, 16-Bit IC or OC RTI, pulse accumulator | Up to 67 I/O and 18 i | Dual SCI SPI, CAN              | 8-CH or 16-CH, 10-Bit | 4-CH, 8-bit or 2-CH, 16-bit | 5.0                   | 8.0                     | C, V, M           | 112-pin LQFP (PV)                    | Available | Replaces the XC912DG128 with 5 V Flash voltage and a different programming algorithm.   | MC68HC912DG128/D |
| MC68HC912DT128A |          |          |            |                | 8-CH, 16-Bit                                 |                       | Dual SCI, SPI                  | Dual 8-CH, 10-Bit     |                             |                       |                         |                   |                                      |           | Part equipped with 3xCAN 2.0A/B.  |                  |

<sup>1</sup>C = -40°C to 85°C, M = -40°C to 125°C, and V = -40°C to 105°C.



**UNIVERSAL SERIAL BUS MICROCONTROLLERS**  
**68HC08 Family USB MCUs**

| Product       | ROM (Bytes) | RAM (Bytes) | Flash or OTP (Bytes) | EEPROM (Bytes) | Timer                            | I/O         | Serial                               | A/D | PWM       | COP | Operating Voltage (V) | Max Bus Freq (MHz) | Temp             | Packaging   | OTP or Flash Equiv. | Status    | Additional Information   | Documentation   |
|---------------|-------------|-------------|----------------------|----------------|----------------------------------|-------------|--------------------------------------|-----|-----------|-----|-----------------------|--------------------|------------------|---|---------------------|-----------|--|---|
| MC68HC08JB1   | 5.5K        | 128         | n/a                  | n/a            | 2-CH, 16-bit IC, OC, or PWM      | 13          | USB PS/2                             | n/a | See Timer | Y   | 5.0                   | 3.0                | 0°C to 70°C only | 20-pin DIP (P)<br>20-pin SOIC (JDW)   | 908JB8              | Available | Supports both USB and PS/2; 1.5Mbps USB with 2 endpoints, low voltage reset, keyboard interrupt, 3.3 V bandgap reference | n/a   |
| MC68HC908JB8  | n/a         | 256         | 8K Flash             |                |                                  | Up to 37    | USB                                  |     |           |     |                       |                    |                  | 20-pin DIP (P)<br>28-pin SOIC (DW)<br>44-pin QFP (FB)                         | n/a                 |           | Complies with USB 1.1 specification for low-speed USB (1.5Mbps) On-chip 3.3 V regulator                                  | MC68HC908JB8/D  |
| MC68HC908JB12 |             | 384         | 12K Flash            |                | Up to 21                         | SCI USB 2.0 | 20-pin SOIC (DW)<br>28-pin SOIC (DW) |     |           |     |                       |                    |                  | n/a   | www.freescale.com   |           | n/a  |   |
| MC68HC08JB8   | 8K          | 256         | n/a                  |                | Dual 2-CH, 16-bit IC, OC, or PWM | Up to 37    | USB                                  |     |           |     | 4.0 to 5.5            | 3.0                |                  | 20-pin PDIP (JP)<br>20-pin SOIC (JDW)<br>28-pin SOIC (ADW)<br>44-pin QFP (FB) | 908JB8              |           | Complies with USB 1.1 specification for low-speed USB (1.5Mbps), LVI   | MC68HC908JB8/D  |
| MC68HC08KH12  | 12K         | 384         |                      |                |                                  |             |                                      |     |           |     | 42                    | 3.3 V              |                  | 6.0   | 64-pin QFP (FU)     |           | 708KH12  | PC keyboard/hub 12mbs USB (1 up, 4 down) 5 LED direct drive port pins |

## MOTOR CONTROL MCUs

### MOTOR CONTROL MICROCONTROLLERS

#### Motor Control Unit Product Table

| Product | COP | Operating Voltage (V) | Max Bus Frequency (MHz) | Temp | Packaging   | OTP | Status            | Additional Information  | Documentation     |
|---------|-----|-----------------------|-------------------------|------|---|-----|-------------------|---|-------------------|
| MC3PHAC | Y   | 5.0                   | 4.0                     | V    | 32-pin LQFP (FA)<br>28-pin SOIC (DW)<br>28-pin PDIP (P) | n/a | Samples Available | A complete solution, contains all functions required to implement control of open loop 3-phase AC motor drive | MC3PHAC<br>DRM006 |

#### 56F800 MCUs *Note*

| Product  | Performance       | Program ROM/RAM/Flash   | Data ROM/RAM/Flash   | Peripherals   | Packaging  | Additional Information  |
|--|-------------------|-------------------------|----------------------|---|--|---|
| DSP56F801FA80<br>DSP56F801FA80E  | 80 MHz<br>40 MIPS | n/a/1K/8K<br>(words)    | n/a/1K/2K<br>(words) | SCI, SPI, ADC, PWM,<br>Quad Timer                             | 48-pin LQFP<br>48-pin LQFP*  | MCU-friendly instruction set, OnCE for debug, on-chip relaxation oscillator, 2K Boot Flash, up to 11 GPIO.                              |
| DSP56F801FA60<br>DSP56F801FA60E  | 60 MHz<br>30 MIPS |                         |                      |   |  | MCU-friendly instruction set, OnCE for debug, on-chip relaxation oscillator, 2K Boot Flash, up to 11 GPIO.                              |
| DSP56F802TA80<br>DSP56F802TA80E  | 80 MHz<br>40 MIPS |                         |                      | SCI, ADC, PWM, Quad Timer                                     | 32-pin LQFP<br>32-pin LQFP*  | MCU-friendly instruction set, OnCE for debug, on-chip relaxation oscillator, 2K Boot Flash, up to 4 GPIO.                               |
| DSP56F802TA60<br>DSP56F802TA60E  | 60 MHz<br>30 MIPS |                         |                      |   |  | MCU-friendly instruction set, OnCE for debug, on-chip relaxation oscillator, 2K Boot Flash, up to 4 GPIO.                               |
| DSP56F803BU80<br>DSP56F803BU80E  | 80 MHz<br>40 MIPS | n/a/512K/32K<br>(words) | n/a/2K/4K<br>(words) | CAN, SCI, SPI, ADC, PWM,<br>Quadrature Decoder,<br>Quad Timer | 100-pin LQFP<br>100-pin LQFP*  | MCU-friendly instruction set, OnCE for debug, 2K Boot Flash, external memory expansion available, up to 16 GPIO.                        |
| DSP56F805FV80<br>DSP56F805FV80E  |                   |                         |                      |   | 144-pin LQFP<br>144-pin LQFP*  | MCU-friendly instruction set, OnCE for debug, 2K Boot Flash, external memory expansion available, up to 32 GPIO.                        |
| DSP56F807PY80 (LQFP)<br>DSP56F807PY80E (LQFP)<br>DSP56F807VF80 (MAPBGA)<br>DSP56F807VF80E (MAPBGA) |                   | n/a/2K/60K<br>(words)   | n/a/4K/8K<br>(words) |   | 160-pin LQFP<br>160-pin LQFP*<br>160-ball MAPBGA<br>160-ball MAPBGA* | MCU-friendly instruction set, OnCE for debug, 2K Boot Flash, external memory expansion available, up to 32 GPIO.<br>MOQ of 40 for LQFP. |

Note: Contact your local Freescale Semiconductor Sales Office or authorized Freescale Semiconductor distributor for product availability.

\*This package is RoHS compliant.

## MOTOR CONTROL MICROCONTROLLERS (continued)

### 56F8300 MCUs <sup>Note</sup>

| Product   | Performance       | Flash/RAM (KB) | Off-Chip Memory Expansion (EMI) | Peripherals  | Packaging  | Additional Information   |
|---|-------------------|----------------|---------------------------------|--|--|--|
| <b>F832x Family</b>   |                   |                |                                 |  |  |  |
| MC56F8322MFA60<br>MC56F8322MFAE   | 60 MHz<br>60 MIPS | 48/12          | n/a                             | 2 SPI, 2 SCI, 2 ADC, PWM, COP, PLL, Decoder, 2 Quad Timers, FlexCAN      | 48-pin LQFP<br>48-pin LQFP*                      | Extended (-40°C to 125°C) MCU-friendly instruction set, Enhanced OnCE for debug, on-chip relaxation oscillator, temperature sensor, and up to 21 GPIOs.  |
| MC56F8322VFA60<br>MC56F8322VFAE   |                   |                |                                 |  | 48-pin LQFP<br>48-pin LQFP*                      | Industrial (-40°C to 105°C), MCU-friendly instruction set, Enhanced OnCE for debug, on-chip relaxation oscillator, temperature sensor and up to 21 GPIOs.  |
| MC56F8323MFB60<br>MC56F8323MFB E  |                   |                |                                 |  | 64-pin LQFP<br>64-pin LQFP*                      | Extended (-40°C to 125°C), MCU-friendly instruction set, Enhanced OnCE for debug, on-chip relaxation oscillator, temperature sensor, and up to 27 GPIOs.   |
| MC56F8323VFB60<br>MC56F8323VFB E  |                   |                |                                 |  | 64-pin LQFP<br>64-pin LQFP*                      | Industrial (-40°C to 105°C), MCU-friendly instruction set, Enhanced OnCE for debug, on-chip relaxation oscillator, temperature sensor, and up to 27 GPIOs.   |
| <b>F833x Family</b>   |                   |                |                                 |  |  |  |
| MC56F8335VFG60<br>MC56F8335MFG E  | 60 MHz<br>60 MIPS | 80/12          | n/a                             | 2 SPI, 2 SCI, 4ADC, PWM, COP, PLL, 2 Decoders, 4 Quad Timers, FlexCAN    | 128-pin LQFP*                                    | Industrial (-40°C to 105°C), MCU-friendly instruction set, Enhanced OnCE for debug, temperature sensor, and up to 49 GPIOs.<br>Industrial (-40°C to 105°C), MCU-friendly instruction set, Enhanced OnCE for debug, temperature sensor, and up to 49 GPIOs. |
| <b>F834x Family</b>   |                   |                |                                 |  |  |  |
| MC56F8345MFG60<br>MC56F8345MFG E  | 60 MHz<br>60 MIPS | 144/12         | n/a                             | 2 SPI, 2 SCI, 4 ADC, 2 PWM, COP, PLL, 2 Decoders, 4 Quad Timers, FlexCAN | 128-pin LQFP<br>128-pin LQFP*                    | Extended (-40°C to 125°C), MCU-friendly instruction set, Enhanced OnCE for debug, temperature sensor, and up to 49 GPIOs..   |
| MC56F8345VFG60<br>MC56F8345VFG E  |                   |                |                                 |  | 128-pin LQFP<br>128-pin LQFP*                    | Industrial (-40°C to 105°C), MCU-friendly instruction set, Enhanced OnCE for debug, temperature sensor, and up to 49 GPIOs.  |
| MC56F8346MVF60<br>MC56F8346MVF E  |                   |                | Yes                             |  | 144-pin LQFP<br>144-pin LQFP*                    | Extended (-40°C to 125°C), MCU-friendly instruction set, Enhanced OnCE for debug, temperature sensor, and up to 62 GPIOs.  |
| MC56F8346VVF60<br>MC56F8346VVF E  |                   |                |                                 |  | 144-pin LQFP<br>144-pin LQFP*                    | Industrial (-40°C to 105°C), MCU-friendly instruction set, Enhanced OnCE for debug, temperature sensor, and up to 62 GPIOs.  |
| MC56F8347MPY60<br>MC56F8347MPY E  |                   |                |                                 |  | 160-pin LQFP<br>160-pin LQFP*<br>160-pin MAPBGA* | Extended (-40°C to 125°C), MCU-friendly instruction set, Enhanced OnCE for debug, temperature sensor, and up to 76 GPIOs.  |
| MC56F8347VPY60 (LQFP)<br>MC56F8347VPY E (LQFP)<br>MC56F8347VVF E (MAPBGA) |                   |                |                                 |  |  | Industrial (-40°C to 105°C), MCU-friendly instruction set, Enhanced OnCE for debug, temperature sensor, and up to 76 GPIOs.  |

Note: Contact your local Freescale Semiconductor Sales Office or authorized Freescale Semiconductor distributor for product availability.

\*This package is RoHS compliant.

## MOTOR CONTROL MCUs

### MOTOR CONTROL MICROCONTROLLERS (continued)

#### 56F8300 MCUs <sup>Note</sup> (continued)

| Product  | Performance       | Flash/RAM (KB) | Off-Chip Memory Expansion (EMI) | Peripherals   | Packaging   | Additional Information  |
|--|-------------------|----------------|---------------------------------|---|---|---|
| <b>F835x Family</b>  |                   |                |                                 |   |   |   |
| MC56F8355MFG60<br>MC56F8355MFGE  | 60 MHz<br>60 MIPS | 280/20         | Yes                             | 2 SPI, 2 SCI, 4 ADC,<br>2 PWM, COP, PLL,<br>2 Decoders, 4 Quad Timers,<br>FlexCAN   | 128-pin LQFP<br>128-pin LQFP*                     | Extended (-40°C to 125°C), MCU-friendly instruction set, Enhanced OnCE for debug, temperature sensor, and up to 49 GPIOs.   |
| MC56F8355VFG60<br>MC56F8355VFGVE                                       |                   |                | n/a                             |   | 128-pin LQFP<br>128-pin LQFP*                     | Industrial (-40°C to 105°C), MCU-friendly instruction set, Enhanced OnCE for debug, temperature sensor, and up to 49 GPIOs. |
| MC56F8356MFV60<br>MC56F8356MFVE  |                   |                | Yes                             |   | 144-pin LQFP<br>144-pin LQFP*                     | Extended (-40°C to 125°C), MCU-friendly instruction set, Enhanced OnCE for debug, temperature sensor, and up to 62 GPIOs.   |
| MC56F8356VFG60<br>MC56F8356VFGVE                                       |                   |                | Yes                             |   | 144-pin LQFP<br>144-pin LQFP*                     | Industrial (-40°C to 105°C), MCU-friendly instruction set, Enhanced OnCE for debug, temperature sensor, and up to 62 GPIOs. |
| MC56F8357MPY60<br>MC56F8357MPYE  |                   |                | Yes                             |   | 160-pin LQFP<br>160-pin LQFP*                     | Extended (-40°C to 125°C), MCU-friendly instruction set, Enhanced OnCE for debug, temperature sensor, and up to 76 GPIOs.   |
| MC56F8357VPY60 (LQFP)<br>MC56F8357VPYE (LQFP)<br>MC56F8357VVE (MAPBGA) |                   |                | Yes                             |   | 160-pin LQFP<br>160-pin LQFP*<br>160-pin MAPBGA*  | Industrial (-40°C to 105°C), MCU-friendly instruction set, Enhanced OnCE for debug, temperature sensor, and up to 76 GPIOs. |
| <b>F836x Family</b>  |                   |                |                                 |   |   |   |
| MC56F8365VFG60<br>MC56F8365VFGVE                                       | 60 MHz<br>60 MIPS | 576/36         | n/a                             | 2 SPI, 2 SCI, 4 ADC,<br>2 PWM, COP, PLL,<br>2 Decoders, 4 Quad Timers,<br>2 FlexCAN | 128-pin LQFP<br>128-pin LQFP*                     | Industrial (-40°C to 105°C), MCU-friendly instruction set, Enhanced OnCE for debug, temperature sensor, and up to 49 GPIOs. |
| MC56F8365MFG60<br>MC56F8365MFGE  |                   |                | n/a                             |   | 128-pin LQFP<br>128-pin LQFP*                     | Extended (-40°C to 125°C), MCU-friendly instruction set, Enhanced OnCE for debug, temperature sensor, and up to 49 GPIOs.   |
| MC56F8366VFG60<br>MC56F8366VFGVE                                       |                   |                | Yes                             |   | 144-pin LQFP<br>144-pin LQFP*                     | Industrial (-40°C to 105°C), MCU-friendly instruction set, Enhanced OnCE for debug, temperature sensor, and up to 62 GPIOs. |
| MC56F8366MFV60<br>MC56F8366MFVE  |                   |                | Yes                             |   | 144-pin LQFP<br>144-pin LQFP*                     | Extended (-40°C to 125°C), MCU-friendly instruction set, Enhanced OnCE for debug, temperature sensor, and up to 62 GPIOs.   |
| MC56F8367VPY60 (LQFP)<br>MC56F8367VPYE (LQFP)<br>MC56F8367VVE (MAPBGA) |                   |                | Yes                             |   | 160-pin LQFP<br>160-pin LQFP*<br>160-ball MAPBGA* | Industrial (-40°C to 105°C), MCU-friendly instruction set, Enhanced OnCE for debug, temperature sensor, and up to 76 GPIOs. |
| MC56F8367MPY60<br>MC56F8367MPYE (LQFP)                                 |                   |                | Yes                             |   | 160-pin LQFP<br>160-pin LQFP*                     | Extended (-40°C to 125°C), MCU-friendly instruction set, Enhanced OnCE for debug, temperature sensor, and up to 76 GPIOs.   |

Note: Contact your local Freescale Semiconductor Sales Office or authorized Freescale Semiconductor distributor for product availability.

\*This package is RoHS compliant.

#### 56F8000 MCUs <sup>Note</sup>

| Product       | Performance       | Flash/RAM (KB) | Peripherals   | Packaging   | Additional Information  |
|---------------|-------------------|----------------|---|-------------|---|
| MC56F8013VFAE | 32 MHz<br>32 MIPS | 16/4           | 6-CH PWM, Quad Timer, SPI,<br>SCI with LIN slave, PLL, dual<br>3-CH, 12-bit ADCs, COP, POR,<br>I <sup>2</sup> C, On-Chip oscillator | 32-pin LQFP | Industrial (-40°C to 105°C), MCU-friendly instruction set, Enhanced OnCE for debug, temperature sensor, and up to 26 GPIOs. |

Note: Contact your local Freescale Semiconductor Sales Office or authorized Freescale Semiconductor distributor for product availability.

**MOTOR CONTROL MICROCONTROLLERS (continued)**

**56F8100 MCUs** *Note*

| Product                       | Performance       | Flash/RAM (KB) | Off-Chip Memory Expansion (EMI) | Peripherals   | Packaging  | Additional Information   |  |
|-------------------------------|-------------------|----------------|---------------------------------|---|--|--|--|
| MC56F8122VFA<br>MC56F8122VFAE | 40 MHz<br>40 MIPS | 40/8           | n/a                             | 2 SPI, 2 SCI, 2 ADC, COP, PLL, Quad Timer                 | 48-pin LQFP<br>48-pin LQFP*  | Industrial (-40°C to 105°C), MCU-friendly instruction set, Enhanced OnCE for debug, on-chip relaxation oscillator, and up to 21 GPIOs. |  |
| MC56F8123VFB<br>MC56F8123VFBE |                   |                |                                 |   | 64-pin LQFP<br>64-pin LQFP*  | Industrial (-40°C to 105°C), MCU-friendly instruction set, Enhanced OnCE for debug, on-chip relaxation oscillator, and up to 27 GPIOs. |  |
| MC56F8135VFG                  |                   | 72/8           |                                 |   | 128-pin LQFP*  | Industrial (-40°C to 105°C), MCU-friendly instruction set, Enhanced OnCE for debug, and up to 49 GPIOs.                                |  |
| MC56F8145VFG<br>MC56F8145VFG  |                   | 136/8          |                                 |   |  | 128-pin LQFP<br>128-pin LQFP*  | Industrial (-40°C to 105°C), MCU-friendly instruction set, Enhanced OnCE for debug and up to 49 GPIOs. |
| MC56F8146VFB<br>MC56F8146VFE  |                   |                |                                 |   |  | Yes  | 144-pin LQFP<br>144-pin LQFP*  |
| MC56F8147VPY<br>MC56F8147VPYE |                   | 272/16         |                                 |   | Yes  |  | 160-pin LQFP<br>160-pin LQFP*  |
| MC56F8155VFG<br>MC56F8155VFG  |                   |                | n/a                             | 2 SPI, 2 SCI, 4ADC, PWM, COP, PLL, Decoder, 2 Quad Timers |  | 128-pin LQFP<br>128-pin LQFP*  | Industrial (-40°C to 105°C), MCU-friendly instruction set, Enhanced OnCE for debug and up to 49 GPIOs. |
| MC56F8156VFB<br>MC56F8156VFE  |                   | Yes            |                                 |   | 144-pin LQFP<br>144-pin LQFP*  | Industrial (-40°C to 105°C), MCU-friendly instruction set, Enhanced OnCE for debug and up to 62 GPIOs.                                 |  |
| MC56F8157VPY<br>MC56F8157VPYE |                   |                | 544/32                          | Yes   | 160-pin LQFP<br>160-pin LQFP*  | Industrial (-40°C to 105°C), MCU-friendly instruction set, Enhanced OnCE for debug and up to 76 GPIOs.                                 |  |
| MC56F8165VFG<br>MC56F8165VFG  |                   | n/a            |                                 |   | 128-pin LQFP<br>128-pin LQFP*  | Industrial (-40°C to 105°C), MCU-friendly instruction set, Enhanced OnCE for debug and up to 49 GPIOs.                                 |  |
| MC56F8166VFB<br>MC56F8166VFE  |                   |                | Yes                             | 144-pin LQFP<br>144-pin LQFP*                             |  | Industrial (-40°C to 105°C), MCU-friendly instruction set, Enhanced OnCE for debug and up to 62 GPIOs.                                 |  |
| MC56F8167VPY<br>MC56F8167VPYE |                   | Yes            |                                 | 160-pin LQFP<br>160-pin LQFP*                             | Industrial (-40°C to 105°C), MCU-friendly instruction set, Enhanced OnCE for debug and up to 76 GPIOs. |  |  |

Note: Contact your local Freescale Semiconductor Sales Office or authorized Freescale Semiconductor distributor for product availability.

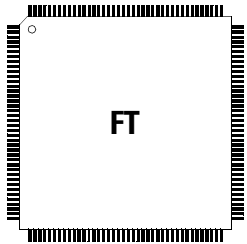
\*This package is RoHS compliant.

*ZIGBEE™ -READY PLATFORM*

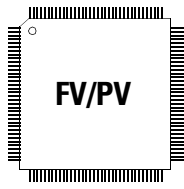
***ZigBee-Ready and Proprietary RF Transceivers***

| Product     | Data Rate (kbps) | Operating Voltage (V) | Band (MHz) | MCU Interface | Packaging        | Status    | Additional Information   |
|-------------|------------------|-----------------------|------------|---------------|------------------|-----------|--|
| MC13191FCR2 | 250 max          | 2.4 to 3.4            | 2.4 GHz    | SPI           | 32-pin QFN 5 x 5 | Available | 2.4 GHz Proprietary RF transceiver data modem for Point-to-Point and Star applications |
| MC13192FCR2 |                  |                       |            |               |                  |           | 2.4 GHz RF transceiver data modem for ZigBee™ applications                             |

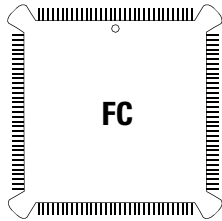
**PACKAGING**  
**(Actual Size)**



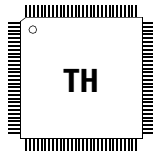
**FT**  
208/160-Pin QFP  
.65 mm Pitch  
28 mm x 28 mm Body



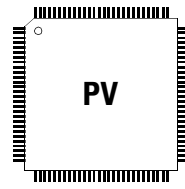
**FV/PV**  
144-Pin LQFP/QFP  
.5 mm Pitch  
20 mm x 20 mm Body



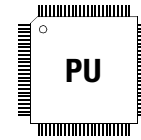
**FC**  
132-Pin PQFP  
25 mil/06.35 mm Pitch  
0.950 in x 0.950 in Body  
(Nominal, w.o. Bumpers)



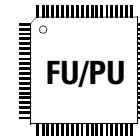
**TH**  
120-Pin QFP/LQFP  
.5 mm Pitch  
16 mm x 16 mm Body



**PV**  
112-Pin LQFP  
.65 mm Pitch  
20 mm x 20 mm Body



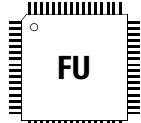
**PU**  
100-Pin LQFP  
.5 mm Pitch  
14 mm x 14 mm Body



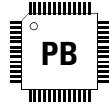
**FU/PU**  
80-Pin QFP/LQFP  
.65 mm Pitch  
14 mm x 14 mm Body



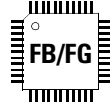
**TU**  
80-Pin TQFP-EP  
.65 mm Pitch  
14 mm x 14 mm Body



**FU**  
64-Pin QFP  
.8 mm Pitch  
14 mm x 14 mm Body



**PB**  
52-Pin LQFP  
.65 mm Pitch  
10 mm x 10 mm Body



**FB/FG**  
44-Pin QFP/LQFP  
.8 mm Pitch  
10 mm x 10 mm Body



**FD**  
48-Pin QFN  
.5 mm Pitch  
7 mm x 7 mm Body



**FF**  
16-Pin QFN  
.5 mm Pitch  
5 mm x 5 mm Body



**FJ**  
32-Pin LQFP  
.8 mm Pitch  
7 mm x 7 mm Body



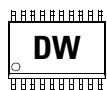
**FA**  
48-Pin LQFP  
.5 mm Pitch  
7 mm x 7 mm Body



**FQ**  
8-Pin DFN  
4 mm x 4 mm Body



**DW**  
28-Pin SOIC  
50 mil/1.27 mm Pitch  
18.0 mm x 7.5 mm Body



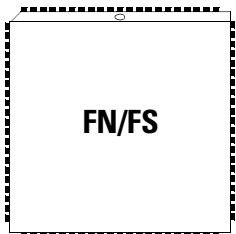
**DW**  
20-Pin SOIC  
50 mil/1.27 mm Pitch  
12.8 mm x 7.5 mm Body



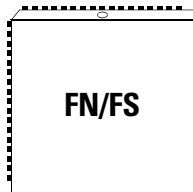
**DW**  
16-Pin SOIC  
50 mil/1.27 mm Pitch  
10.35 mm x 7.5 mm Body



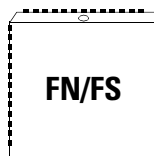
**DW**  
8-Pin SOIC  
50 mil/1.27 mm Pitch  
5.3 mm x 7.5 mm Body



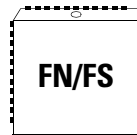
**FN/FS**  
84-Pin PLCC/CLCC  
50 mil/1.27 mm Pitch  
1.15 in x 1.15 in Body



**FN/FS**  
68-Pin PLCC/CLCC  
50 mil/1.27 mm Pitch  
0.950 in x 0.950 in Body



**FN/FS**  
52-Pin PLCC/CLCC  
50 mil/1.27 mm Pitch  
0.750 in x 0.750 in Body



**FN/FS**  
44-Pin PLCC/CLCC  
50 mil/1.27 mm Pitch  
0.650 in x 0.650 in Body

# PACKAGING

## PACKAGING (continued) (Actual Size)



**28-Pin SSOP**

25.6 mil/.65 mm Pitch  
5.28 mm x 10.19 mm Body



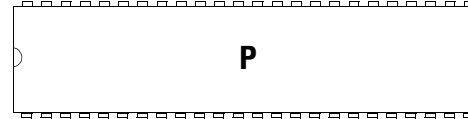
**20-Pin SSOP**

17 mil/.65 mm Pitch  
5.3 mm x 7.2 mm Body



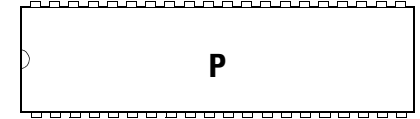
**16-Pin TSSOP**

25 mil/.64 mm Pitch  
5.0 mm x 4.4 mm Body



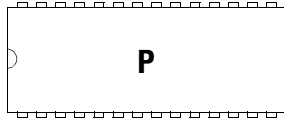
**48-Pin Plastic DIP**

100 mil/2.54 mm Pitch  
2.45 in x .55 in Body  
(100 mil x 600 mil pin centers)



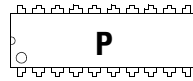
**40-Pin Plastic DIP**

100 mil/2.54 mm Pitch  
2.05 in x .55 in Body  
(100 mil x 600 mil pin centers)



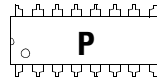
**28-Pin DIP**

100 mil/2.54 mm Pitch  
1.45 in x .55 in Body  
(100 mil x 600 mil pin centers)



**20-Pin Plastic DIP**

100 mil/2.54 mm Pitch  
.97 in x .29 in Body  
(100 mil x 300 mil pin centers)



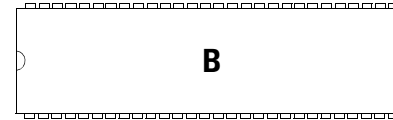
**16-Pin Plastic DIP**

100 mil/2.54 mm Pitch  
.75 in x .25 in Body  
(100 mil x 300 mil pin centers)



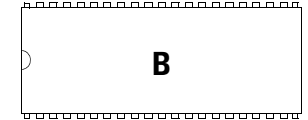
**8-Pin Plastic DIP**

100 mil/2.54 mm Pitch  
.38 in x .25 in Body  
(100 mil x 300 mil pin centers)



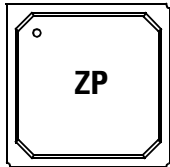
**56-Pin Plastic SDIP**

70 mil/1.778 mm Pitch  
2.05 in x .55 in Body  
(70 mil x 600 mil pin centers)



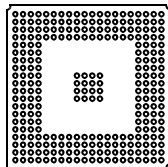
**42-Pin Plastic SDIP**

70 mil/1.778 mm Pitch  
1.45 in x .55 in Body  
(70 mil x 600 mil pin centers)



**272-Ball PBGA**

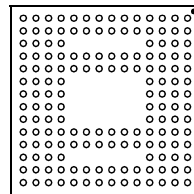
1.27 mm Pitch  
27.0 mm x 27.0 mm Body



**144-Ball Grid Array (BGA)**

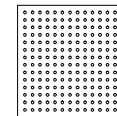
.8 mm Ball Pitch  
12 mm x 12 mm x 1.6 mm

**VF**



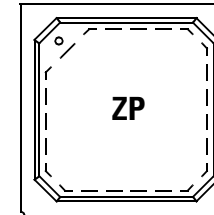
**160-Ball Plastic MAPBGA**

**VF**



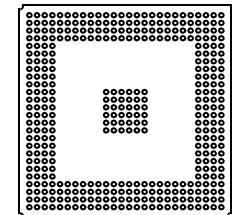
**196-Ball MAPBGA**

1 mm Pitch  
15 mm x 15 mm Body



**388-Ball PBGA**

1 mm Pitch  
27.0 mm x 27.0 mm Body





## ***NOTES***

## ***NOTES***

## ***NOTES***

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