

**JITESH H. PANCHAL**

*Associate Professor*  
School of Mechanical Engineering  
Purdue University, West Lafayette, IN

**Contact Information**

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West Lafayette, IN 47907-2088  
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*URL:* <https://engineering.purdue.edu/DELP>

**Education**

*Degree:* **Doctor of Philosophy**, Mechanical Engineering, Georgia Institute of Technology  
*Major:* Computer Aided Engineering and Design  
*Minor:* Engineering Mathematics  
*PhD Dissertation Title:* "A Framework for Simulation-Based Integrated Design of Multiscale Products and Design Processes"  
*Completed:* **December 2005**  
*Advisors:* Dr. Farrokh Mistree and Dr. Chris Paredis

*Degree:* **Master of Science**, Mechanical Engineering, Georgia Institute of Technology  
*Major:* Computer Aided Engineering and Design  
*MS Thesis Title:* "Towards a Design Support System for Distributed Product Realization"  
*Completed:* **May 2003**  
*Advisors:* Dr. Farrokh Mistree and Dr. Janet Allen

*Degree:* **Bachelor of Technology**, Indian Institute of Technology (IIT), Guwahati  
*Major:* Mechanical Engineering  
*B. Tech. Project Title:* "Optimization of Turning Process using a Neuro-Fuzzy Controller"  
*Completed:* **May 2000**

**Academic Appointments**

**Associate Professor**, August 2015 – Present  
School of Mechanical Engineering, Purdue University, West Lafayette, IN

**Assistant Professor**, August 2012 – August 2015  
School of Mechanical Engineering, Purdue University, West Lafayette, IN

**Assistant Professor**, August 2008 – August 2012  
School of Mechanical and Materials Engineering, Washington State University, Pullman, WA

**Visiting Assistant Professor**, January 2006 – August 2008  
The G.W. Woodruff School of Mechanical Engineering, Georgia Institute of Technology, Savannah, GA

## Other Appointments as a Graduate Student

**Graduate Research Assistant**, August 2001 – December 2005  
*Systems Realization Laboratory, Georgia Institute of Technology, Atlanta, GA*

**Woodruff School Doctoral Teaching Fellow**, Spring 2005  
*The G.W. Woodruff School of Mechanical Engineering, Georgia Institute of Technology, Atlanta*

**Teaching Associate**, Spring 2004  
*The G.W. Woodruff School of Mechanical Engineering, Georgia Institute of Technology, Atlanta*

**Doctoral Teaching Practicum Participant**, Spring 2004  
*The G.W. Woodruff School of Mechanical Engineering, Georgia Institute of Technology, Atlanta*

## Industrial Experience

**Research Analyst**, May 2004 – Aug 2004  
*Collaborative Product Development Associates (CPDA), New York, USA*  
*Mentor: Donald H. Brown (Managing Partner, Collaborative Product Development Associates)*

**Software Engineer**, July 2000 – June 2001  
*Interra Information Technologies, Noida, India*

**Summer Intern**, May 1999 – July 1999  
*Hindustan Lever Limited, Haldia, India*

## Research Grants

### **NSF Systems Science (SYS) Grant**

*Title: A theoretical framework for understanding strategic behavior in systems engineering*

*Duration: August 2017 – July 2020*

*Amount: \$502,945*

*Role: Co-PI*

*Collaborators: Ilias Bilionis (PI), Thanh Nguyen*

*URL: [http://www.nsf.gov/awardsearch/showAward?AWD\\_ID=1728165](http://www.nsf.gov/awardsearch/showAward?AWD_ID=1728165)*

### **NSF Systems Science (SYS) Grant**

*Title: Understanding Information Acquisition Decisions in Systems Design through Behavioral Experiments and Bayesian Analysis*

*Duration: August 2017 – July 2020*

*Amount: \$649,876*

*Role: Principal Investigator*

*Collaborators: Ilias Bilionis, Sebastien Helie, and Karthik N. Kannan*

*URL: [http://www.nsf.gov/awardsearch/showAward?AWD\\_ID=1662230](http://www.nsf.gov/awardsearch/showAward?AWD_ID=1662230)*

**Naval Postgraduate School**

*Title:* Computing without Revealing: A Cryptographic Approach to eProcurement

*Duration:* December 2016 – July 2018

*Amount:* \$120,000

*Role:* Principal Investigator (Funding share: \$60,000)

*Collaborator:* Mikhail Atallah

**Tata Consultancy Services (TCS) Grant**

*Title:* Decision-based Collaborative Design Workflow Management for PREMAP

*Duration:* July 2015 – present

*Amount:* \$443,798

*Role:* Principal Investigator (Funding share: \$167,000)

*Collaborators:* Farrokh Mistree and Janet K. Allen (University of Oklahoma)

**NSF Systems Science (SYS) Grant**

*Title:* Decision-centric foundations for modeling and analysis of complex networked systems

*Duration:* September 2014 – August 2018

*Amount:* \$425,466

*Role:* Principal Investigator (Funding share: \$245,346)

*Collaborators:* Daniel A. DeLaurentis

*URL:* [http://www.nsf.gov/awardsearch/showAward?AWD\\_ID=1360361](http://www.nsf.gov/awardsearch/showAward?AWD_ID=1360361)

**NSF Engineering Systems Design (ESD) Grant**

*Title:* Crowdsourcing for Engineering Systems Design: Theoretical and Experimental Studies

*Duration:* July 2014 – June 2017

*Amount:* \$400,580

*Role:* Principal Investigator (Funding share: \$315,850)

*Collaborators:* Karthik N. Kannan

*URL:* [http://www.nsf.gov/awardsearch/showAward?AWD\\_ID=1400050](http://www.nsf.gov/awardsearch/showAward?AWD_ID=1400050)

**NSF Cyber Physical Systems (CPS) Grant**

*Title:* Foundations of Cyber-Physical Infrastructure for Creative Design and Making of Cyber-physical Products

*Duration:* September 2013 – August 2017

*Amount:* \$1,075,000

*Role:* Principal Investigator (Funding share: \$340,000)

*Collaborators:* Mikhail Atallah and Karthik Ramani

*URL:* [http://www.nsf.gov/awardsearch/showAward?AWD\\_ID=1329979](http://www.nsf.gov/awardsearch/showAward?AWD_ID=1329979)

**NSF Engineering Design and Innovation (EDI) Grant**

*Title:* Integrated Policy and Engineering Design for Complex Systems with Applications to Distribution Systems within Smart Electric Grid

*Duration:* August 2012 – April 2017

*Amount:* \$350,000

*Role:* Principal Investigator (Funding share: \$200,000)

*Collaborator:* Anurag K. Srivastava (Washington State University)

*URL:* [http://www.nsf.gov/awardsearch/showAward?AWD\\_ID=1261860](http://www.nsf.gov/awardsearch/showAward?AWD_ID=1261860)

**NSF EAGER Grant**

*Title:* Managing Uncertainty by Integrating Information Economics and Robust Design

*Duration:* August 2010 – July 2011

*Amount:* \$79,817

*Role:* Co-Principal Investigator (Funding share: \$39,818)

*Collaborator:* Farrokh Mistree, PI (University of Oklahoma)

*URL:* [http://nsf.gov/awardsearch/showAward?AWD\\_ID=1042350](http://nsf.gov/awardsearch/showAward?AWD_ID=1042350)

**NSF CAREER Award**

*Title:* Collective Innovation - Transforming the Realization of Complex Engineering Systems

*Duration:* April 2010 – March 2016

*Amount:* \$400,000

*Role:* Principal Investigator (single investigator grant)

*URL:* [http://www.nsf.gov/awardsearch/showAward?AWD\\_ID=1265622](http://www.nsf.gov/awardsearch/showAward?AWD_ID=1265622)

**PLM Center of Excellence Grant**

*Title:* Community-based Product Lifecycle Management (c-PLM) – Bridging the gap between PLM and social innovation

*Duration:* January 2013 – December 2013

*Amount:* \$30,000

*Role:* Principal Investigator (Funding share: \$15,000)

*Collaborator:* Karthik Ramani

**Procter and Gamble Fund's Higher Education Grant**

*Title:* Collective Innovation in Product Development: A Web-Based Platform for Educating Next Generation Engineers

*Duration:* January 2009 – January 2010

*Amount:* \$10,000

*Role:* Principal Investigator (single investigator grant)

**Awards and Honors**

1. **B.F.S. Schaefer Outstanding Young Faculty Scholar Award** (2017-2019) from the School of Mechanical Engineering at Purdue University.
2. **NSF CAREER Award**, April 2010 – March 2016.
3. **Best paper award** at the ASME iDETC conference for:  
Shergadwala, M., Kannan K., and Panchal J. H., 2016, "Understanding the Impact of Expertise on the Design Quality: An Approach based on Concept Inventories and Item Response Theory," *ASME 2016 International Design Engineering Technical Conferences & Computers and Information in Engineering Conference (IDETC/CIE 2016)*, Charlotte, NC, USA, August 21-24, 2016. Paper Number: DETC2016-59038.
4. **Robert E. Fulton SEIKM Best Paper Award** at the ASME CIE conference, 2013 for:  
Sha, Z., and Panchal, J.H., 2013, "Estimating the Node-Level Behaviors in Complex Networks from Structural Datasets," *2013 ASME International Design Engineering Technical Conferences (IDETC) and Computers and Information in Engineering (CIE) Conference*, Portland, OR. Paper Number: DETC2013-12063.

5. **Reid Miller Outstanding Teaching Faculty Award** by the College of Engineering and Architecture (CEA), 2012, Washington State University, Pullman, WA.
6. **Teaching Excellence Award**, two consecutive years (2011 and 2012), School of Mechanical and Materials Engineering, Washington State University, Pullman, WA.
7. **Young Engineer Award**, 2010, ASME Computers and Information in Engineering Division.
8. **Robert E. Fulton EIM Best Paper Award** at the ASME CIE conference, 2004 for:  
Panchal, J.H., Fernández, M.G., Paredis, C.J.J., Allen, J.K., and Mistree, F., Designing Design Processes in Product Lifecycle Management: Research Issues and Strategies. in *ASME 2004 Design Engineering Technical Conferences and Computer and Information in Engineering Conference*, 2004, Salt Lake City, Utah. Paper No. DETC2004/CIE-57742.
9. **NSF/ASME Essay Competition Winner** at ASME DETC conference, 2004
10. **Woodruff School Doctoral Teaching Fellowship**, 2005, Georgia Institute of Technology.
11. **University Silver Medal** for the years 1996-2000 for being the top undergraduate student in Mechanical Engineering department at Indian Institute of Technology (IIT), Guwahati.

### Courses Taught

#### PURDUE UNIVERSITY

##### Undergraduate Level

ME 352 – Machine Design I (Spring 2015, Fall 2015, Spring 2016, Spring 2017, Spring 2018)

ME 452 – Machine Design II (Fall 2016, Fall 2012, Spring 2013, Fall 2013, Fall 2016)

ME 463 – Engineering Design (Spring 2014)

##### Graduate Level

\*ME579 – Decision Making for Engineering Systems Design (Fall 2014, Fall 2017)

\*ME697 – Topics in Engineering Design Science (Spring 2016)

#### WASHINGTON STATE UNIVERSITY

##### Graduate Level

\*ME503 - Systems-based Design Approaches for Sustainability

ME575 – Geometric Modeling

##### Undergraduate Level

ME310 – Manufacturing Processes

ME311 – Manufacturing Processes Lab

ME348 – System Dynamics

ME414 – Machine Design

#### GEORGIA INSTITUTE OF TECHNOLOGY

##### Graduate Level

ME6101 – Engineering Design

ME6102 – Designing Open Engineering Systems

##### Undergraduate Level

ME3180 – Machine Design

ME4041 – Interactive Computer Graphics and Computer-Aided Design

\*ME4813 – Rapid Product Development for a Global Economy

\*ME4903 – Information Engineering for Systems Realization

\* *New courses developed by Jitesh H. Panchal*

<b>Students Advised/In-progress</b>
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**IN PROGRESS****PhD**

1. *Joseph D Thekinen* (2014 - present): Decision-centric Foundations for Complex Networked Systems
2. *Siva Chaduvula* (2014 - present): Secure Co-Design (co-advised with Mikhail Atallah)
3. *Murtuza Shergadwala* (2014 - present): Crowdsourcing in Engineering Systems Design
4. *Piyush Pandita* (2014 - present): A framework for Design of Experiments for Blackbox Functions under Uncertainty (co-advised with Ilias Bilonis)
5. *Adam Dachowicz* (2015-present): Microstructure-based Fingerprinting of Metal Parts
6. *Sharmila Karumuri* (2016 - present): To be decided
7. *Ashish Chaudhari* (2017-present): Information Acquisition Decisions in Engineering Design

**MS Thesis**

1. *Vikranth Kattukuri* (2017-present): Spacecraft Systems Engineering: A Benchmark Problem

**COMPLETED****PhD**

1. *Zhenghui Sha* (2010 - 2015): Decision-centric Foundations for Complex Systems Engineering and Design (Purdue University, ME). Recipient of 2017 **Best Dissertation Award** from the ASME Computers and Information in Engineering (CIE) division.
2. *Ahmad Taha* (2015): Secure Estimation, Control and Optimization of Uncertain CPSs with Applications to Power Systems (Purdue University, ECE, co-advised by Oleg Wasynczuk)
3. *Qize Le* (2008 - 2012): Analysis and Modeling of the Product Structure and Community Structure in Open Source Processes (Washington State University)

**MS Thesis**

1. *Ashish Chaudhari* (2015-2017): Crowdsourcing for Engineering Design: Theoretical and Experimental Studies (Purdue University, ME)
2. *Parth Paritosh* (2015-2017): Gaussian Process Dynamical Models for Designing Multi-stage Manufacturing Processes (co-advised with Ilias Bilonis; Purdue University, ME)
3. *Naman Mandhan* (2014-2016): A Decentralized 3D Printing Framework Based on Gale Shapley Matching (Purdue University, ME)
4. *Sainath Varikuti* (2013-2014): A Web-based Online Collaboration Platform for Formulating Engineering Design Projects (Purdue University, ME)
5. *Bryant Hawthorne* (2010 - 2012): Towards Feed-In-Tariff Policy Design Considering Multiple Objectives and Incomplete Preferences (Washington State University)
6. *Yiwen Liu* (2010 - 2011): Evaluating the Technical, Economic, and Environmental Impact of the Level of Decentralization in Energy Investment Decisions (Washington State University)
7. *Marc Somers* (2011-2011): Collective Innovation Ecosystem for Design Courses (Washington State University)
8. *HaoYun Huang* (2008 – 2010): Analysis of the Structure and Evolution of an Open-Source Community (Washington State University)

**BS** (Research Projects)

1. *Murtuza Shergadala*, May – July 2013, Adaptive and innovative design interface technology.
2. *Nadim Hachem*, May 2013 – July 2013, Optimal power flow modeling for smart-grids.

3. *Yuxing Zhang*, August 2013 – December 2013, Stress visualization in beams and shafts.
4. *Stephanie Pitts* (Spring 2009 - Fall 2009): Creation of a Sealing Subspace within the Collective Innovation Framework (Washington State University)

## Professional Activities

### Invited Talks and Workshops

1. Invited plenary talk at the 4<sup>th</sup> World Congress on Integrated Computational Materials Engineering (ICME 2017). May 24<sup>th</sup>, 2017.
2. Invited Seminar at the School for Engineering of Matter, Transport and Energy, *Arizona State University*. March 31, 2017.
3. Invited Seminar at the Center for Advanced Vehicular Systems (CAVS), *Mississippi State University*. February 14, 2017.
4. Invited Seminar in the Department of Mechanical and Industrial Engineering (MIE) at *University of Illinois at Chicago (UIC)*. October 11, 2016.
5. Invited talk at the International Symposium on *Digital Platforms for Decision based Design of Complex Engineered Systems*, June 21-22, 2016, Beijing, China.
6. Invited to National Science Foundation Workshop on *Theoretical Foundations of Systems Engineering*. February 8-12, 2016.
7. Invited to National Science Foundation Workshop on *Research Challenges in Modeling & Simulation for Engineering Complex Systems*. January 13-14, 2016.
8. Keynote speaker at the Indian Institute of Metals (IIM)-TMS Symposium on *ICME for Steel: Handshakes for Industrial Adoption*, Coimbatore, India. November 15, 2015.
9. Invited Seminar in the Department of Mechanical Engineering at *Indiana University-Purdue University Indianapolis (IUPUI)*. December 03, 2015.
10. Invited speaker for NSF sponsored *CAREER proposal writing workshop*, held at the ASME IDETC/CIE conference, Boston, MA. August 04, 2015.
11. Invited to National Science Foundation Workshop on the *Theory of Systems Engineering*, Arlington, VA. November 12-14, 2014.
12. Invited member of US delegation for Indo-US Workshop on *ICME for Integrated Realization of Engineered Materials and Products*, Pune, India. December 18-21, 2013.
13. 2-day workshop (with Farrokh Mistree) on *Engineering Education for the 21<sup>st</sup> Century*, College of Engineering, Pune, India. May 17-18, 2013.
14. Invited talk at *Technical Architects Conference (TACTICS)*, Tata Consulting Services, Pune, India. January 03, 2013.

### Professional Service

1. *Associate Editor*, ASME Journal of Computing and Information Science in Engineering (2016-2018).
2. *Editor of Special Issue* on "Highlights from ASME CIE 2017", ASME Journal of Computing and Information Science in Engineering (2018). Editorial: <https://doi.org/10.1115/1.4040307>
3. *Guest Editor*, Design Science Journal, for the Thematic Collection - Network-based Modeling and Analysis in Design (2016-17).
4. *Chair*, ASME Design Education Conference Executive Committee. (2016-2018).
5. *Secretary*, Executive Committee of the ASME Computers and Information in Engineering (CIE) division (2017-18).
6. *Member at Large*, Executive Committee of the ASME Computers and Information in Engineering (CIE) division (2016-17).

7. *Co-organizer*, International Symposium on Digital Platforms for Decision Based Design of Complex Engineered Systems, Beijing Institute of Technology, Beijing, China. June 21-23, 2016.
8. *Vice Chair*, ASME Design Education Conference Executive Committee. (2014-2016).
9. *Review co-coordinator*, Decision making in engineering design, 42nd Design Automation Conference (DAC), 2016.
10. *Review coordinator*, Decision making in engineering design, 41st Design Automation Conference (DAC), 2015.
11. *Conference Chair*, ASME 10th International Conference on Design Education (DEC) (2013-2014)
12. *Track organizer*, Engineering Science in Education, Society of Engineering Science (SES) 2014 Technical Meeting.
13. *Past Chair and Awards committee chair*, ASME CIE Computer Aided Product and Process Design (CAPPD) Technical Committee (2013-2014)
14. *Program Chair*, ASME 10th International Conference on Design Education (DEC) (2012-2013)
15. *Panel organizer*, Educating the Faculty of the Future, DEC-2-2, at the 2013 Design Education Conference (DEC) at the ASME iDETC conference
16. *Chair*, ASME CIE Computer Aided Product and Process Design Technical Committee (2012-2013)
17. *Vice-chair*, ASME CIE Computer Aided Product and Process Design Technical Committee (2011-2012)
18. *Secretary*, ASME CIE Computer Aided Product and Process Design Technical Committee (2010-2011)
19. *Review coordinator*, "Systems Engineering," CIE-26 at 2011 CIE Conference
20. *Session Chair*, "Design Process Modeling," CIE-9-4 at 2010 CIE Conference
21. *Review coordinator*, "Conceptual Design Methods," DAC-3 at 2010 DAC Conference
22. *Co-organizer*, "Network Centric Product Realization," Special session at 2009 CIE Conference
23. *Review coordinator*, "Feature-Based Design and Recognition," CIE-1-2 at 2009 CIE Conference
24. *Co-organizer*, "Mass Collaborative Product Realization," Workshop at 2008 ASME CIE Conference

### **Reviewer**

1. *Reviewer*, Journal of Mechanical Design
2. *Reviewer*, Computer Aided Design
3. *Reviewer*, Research in Engineering Design
4. *Reviewer*, Journal of Engineering Design
5. *Reviewer*, Journal of Networks and Spatial Economics
6. *Reviewer*, Engineering Optimization
7. *Reviewer*, IEEE System, Man and Cybernetics – Part a
8. *Reviewer*, Networks and Spatial Economics
9. *Reviewer*, Journal for Computing and Information Science in Engineering
10. *Reviewer*, Concurrent Engineering - Research and Applications
11. *Reviewer*, ASME Design Automation Conference
12. *Reviewer*, ASME Computers and Information in Engineering Conference
13. *Book proposal reviewer*, for two books for Springer in 2012

### **School-Level Service Activities**

1. Member of ABET Committee (chaired by C. Wassgren), Summer 2017-present
2. Member of the Undergraduate Curriculum Committee (chaired by Jim Jones), Fall 2017-present.
3. Member of Graduate Admissions Committee (chaired by F. Sadeghi), Fall 2015-present.
4. Member of Manufacturing Facilities Planning committee (chaired by Y. Shin), Spring 2015.
5. Member of School Impact and Reputation committee (chaired by I. Mudawar), Fall 2014.
6. Member of the Graduate Studies Committee, School of Mechanical Engineering, Purdue University. Fall 2014 – Summer 2015.



7. Member of the graduate studies committee, undergraduate studies committee and the laboratory and computing committee, Mechanical and Materials Engineering, Washington State University, August 2008-present.
8. IT committee member at Georgia Tech, Savannah, January 2006 – December 2006.
9. Member, Program Committee, Woodruff School Savannah. Developed a web-based tool for ABET 2008 assessment.

### **Professional Affiliations**

1. Member, American Society of Mechanical Engineers (ASME)
2. Member, Design Society
3. Member, Society for Judgment and Decision Making

### **List of Publications**

#### *Books*

- B1. McDowell, D. L., Panchal, J. H., Choi, H.-J., Seepersad, C. C., Allen, J. K., and Mistree, F., 2009, *Integrated Design of Multiscale, Multifunctional Materials and Products*, Elsevier. ISBN: 9781856176620. [http://www.amazon.com/Integrated-Multiscale-Multifunctional-Materials-Products/dp/1856176622/ref=sr\\_1\\_1?ie=UTF8&s=books&qid=1300980247&sr=8-1](http://www.amazon.com/Integrated-Multiscale-Multifunctional-Materials-Products/dp/1856176622/ref=sr_1_1?ie=UTF8&s=books&qid=1300980247&sr=8-1)
- B2. Fujimoto, R., Bock, C., Chen, W., Page, E., and Panchal, J.H. (Editors), 2017, *Research Challenges in Modeling and Simulation for Engineering Complex Systems*, Springer. ISBN: 978-3-319-58543-7. <http://www.springer.com/us/book/9783319585437>

#### *Book Chapters*

- BC1. Chen, W., Kesidis, G., Morrison, T., Oden, J.T., Panchal, J.H., Paredis, C., Pennock, M., Atamturktur, S., Terejanu, G., Yukish, M., 2017, "Uncertainty in Modeling and Simulation," *Research Challenges in Modeling and Simulation for Engineering Complex Systems*, Springer, pp. 75-86. ISBN: 978-3-319-58543-7.
- BC2. Shukla, R., Anapagaddi, R., Singh, A.K., Allen, J.K., Panchal, J.H. and Mistree, F., 2016, "Integrated Computational Materials Engineering for Determining Set Points of Unit Operations for Production of a Steel Product Mix," *Computational Approaches to Materials Design Theoretical and Practical Aspects* (Eds. S. Datta and J.P. Davim), Hershey, PA, Chapter 6, IGI Publishers, pp. 163-191. DOI: 10.4018/978-1-5225-0290-6.ch006
- BC3. Panchal, J. H. and Le, Q., 2014, "Product Development by Self-Organized Virtual Communities," *Advances in Computers and Information in Engineering Research*, (Editors: Michopoulos, J., Rosen, D., Paredis, C.J.J., and Vance, J.), Chapter 7. ASME press, pp. 155-182. ISBN: 978-0791860328.
- BC4. Yanamandram, V.M.K. and Panchal, J. H., 2014, "Evaluating the Level of Openness in Open Source Hardware," *Product Development in the Socio-sphere: Game Changing Paradigms for 21st Century Breakthrough Product Development and Innovation*, (Editor: Schaefer, D.), Chapter 6, Springer, pp. 99-120. DOI: 10.1007/978-3-319-07404-7.
- BC5. Mistree, F., Panchal, J. H., Schaefer, D., Allen, J. K., Haroon, S., Siddique, Z., 2014, "Personalized Engineering Education for the 21<sup>st</sup> Century," *Curriculum Models for the 21<sup>st</sup> Century: Using Learning Technologies in Higher Education*, (Editors: Gosper, M. and Ifenthaler, D.), Chapter 6, Springer Science+Business Media, New York, pp. 91-111. ISBN: 978-1-4614-7365-7. DOI: 10.1007/978-1-4614-7366-4\_6.

- BC6. Mistree, F., Panchal, J.H., and Schaefer, D., 2012, "Mass-Customization: From Personalized Products to Personalized Engineering Education," *Supply Chain Management*, InTech Publishing, pp. 149-173. ISBN: 978-953-51-0367-7.
- BC7. Pezeshki C, Panchal J. H., and Ameta G., 2011, "The Need for Teaching Ecodesign and Sustainability to University Students – Blueprints for Success," *Handbook of Sustainable Engineering* (Editors: W. Wimmer and J. Kauffman), Springer. ISBN: 978-1-4020-8939-8.
- BC8. Chen, K., Bankston, J., Panchal, J. H., and Schaefer, D., 2009, "A Framework for the Integrated Design of Mechatronic Systems", *Collaborative Design and Planning for Digital Manufacturing*, (Editors: L. Wang and A. Nee), Springer, pp. 37-70.
- BC9. Panchal, J. H., Fernández, M. G., Paredis, C. J. J., Allen, J. K., and Mistree, F., 2007, "Leveraging Design Process Related Intellectual Capital – A Key to Enhancing Enterprise Agility," *Collaborative Product Design & Manufacturing Methodologies and Applications* (Editors: W. Li, S. Ong, C. McMahon and A. Nee), Springer-Verlag, pp. 211-244.
- BC10. Panchal, J. H., Choi, H.-J., Allen, J. K., Rosen, D., and Mistree, F., 2007, "An Adaptable Service-based Framework for Distributed Product Realization," *Collaborative Product Design & Manufacturing Methodologies and Applications* (W. Li, S. Ong, C. McMahon and A. Nee, Eds.), Springer-Verlag, pp. 1-37.

<b>Journal Papers</b>
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- J1. Chaduvula, S.C., Dachowicz, A., Atallah, M., and Panchal, J.H., 2018, "Security in Cyber-Enabled Design and Manufacturing: A Survey", *ASME Journal of Computing and Information Science in Engineering*, in press.
- J2. Chaudhari, A.M., Sha, Z., and Panchal, J.H., 2018, "Analyzing Participant Behaviors in Design Crowdsourcing Contests using Game Theoretic Models and Field Data", *ASME Journal of Mechanical Design*, in press. DOI: 10.1115/1.4040166
- J3. Dachowicz, A., Chaduvula, S.C., Atallah, M., Bilonis, I., Panchal, J.H., 2018, "Strategic Information Revelation in Collaborative Design", *Advanced Engineering Informatics*, Vol. 36, No. 2, pp. 242-253. DOI: 10.1016/j.aei.2018.04.004.
- J4. Chaitanya, C.S., Atallah, M., and Panchal, J.H., 2018, "Secure Co-design: Achieving Optimality without Revealing," *ASME Journal of Computing and Information Science in Engineering*, Vol. 18, No. 2, 021007. DOI: 10.1115/1.4039431.
- J5. Pandita, P., Bilonis, I., Panchal, J.H., Gautham, B.P., Joshi, A., Zagade, P., 2018, "Stochastic Multi-objective Optimization on a Budget: Application to multi-pass wire drawing with quantified uncertainties", *International Journal of Uncertainty Quantification*, in press. DOI: 10.1615/Int.J.UncertaintyQuantification.2018021315.
- J6. Wang, G., Ming, Z., Yan, Y., Panchal, J.H., Goh, D., Allen, J.K., Mistree, M., 2018, "Ontology-Based Representation of Design Decision Hierarchies", *ASME Journal of Computing and Information Science in Engineering*, Vol. 18, No. 1, 011001. DOI: 10.1115/1.4037934.
- J7. Taha, A.F., Qi, J., Wang, J., and Panchal, J.H., 2018, "Risk Mitigation for Dynamic State Estimation against Cyber Attacks and Unknown Inputs," *IEEE Transactions on Smart Grids*, Vol. 9, No. 2, pp. 886-899. DOI: 10.1109/TSG.2016.2570546.

- J8. Panchal, J.H., Szajnarfarber, Z., 2017, "Experiments in Systems Engineering and Design Research", *Systems Engineering*. Vol. 20, No. 6, pp. 529-541. DOI: 10.1002/sys.21415
- J9. Panchal, J.H., Sha, Z., Kannan, K., 2017, "Understanding Design Decisions under Competition using Games with Information Acquisition and a Behavioral Experiment", *ASME Journal of Mechanical Design*, Vol. 139, No. 9, 091402. DOI: 10.1115/1.4037253.
- J10. Dachowicz, A., Chaduvula, S.C., Atallah, M., Panchal, J.H., 2017, "Microstructure-based Counterfeit Detection in Metal Part Manufacturing", *JOM: The Journal of the Minerals, Metals & Materials Society*, Vol. 69, No. 11, pp. 2390-2396. DOI: 10.1007/s11837-017-2502-8.
- J11. Wang, S., Bhandari, S., Chaduvula, S., Atallah, M., Panchal, J.H., Ramani, K., 2017, "Secure Collaborations in Engineering System Design," *ASME Journal of Computing and Information Science in Engineering*, Vol 17, No. 4, 041010. DOI: 10.1115/1.4036615.
- J12. Thekinen, J., and Panchal, J.H., 2017, "Resource Allocation in Cloud-based Design and Manufacturing: A Mechanism Design Approach," *Journal of Manufacturing Systems*, Vol. 43, No. 2, pp. 327-338. DOI: 10.1016/j.jmsy.2016.08.005
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#### ***Journal Papers (under review)***

- J57. Shergadwala, M., Billionis, I., Kannan, K., and Panchal, J.H., "Quantifying the Impact of Domain Knowledge and Problem Framing on Sequential Decisions in Engineering Design", *ASME Journal of Mechanical Design*, under review.
- J58. Thekinen, J.D., Moolchandani, K.A., Panchal, J.H., and DeLaurentis, D.A., "Modeling Airlines' Route Selection Decisions under Competition: A Model based on Discrete Games", *AIAA Journal of Air Transportation*, under review.

#### ***Refereed Conference Papers***

- C1. Chaudhari, A., Billionis, I., and Panchal, J.H., 2018, "How do Designers Choose Among Multiple Noisy Information Sources in Engineering Design Optimization?: An Experimental Study," *ASME 2018 International Design Engineering Technical Conferences & Computers and Information in Engineering Conference (IDETC/CIE 2018)*, Quebec City, QC, Canada, August 26-29, 2018. Paper Number: DETC2018-85460.

- C2. Shergadwala, M., Panchal, J.H., and Bilonis, I., 2018, "Students as Sequential Decision-Makers: Quantifying the Impact of Problem Knowledge and Process Deviation On the Achievement of Their Design Problem Objective," *ASME 2018 International Design Engineering Technical Conferences & Computers and Information in Engineering Conference (IDETC/CIE 2018)*, Quebec City, QC, Canada, August 26-29, 2018. Paper Number: DETC2018-85537.
- C3. Chaduvula, S., Atallah, M., and Panchal, J.H., 2018, "sFEA: A lightweight, scalable, and secure finite element analysis technique," *ASME 2018 International Design Engineering Technical Conferences & Computers and Information in Engineering Conference (IDETC/CIE 2018)*, Quebec City, QC, Canada, August 26-29, 2018. Paper Number: DETC2018-85566.
- C4. Sha, Z., Chaudhari, A., and Panchal, J.H., 2018, "Modeling Participation Behaviors in Design Crowdsourcing Using A Bipartite Network-Based Approach," *ASME 2018 International Design Engineering Technical Conferences & Computers and Information in Engineering Conference (IDETC/CIE 2018)*, Quebec City, QC, Canada, August 26-29, 2018. Paper Number: DETC2018-85686.
- C5. Dachowicz, A., Atallah, M., and Panchal, J.H., 2018, "Optical PUF Design for Anti-Counterfeiting in Manufacturing of Metallic Goods," *ASME 2018 International Design Engineering Technical Conferences & Computers and Information in Engineering Conference (IDETC/CIE 2018)*, Quebec City, QC, Canada, August 26-29, 2018. Paper Number: DETC2018-85714.
- C6. Thekinen, J., Han, Y., and Panchal, J.H., 2018, "Designing Market Thickness and Optimal frequency of Multi-period Stable Matching in Cloud-Based Design and Manufacturing," *ASME 2018 International Design Engineering Technical Conferences & Computers and Information in Engineering Conference (IDETC/CIE 2018)*, Quebec City, QC, Canada, August 26-29, 2018. Paper Number: DETC2018-85801.
- C7. Pandita, P., Bilonis, I., and Panchal, J.H., 2018, "Deriving Information Acquisition Criteria For Sequentially Inferring The Expected Value Of A Black-Box Function," *ASME 2018 International Design Engineering Technical Conferences & Computers and Information in Engineering Conference (IDETC/CIE 2018)*, Quebec City, QC, Canada, August 26-29, 2018. Paper Number: DETC2018-85893.
- C8. Safarkhani, S., Bilonis, I., and Panchal, J.H., 2018, "Understanding The Effect Of Task Complexity And Problem-Solving Skills On The Design Performance Of Agents In Systems Engineering," *ASME 2018 International Design Engineering Technical Conferences & Computers and Information in Engineering Conference (IDETC/CIE 2018)*, Quebec City, QC, Canada, August 26-29, 2018. Paper Number: DETC2018-85941.
- C9. Sardeshmukh, A., Reddy, S., Basavarsu, G., Joshi, A., and Panchal, J.H., 2017, "A Data Science Approach for Analysis of Multi-pass Wire Drawing," *ASME 2017 International Design Engineering Technical Conferences & Computers and Information in Engineering Conference (IDETC/CIE 2017)*, Cleveland, OH, USA, August 6-9, 2017. Paper Number: DETC2017-67839.
- C10. Dachowicz, A., Chaduvula, S.C., Panchal J. H., and Atallah, M., 2016, "Confidentiality Management in Collaborative Design," *ASME 2016 International Design Engineering Technical Conferences & Computers and Information in Engineering Conference (IDETC/CIE 2016)*, Charlotte, NC, USA, August 21-24, 2016. Paper Number: DETC2016-59222.
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- C18. Chaduvula S. C., Kriegel T., and Panchal J. H., 2015, "Estimation of CO<sub>2</sub> Emissions Considering the Decisions of Multiple Drivers within Car-Following Models," *ASME 2015 International Design Engineering Technical Conferences & Computers and Information in Engineering Conference* (IDETC/CIE 2015), Boston, MA, USA, August 2-5, 2015. Paper Number: DETC2015-46215.
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- C32. Shukla, R., Goyal, S., Singh, A.K., Panchal, J.H., Allen, J.K. and Mistree, F. "An Approach to Robust Process Design for Continuous Casting of Slab." *2014 ASME International Design Technical Conferences (I-DETC) and Computers and Information in Engineering (CIE) Conference*. Buffalo, N.Y., August 17-20, 2014, Paper Number: DETC204-34208
- C33. Anapagaddi, R., Shukla, R., Goyal, S., Singh, A.K., Allen, J.K., Panchal, J.H., and Mistree, F. "Exploration of the Design Space in Continuous Casting Tundish." *2014 ASME International Design Technical Conferences (I-DETC) and Computers and Information in Engineering (CIE) Conference*. Buffalo, N.Y., August 17-20, 2014, Paper Number: DETC2014-34254.
- C34. Kulkarni, N., Gupta, R., Khan, D., Gautham, B.P., Allen, J.K. Panchal, J.H., and Mistree, F. "Inverse Design of Manufacturing Process Chains." *2014 ASME International Design Technical Conferences (I-*

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- C36. Sha, Z., and Panchal, J.H., 2013, "Towards the Design of Complex Evolving Networks with High Robustness and Resilience," *Procedia Computer Science*, Vol. 16, *Proceedings of the 2013 Conference on Systems Engineering Research (CSER)*, March 19-22, Atlanta, GA), pp. 522-531.
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- C42. Rytsareva, I., Le, Q., Conner, E., Kalyanaraman, A., and Panchal, J.H., 2012, "Evaluating Socio-Technical Coordination in Open-Source Communities: A Cluster-based Approach," *2012 ASME Computers and Information in Engineering*, Chicago, IL. Paper Number: DETC2012-70604.
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