WHY DO COMPUTER SYSTEMS MATTER?

- Implement component-level feedback control
- Implement system-level supervisory control
- Signal processing
- Communication with other systems
- Easily modified through software

We cover basics of computer system operation in **UNIT 3** and **UNIT 4**
UNIT 3: COMBINATIONAL LOGIC
TOPICS

- Part A: Boolean Algebra
- Part B: Boolean Simplification (K-map)
- Part C: Implementing Boolean Logic
- Part D: Digital Interfacing
COMBINATIONAL LOGIC

At the end of this section, students should be able to:

- Explain the value of Computer Systems to Mechatronics
- Write simple logical statements using Boolean algebra
- Perform graphical minimization on combinational logic
- Implement combinational logic using ICs
UNIT 3: COMBINATIONAL LOGIC

PART A: BOOLEAN ALGEBRA
BOOLEAN ALGEBRA INVENTED BY GEORGE BOOLE

*George Boole* – English mathematician (1815-1864)
Boolean variables may assume two (and only two) values:

- 0/1
- TRUE/FALSE
- ON/OFF
- OPEN/CLOSE
- HI/LO
GENERAL SYSTEMS MAY BE CONSIDERED ‘STATIC’ OR ‘DYNAMIC’

Static Systems

• Outputs depends on the current values of the inputs
• Described by *algebraic* equations
• Can be implemented as a look-up table (LUT)

Dynamic Systems

• Outputs depends on the current and past values of the inputs
• Described by differential equations (calculus)
BOOLEAN BEHAVIOR MAY ALSO BE ‘STATIC’ OR ‘DYNAMIC’

Static Behavior

• Described by combinational logic (Unit 3)
• Subject to mathematical tools of Boolean algebra

Dynamic Behavior

• Described by sequential logic (Unit 4)
• Developed *ad hoc* (no formal calculus for binary systems)
BINARY REPRESENTATION USES ONES AND ZEROS

Expression of numerical values using a base-2 system (0/1) is called a *binary* representation.

\[ 5_{10} = 101_2 \]

Boolean logic *is not* dependent upon using binary values to represent Boolean values. However, it is quite convenient for use in digital computing devices.
DIGITAL DATA

Digital:
1s and 0s, \((1001\ 1011)_2\)

Advantages
- Less susceptible to noise
- Easy to manipulate using a computer

Disadvantages
- Finite precision
- Sample loses information
- Time lag associated with sample and hold

Analog:
3.141592687..., 1/3

Advantages
- Exact – infinite resolution
- Instantaneous

Disadvantages
- Noise
- Repeatability
- Difficult to manipulate
**BOOLEAN ALGEBRA**

**NOT** $(\bar{x} \text{ or } x' \text{ or } /x)$

- Truth Table:

<table>
<thead>
<tr>
<th>$x$</th>
<th>$\bar{x}$</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td>1</td>
</tr>
<tr>
<td>1</td>
<td>0</td>
</tr>
</tbody>
</table>

- Map:

```
 x
 0  1
\bar{x} 1 0
```
BOOLEAN ALGEBRA

AND (●)

• Truth Table:

<table>
<thead>
<tr>
<th>x</th>
<th>y</th>
<th>x • y</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td>0</td>
<td>0</td>
</tr>
<tr>
<td>0</td>
<td>1</td>
<td>0</td>
</tr>
<tr>
<td>1</td>
<td>0</td>
<td>0</td>
</tr>
<tr>
<td>1</td>
<td>1</td>
<td>1</td>
</tr>
</tbody>
</table>

• Map:

```
 x AND y
```

```
 x
 0  1
```

```
 y
 0  0  0
 1  0  1
```
**BOOLEAN ALGEBRA**

**OR** ( + )

- **Truth Table:**

<table>
<thead>
<tr>
<th>x</th>
<th>y</th>
<th>x + y</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td>0</td>
<td>0</td>
</tr>
<tr>
<td>0</td>
<td>1</td>
<td>1</td>
</tr>
<tr>
<td>1</td>
<td>0</td>
<td>1</td>
</tr>
<tr>
<td>1</td>
<td>1</td>
<td>1</td>
</tr>
</tbody>
</table>

- **Map:**

  \[
  \begin{array}{c|c|c}
  x \lor y & x & \\
  \hline
  0 & 0 & 1 \\
  1 & 1 & 1 \\
  \end{array}
  \]
**BOOLEAN ALGEBRA**

**XOR** (⊕) "Exclusive OR"

- Truth Table:

<table>
<thead>
<tr>
<th>x</th>
<th>y</th>
<th>x ⊕ y</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td>0</td>
<td>0</td>
</tr>
<tr>
<td>0</td>
<td>1</td>
<td>1</td>
</tr>
<tr>
<td>1</td>
<td>0</td>
<td>1</td>
</tr>
<tr>
<td>1</td>
<td>1</td>
<td>0</td>
</tr>
</tbody>
</table>

- Map:

```
 x XOR y
```

```
           x
0  1
0  0  1
1  1  0
```
BOOLEAN ALGEBRA EQUATIONS

- Write equations in the “normal” way
- Hierarchy and parentheses usage borrowed from “ordinary” algebra
- Unlike “ordinary” algebra, the AND and OR operators are dual operators – if one substitutes 0 for 1, and also substitutes AND for OR, the result is unchanged
**BOOLEAN ALGEBRA AXIOMS**

(Axioms define domain characteristics, from which other ‘truths’ can be derived.)

- **Commutativity:**  
  \[ x + y = y + x \]  
  \[ x \cdot y = y \cdot x \]

- **Associativity:**  
  \[ (x + y) + z = x + (y + z) \]  
  \[ (x \cdot y) \cdot z = x \cdot (y \cdot z) \]

- **Distributivity:**  
  \[ x \cdot (y + z) = x \cdot y + x \cdot z \]  
  \[ x + (y \cdot z) = (x + y) \cdot (x + z) \]

- **Existence of 0 and 1:**  
  \[ x + 0 = x \]  
  \[ x \cdot 1 = x \]

- **Existence of complements:**  
  \[ x \cdot \bar{x} = 0 \]  
  \[ x + \bar{x} = 1 \]
BOOLEAN ALGEBRA THEOREMS
(Theorems are proven through algebraic manipulation or exhaustive substitution.)

- **Idempotent Operators:**
  
  \[ x + x = x \]
  \[ x \cdot x = x \]

- **Absorption:**

  \[ x + x \cdot y = x \]
  \[ x \cdot (x + y) = x \]

- **Simplification:**

  \[ x + \overline{x} \cdot y = x + y \]
  \[ x \cdot (\overline{x} + y) = x \cdot y \]

- **DeMorgan’s Law:**

  \[ (x + y) = \overline{x} \cdot \overline{y} \]
  \[ (x \cdot y) = \overline{x} + \overline{y} \]
COMBINATIONAL LOGIC DESIGN

Use Boolean algebra to go from design specification to implementable logic function

Boolean algebra can be used to design systems that have:

- Binary inputs
- Binary outputs
- No history dependence or memory
EXAMPLE: ELECTRONIC DOOR LOCK

- The lock has a set of buttons. To enter (open the lock), one must simultaneously press the correct combination of buttons.
- Inputs: buttons B1 and B2, Button-IN = 1, Button-OUT = 0
- Output: Lock actuator (L), 0 = Lock, 1 = Unlock
- Truth Table: (two button lock)

<table>
<thead>
<tr>
<th>B1</th>
<th>B2</th>
<th>L</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td>0</td>
<td>0</td>
</tr>
<tr>
<td>0</td>
<td>1</td>
<td>0</td>
</tr>
<tr>
<td>1</td>
<td>0</td>
<td>1</td>
</tr>
<tr>
<td>1</td>
<td>1</td>
<td>0</td>
</tr>
</tbody>
</table>
SUM OF PRODUCTS

Generate an algebraic expression for desired function from a truth table by creating a sum-of-products expression:

- Form a sum-of-products function:
  \[ y = (C_1) + (C_2) + \cdots + (C_{n-1}) + (C_n) \]
  where each \( C_i \) term expresses one of \( n \) possible combinations of the input variables, or their complements, as Boolean products. Treat ones in the truth table as uncomplemented variables, and zeros as complimented variables.

- Drop terms corresponding to an output of zero in the truth table.
SUM OF PRODUCTS

All Boolean design flows from this procedure. It is the Boolean equivalent of regression, but it is exact!
EXAMPLE: TWO BUTTON DOOR LOCK

<table>
<thead>
<tr>
<th>$B1$</th>
<th>$B2$</th>
<th>$L$</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td>0</td>
<td>0</td>
</tr>
<tr>
<td>0</td>
<td>1</td>
<td>0</td>
</tr>
<tr>
<td>1</td>
<td>0</td>
<td>1</td>
</tr>
<tr>
<td>1</td>
<td>1</td>
<td>0</td>
</tr>
</tbody>
</table>

- Form a sum-of-products function:
  
  \[ y = (\overline{B1} \cdot \overline{B2}) + (\overline{B1} \cdot B2) + (B1 \cdot \overline{B2}) + (B1 \cdot B2) \]

- Drop terms associated with an output of zero in the truth table:
  
  \[ y = (\overline{B1} \cdot \overline{B2}) + (\overline{B1} \cdot B2) + (B1 \cdot \overline{B2}) + (B1 \cdot B2) \]

  \[ y = (B1 \cdot \overline{B2}) \]
PRODUCT OF SUMS

Complementary to Sum-of-Products

- All rules are dual

Example: Two button door lock

- Form product of sums function:
  \[ y = \overline{B1} \cdot \overline{B2} \cdot (\overline{B1} + B2) \cdot \overline{B1} \cdot B2 \cdot (B1 + B2) \]

- Drop terms with truth table output of 1:
  \[ y = \overline{B1} \cdot \overline{B2} \cdot (\overline{B1} + B2) \cdot B1 \cdot B2 \cdot (B1 + B2) \]
  \[ y = \overline{B1} \cdot \overline{B2} \cdot (B1 + B2) \cdot (B1 + B2) \]

In this example, the product-of-sums expression is more complex than the sum-of-products.

- Complexity is problem dependent
- Function minimization can be done algebraically and graphically.
COMING UP...

Combinational Logic

- Minimizing combinational logic
- Implementing combinational logic

Then, we investigate sequential logic...