**Prerequisites information for ECE565, Computer Architecture**

ECE 565 will assume that you know basic 5-stage pipeline, basic caches, and basic virtual memory.

(a) If you have had undergrad 437 equivalent (where students write RTL and FPGA-synthesize a 5-stage pipeline, caches, and dual-core atomic-bus coherence - so not just textbook exercises but actual implementation) that is best.

(b) Else if you have had undergrad compilers or OS, that will help.

(c) Else if you have had assembly programming, that is the bare minimum.

(d) Else 565 will be all but impossible.

If you are in group (c) then be prepared to learn 437 (on your own)
AND 565 simultaneously.