#### **PURDUE UNIVERSITY**

#### REQUEST FOR ADDITION, EXPIRATION, OR REVISION OF AN UNDERGRADUATE COURSE (10000-40000 LEVEL)

5-12

DEPARTMENT	Engineering To	echnology		EFFECTIVE SES	SION Spring	2012	
INSTRUCTIONS	: Please check the it	ems below which	describe the purpose of the	his request.			
	<ol> <li>New course with</li> </ol>	ith supporting d	ocuments		7. Change	in course attributes (	department head signature only)
	<ol><li>Add existing co</li></ol>	ourse offered at	another campus		8. Change	in instructional hours	
	<ol><li>Expiration of a</li></ol>	course		Γ	9. Change	in course description	
1 7 4	4. Change in cou	rse number		Ī	10. Change	in course requisites	
	5. Change in cou	rse title			11. Change	in semesters offered	(department head signature only)
	6. Change in cou	rse credit/type		ŗ	12. Transfer	from one departmen	t to another
			EVIOTINO.		<del></del>		TOMO OFFICER
PROPOSED:	. —		EXISTING:	ECE		ſ	ERMS OFFERED ::heck All That Apply:
Subject Abbreviation	n		Subject Abbreviation	ECE			
l			1		26200	Summer	✓ Fall ✓ Spring
Course Number			Course Number		36200	1	PUS(ES) INVOLVED
 	C:					Calumet	N. Central
Long Title [IVII	croprocessor Sy	stems and in	enacing			Cont Ed	Tech Statewide
l						Ft. Wayne	W. Lafayette
Short Title						☐ Indianapolis	
Abb	reviated title will be entere	d by the Office of the	Registrar if omitted. (30 CHARACT	TERS ONLY)			
	CREDIT TYPE			COUR	SE ATTRIBUTES	S: Check All That Apply	
1. Fixed Credit: Cr.	Hrs. 4		1. Pass/Not Pass Only		6 Registratio	n Approval Type	
2. Variable Credit F			2. Satisfactory/Unsatisfactory	Only	D	epartment	Instructor
Minimum Cr.	-		3. Repeatable		7 Variable Ti	•	
(Check One)	То 🔲 О	r 🗌 📗	Maximum Repeatable	Credit:	8 Honors		Ä l
Maximum Ci	. Hrs		Credit by Examination	一	9 Full Time F	Privilege	
<ol><li>Equivalent Credi</li></ol>			5. Fees Coop Lab	Rate Request	10 Off Campu	=	
	H	$\vdash$	Include comment to explai			a Enponent	
Schedule Type	Minutes	Meetings Per	Weeks % of Credit				? =
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Lecture Recitation							ୁଲ <b>ପ୍ର</b>
Presentation							~ C 🔊
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Lab Prep Studio							
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COURCE DESCRIE	TION (INCLUDE REQ	LICITEC/DECEDIO:	FIONIC).				77
			6400 & ECE 27000.			·····	
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			i sets, assembly lang	juage programm	ing, microcom	troller interracing, n	nicrocontroller peripherals,
and embedde	d system design	1.					
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*COURSE LEARNI	NG OUTCOMES						-
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Calumet Departmer	nt Head	Date	Calumet School Dean		Date		
Fort Wayne Departs	ment Head	Date	Fort Wayne School Dean		Date		
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Indianapolis Depart	ment Head	Date	Indianapolis School Dean	^	Date		
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North Central Facul	y Senate Chair	Date	Vice Chancellor for Academ	id Affairs	Date		
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Vest Lafayette Dep	artment Head	Date	West Lafayette College/Sch	ool Dean	Date V	West Lafavette Registrar	The Park
		•					JO . I Date

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## **Purdue North Central Curriculum Document**

Submission Date: (Date sent to College Curr Comm)	Nov 18, 2008	Document No: (Leave blank)	08-C-11		
Proposed Effective Date: (Semester, Year)	Fall 2009	Submitting Dept: (Name of dept/pgm)	Engineering		
Reviewed by College: (Date reviewed by College CC)	Jan 9, 2009	Contact Person: (Name & Title)	Larryl Matthews, Dean, College of Engineering & Technology		
Name(s) of Library Staff Consulted: (N/A if not required.)	N/A	Will New Library Resources Used?	Yes No Double-click to check Yes / No.		
Approval by Curriculum Committee: (Leave blank)	Jan 30, 2009	Form 40 Needed? (Double-click one box.)	Yes New courses or <u>any</u> course change, check YES.		
Approval by Faculty Senate: (Leave blank)	March 20, 2009	Send Form 40 to PNC Registrar <u>after</u> Senate approval of document.	No For <u>all other</u> curriculum matters, check NO.		
Subject. (Brief description of propos	ed change, addition or de	letion.)			
New Bachelor of Science degree	e in Electrical & Comput	er Engineering (BS ECE	≣).		
Justification. (Briefly list main reas	sons for proposed change	, addition or deletion.)			
The freshman engineering program has existed at PNC for many years. Since being approved two years ago, the BS Mechanical Engineering degree, with a Minor in Electrical Engineering, has experienced strong enrollment. Currently, engineering students seeking to major in Electrical & Computer Engineering (ECE) must transfer to West Lafayette or Calumet. The proposed BS ECE Degree will retain many students who would otherwise transfer, thus increasing our enrollment and helping the students to lower the cost of their education.					
Use the <u>Current</u> and	Proposed spaces below	for course changes only.	Otherwise, mark "N/A".		
Current: (Course changes: presen	t catalog info.)	Proposed: (Course	changes: <u>new</u> catalog information.)		
N/A. (New program.)		See following pag	es for sample plan of study and list of		
N/A. (New program.)			ses that will be brought to PNC.		
Course Objectives. (For new cou	ırses only. List main learr	West Lafayette cours	ses that will be brought to PNC.		
	g this program will: dergraduate engineerin modern sciences and to ip services to local indu- iduate research activitie	West Lafayette cours  ning objectives. If lengthy, g education, echnologies, stry. s.	ses that will be brought to PNC.  attach as separate page.)		
Course Objectives. (For new countries of Students successfully completing (1) Have a well rounded, quality und (2) Be able to apply applications of (3) Provide engineering or internshal (4) Have been involved in undergra	g this program will: dergraduate engineering modern sciences and to provide the services to local industrial aduate research activities aduate engineering com	West Lafayette cours  ning objectives. If lengthy, g education. echnologies. stry. s. petitions and participate	ses that will be brought to PNC.  attach as separate page.)		
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Course Objectives. (For new council Students successfully completing (1) Have a well rounded, quality und (2) Be able to apply applications of (3) Provide engineering or internsh (4) Have been involved in undergrad (5) Have been involved in undergrad Impact on Students. (State "N/A")	g this program will: dergraduate engineering modern sciences and to ip services to local indust iduate research activities iduate engineering com " if proposal will not great to the cost of transfering	West Lafayette cours  ning objectives. If lengthy, g education. echnologies. stry. s. petitions and participate ly affect students.) g to West Lafayette or co	e in local engineering societies.		
Course Objectives. (For new council Students successfully completing (1) Have a well rounded, quality und (2) Be able to apply applications of (3) Provide engineering or internsh (4) Have been involved in undergrad (5) Have been involved in undergrad (6) Have been involved (6) Have b	g this program will: dergraduate engineering modern sciences and to ip services to local indust iduate research activities iduate engineering com " if proposal will not great to the cost of transfering ees. (State "N/A" if propose	West Lafayette cours  ning objectives. If lengthy, g education. echnologies. stry. s. petitions and participate ly affect students.) g to West Lafayette or co	e in local engineering societies.  commuting to Calumet.  sources, faculty or funds.)		
Course Objectives. (For new council Students successfully completing (1) Have a well rounded, quality und (2) Be able to apply applications of (3) Provide engineering or internsh (4) Have been involved in undergrad (5) Have been involved in undergrad (6) Have been involved in undergrad (7) Have been involved (7) Have been involved (7) Have been involve	g this program will: dergraduate engineering modern sciences and to ip services to local indust iduate research activities iduate engineering come " if proposal will not great to the cost of transfering ess. (State "N/A" if propose ECET laboratories and  nits. (State "N/A" if propose	West Lafayette cours  ning objectives. If lengthy, g education. echnologies. stry. s. petitions and participate ly affect students.) g to West Lafayette or cours sal will not require new res current full- and part-timesal will not affect other un	e in local engineering societies.  commuting to Calumet.  sources, faculty or funds.)		

## Sample Plan of Study for BS-ECE Degree.

SEMESTER 1	CR	SEMESTER 2	CR
MA 167 Plane Analytic Geometry & Callculas I	5	MA 169 Plane Analytic Geometry & Calculas II	5
CHM 115 General Chemistry	4	CHM 116 General Chemistry	4
ENGL 101 English Composition I	3	PHYS 152 Mechanics	4
ENGR 171 Engineering Fundamentalsl	5	ENGR 181 Engineering FundamentalsII	5
CREDIT HOURS	17	CREDIT HOURS	18
SEMESTER 3	CR	SEMESTER 4	CR
ECE 201 Linear Circuit Analysis I	3	ECE 202 Linear Circuit Analysis II	3
ECE 207 Electronic Measurement Techniques	1	ECE 255 Intro. to Electronics Analysis & Design	3
PHYS 261 Electricity and Optics	4	ECE 208 Electronic Design & Dev. Lab	1
MA 261 Multivariate Calculas	4	MA 262 Linear Algebra & Diff. Eqs.	4
ME 270 Basic Mechanics I	3	ECE 270 Intro. to Digital Sys. Design	4
Humanities/Social Science Elective	3	Humanities/Social Science Elective	3
CREDIT HOURS	18	CREDIT HOURS	18
SEMESTER 5	CR	SEMESTER 6	CR
ECE 264 Advanced C programming	2	ECE 311 Electric and Magnetic Fields	3
ECE 321 Electromech. Mot. Devices ECE 323 Electro. & Motion Dev.Lab.	3 1	ECE 382 Feedback System Analysis ECE 308 Systems Simulation and Control Lab	3 1
ECE 301 Signals and Systems	3	ECE 362 Microprocessor Systems & Interfacing	3
ECE 302 Prob. Methods in Elect. Engineering	3	Humanities/Social Science Elective	3
Humanities/Social Science Elective	3		
CREDIT HOURS	15	CREDIT HOURS	13
SEMESTER 7	C	SEMESTER 8	CR
ECE 402 EE Design Projects	3	ENGR 461 Engineering Design Exp.	3
ECE 440 Transmission of Information	4	ECE 438 Digital Signal Processing	4
ENGR Elective	3	ENGR Elective	3
Humanities/Social Science Elective	3	Humanities/Social Science Elective	3
CREDIT HOURS	13	CREDIT HOURS	13

**TOTAL CREDIT HOURS FOR DEGREE: 124** 

## Courses to Be Brought from West Lafayette.

## CE 208 Electronic Devices and Design Laboratory

Lab. 3. Cr. 1. Prerequisite: ECE 207. Corequisites: ECE 255.

Laboratory experiments in the measurement of electronic device characteristics. Design of biasing networks, small signal amplifiers and switching circuits.

## √ ECE 255 Electronic Circuit analysis and Design

Class 3, Cr 3. Prerequisites: ECE 201.

Diode, bipolar transistor and FET circuit models for the design and analysis of electronic circuits. Single and multistage analysis and design; introduction to digital circuits. Computer aided design calculations, amplifier operating point design, and frequency response of single and multistage amplifiers. High frequency and low frequency designs are emphasized.

## , ECE 264 Advanced C Programming

Class 2, Cr. 2. Prerequisite: ENGR 181 or ENGR 195E.

Continuation of a first programming course. Topics include files, structures, pointers, and the proper use of dynamic data structures.

## ✓ECE 270 Introduction to Digital System Design

Class 3, Lab. 3. Cr. 4. Prerequisites: ECE 201 and ECE 207.

An introduction to digital system design, with an emphasis on practical design techniques and circuit implementation.

## ✓ ECE 301 Signals and Systems

Class 3, Cr. 3. Prerequisite: ECE 202...

assification, analysis and design of systems in both the time- and frequency-domains. Continuous-time linear systems: Fourier Series, Fourier Transform, bilateral Laplace Transform. Discrete-time linear systems: difference equations, Discrete-time Fourier Transform, bilateral z-Transform. Sampling, quantization, and discrete-time processing of continuous-time signals. Discrete-time nonlinear systems: median-type filters, threshold decomposition. System design examples such as the compact disc player and AM radio.

## ✓ ECE 302 Probabilistic Methods in Electrical and Computer Engineering

Class 3, Cr. 3. Prerequisite: MA 262.. Corequisite: ECE 301.

An introductory treatment of probability theory including distribution and density functions, moments and random variables. Applications of normal and exponential distributions. Estimation of means, variances. Correlation and spectral density functions. Random processes and response of linear systems to random inputs.

## ECE 308 Systems Simulation and Control Laboratory

Class 3, Cr. 1. Prerequisite: ECE 207. Corequisite: ECE 382

Instruction and laboratory exercises in the solution of differential equations that arise in the modeling of physical systems. Instruction in the principles of operation and design of linear control systems.

## ECE 311 Electric and Magnetic Fields

Class 3, Cr. 3. Prerequisites: ECE 201, PHYS 261 & MA 262.

Continued study of vector calculus, electrostatics, magnetostatics, and Maxwell's Equations. Introduction to electromagnetic waves, transmission lines, and radiation from antennas.

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## Courses to Be Brought from West Lafayette (cont.).

#### **CE 321 Electromechanical Motion Devices**

Class 3, Cr. 3. Prerequisite: ECE 202 or ECE 255.

The general theory of electromechanical motion devices relating electric variables and electromagnetic forces. The basic concepts and operational behavior of dc, induction, brushless dc, and stepper motors used in control applications are presented.

## ECE 323 Electromechanical Motion Devices and Systems Laboratory

Lab. 3, Cr. 1. Corequisite: ECE 321.

Experiments closely coordinated with EE 321 involving measurement of fundamental parameters of various electromechanical devices using modern instrumentation techniques. Computer simulation is used to predict steady-state and dynamic operating characteristics. Comparison of predicted and measured performance is emphasized.

## ✓ ECE 362 Microprocessor Systems and Interfacing

Class: 3, Cr. Lab. 3, Cr. 4. Prerequisites: ECE 264 & ECE 270.

An introduction to microcontroller instruction sets, assembly language programming, microcontroller interfacing, microcontroller peripherals, and embedded system design.

## ECE 382 Feedback System Analysis and Design

Class 3, Cr. 3. Prerequisite: ECE 202. Corequisite: ECE 308.

In this course classical concepts of feedback system analysis and associated compensation techniques are presented. In particular, the root locus, Bode diagram and Nyquist plot are used as determinants of stability.

## CE 402 EE Design Projects

Class 1, Lab. 6, Cr. 3. Prerequisite: Senior standing.

Lecture sessions provide the student with background information on the design and management of projects. Formal lectures cover, for example, design for manufacturability, design for quality, test and evaluation, reliability and ethics, patents and copyrights, plus case studies. During the laboratory sessions the students work in teams on a challenging open-ended electrical engineering project that draws on previous course work. Projects routinely involve standard design facets (such as consideration of alternative solutions, feasibility considerations and detailed system descriptions) and include a number of realistic constraints (such as cost, safety, reliability, and aesthetics).

## √ECE 438 Digital Signal Processing with Applications

Class 3, Lab. 3, Cr. 4. Prerequisites: ECE 301 & ECE 302.

The course is presented in three units. Foundations: the review of continuous-time and discrete-time signals, and spectral analysis; design of finite impulse response and infinite impulse response digital filters; processing of random signals. Speech processing: vocal tract models and characteristics of the speech waveform; short-time spectral analysis and synthesis; linear predictive coding. Image processing: two dimensional signals, systems, and spectral analysis; image enhancement; image coding; image reconstruction. The laboratory experiments are closely coordinated with each unit. Throughout the course, the integration of digital signal processing concepts in a design environment is emphasized.

## √ECE 440 Transmission of Information

Class 3, Lab: 3, Cr.: 4. Prerequisites: ECE 301 & ECE 302.

Analysis and design of Analog and Digital Communication Systems. Emphasis on engineering applications of theory to communication system design. The laboratory introduces the use of advanced engineering workstations in the design and sting of communication systems.

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FORM 40 REV. 11/09

# REQUEST FOR ADDITION, EXPIRATION, OR REVISION OF AN UNDERGRADUATE COURSE (10000-40000 LEVEL)



`	(10000-100	ood LEVEL)	EF D	5-12-
EPARTMENT School of Electrical and Computer	Engineering (EFD 5-12) EFF	ECTIVE SESSION Spring 2012		
ASTRUCTIONS: Please check the items below	which describe the purpose of this re	equest.		
1. New course with supporting docur 2. Add existing course offered at and 3. Expiration of a course 4. Change in course number 5. Change in course title 6. Change in course credit/type		8. Change in instruction 9. Change in course of 10. Change in course of 11. Change in semeste	onal hours description equisites	nt head signature only) ant head signature only) er
PROPOSED:	EXISTING:		TER	RMS OFFERED
Subject Abbreviation	Subject Abbreviation E	CE	1	* All That Apply:
Course Number	Course Number 3	6200		X Fall X Spring JS(ES) INVOLVED
Long Title Microprocessor Systems and Interfa			Calumet	N. Central
Short Title Microprocessor Systems and Int			Cont Ed Ft. Wayne	Tech Statewide
	by the Office of the Registrar if omit	ted. (30 Characters Only)	Indianapolis	<u> </u>
CREDIT TYPE		COURSE ATTRIBUTES: Check A	Ill That Apply	
(Check One) To Or  Maximum Cr. Hrs.  3.Equivalent Credit: Yes No	Pass/Not Pass Only     Satisfactory/Unsatisfactory Only     Repeatable     Maximum Repeatable Credit:     Credit by Examination     Special Fees	6. Registration Depa 7. Veriable Title 8. Honors 9. Full Time F	on Approval Type rtment	Instructor
ScheduleType Minutes Per Mtg Week  Lecture Recitation resentation poratory Lab Prep Studio Distance Clinic Experiential Research Ind. Study Pract/Obsery	Offered Allocated	Total 10. VII vening	MS EADOIRO	Cross Lated Comes
COURSE DESCRIPTION (INCLUDE REQUISITES Prerequisites: ECE 27000 and (CS 15900 or ENG Restrictions: Must be enrolled in: School of Electric Electric School of Electric Ele	R 11700)	**************************************		
See attachment.			<u></u>	
Calumet Department Head Date	Celumet School Dean	Date		:
Fort Wayne Department Head Date	Fort Wayne School Dean	Date		
Indianapolis Department Head Date	Indianapolis School Dean	Date		
North Central Department Head Date  8/30/11  41 Moderate Department Head Plate	North Cartral Chancellar	Date   Date	Barkon	harber 0/30/1

OFFICE OF THE REGISTRAR

Engineering Faculty Document No. 5-12 August 17, 2011 Page 1 of 1

TO:

The Faculty of the College of Engineering

FROM:

The Faculty of the School of Electrical and Computer Engineering

RE:

Change to Existing Undergraduate Course: ECE 36200, Microprocessor Systems

and Interfacing, change in requisites.

The faculty of the School of Electrical and Computer Engineering has approved the following changes to an existing course. This action is now submitted to the Engineering Faculty with a recommendation for approval.

From:

ECE 36200 Microprocessor Systems and Interfacing

Sem. Fall and Spring; Cr. 4; Lecture 3.

Prerequisites: ECE 27000 and (Undergraduate level CS 15900 or Undergraduate level ENGR 11700) and Undergraduate level ECE 25500 [may be taken concurrently].

Restrictions: Must be enrolled in one of the following: School of Electrical & Computer

Engineering

Description: An introduction to microcontroller instruction sets, assembly language programming, microcontroller interfacing, microcontroller peripherals, and embedded system

design.

To:

ECE 36200 Microprocessor Systems and Interfacing

Sem. Fall and Spring; Cr. 4; Lecture 3.

Prerequisites: ECE 27000 and (CS 15900 or ENGR 11700)

Restrictions: Must be enrolled in: School of Electrical & Computer Engineering Description: An introduction to microcontroller instruction sets, assembly language programming, microcontroller interfacing, microcontroller peripherals, and embedded system

design.

Reason:

The Computer Engineering faculty in ECE has determined that ECE 2700 and CS 15900 are

sufficient as the only prerequisites for ECE 36200.

chool of Electrical and Computer Engineering

AFFREVER FOR THE FACULTY OF THE SCHOOLS OF ENGINEERING BY THE ENGINEERING CURRICULUM COMMITTEE

ECC Minutes \_ # 5

Chairman ECC R. Copia



## School of Electrical and Computer Engineering (EFD 5-12)

#### **Course Learning Outcomes:**

- i. an ability to write programs for a microcontroller in assembly language. [1,4; e,k]
- ii. an ability to interface a microcontroller to various devices. [1,3,4; a,b,c,e,k]
- lii. an ability to effectively utilize microcontroller peripherals. [3,4;j,k]
- iv. an ability to design and implement a microcontroller-based embedded system. [3,4,5; a,c,d,e,g,k]