

TO: The Faculty of the College of Engineering

FROM: School of Electrical and Computer Engineering of the College of Engineering

RE: New Graduate Course, ECE 60420 Radio Frequency Integrated Circuits

The faculty of the School of Electrical and Computer Engineering has approved the following new course. This action is now submitted to the Engineering Faculty with a recommendation for approval.

ECE 60420 Radio Frequency Integrated Circuits
Sem. 1, Lecture 3, Cr. 3.

Prerequisite by Topic: Solid-state devices, Electromagnetics and analog and digital circuits

Description: This course aims at analysis and design of CMOS integrated radio frequency (RF), microwave and mm-wave circuits. Various modules of an RF transceiver are discussed including, low noise amplifiers, mixers, oscillators, frequency synthesizers and power amplifiers. A term project on design of an RF to mm-wave module is also required.

Reason: With the proliferation of cell phones and other wireless mobile devices, and the ever-increasing performance requirement, analysis, design and low-cost implementation of radio frequency, microwave and mm-wave integrated circuits have become important topics for graduate studies in electrical engineering. Almost every respected graduate program in devices and circuits offers such a course at an advanced level. Students who may be interested in this course should have a background in applied electromagnetic, analog and digital circuits and solid-state devices. The appropriate level for this course is therefore a 600 level. The targeted audience is 2nd and 3rd year graduate students in vlsi and circuit, microelectronic and nanotechnology and field and optics who have completed the pre-requisites and are planning to widen their knowledge base in this area. The course goes through many topics in a very fast pace to introduce students to all the important topics. Students choose one of the topics for their term project to go deeper and gain a much better understanding of their topic. For their term project, students must do an in-depth literature search, analysis and design and simulation and optimization of their topic.



Michael R. Melloch, Associate Head
School of Electrical and Computer Engineering

PURDUE UNIVERSITY
REQUEST FOR ADDITION, EXPIRATION,
OR REVISION OF A GRADUATE COURSE
(50000-60000 LEVEL)

DEPARTMENT Electrical and Computer Engineering EFFECTIVE SESSION Spring 2016

INSTRUCTIONS: Please check the items below which describe the purpose of this request.

- | | |
|--|--|
| <input checked="" type="checkbox"/> 1. New course with supporting documents (complete proposal form) | <input type="checkbox"/> 7. Change in course attributes |
| <input type="checkbox"/> 2. Add existing course offered at another campus | <input type="checkbox"/> 8. Change in instructional hours |
| <input type="checkbox"/> 3. Expiration of a course | <input type="checkbox"/> 9. Change in course description |
| <input type="checkbox"/> 4. Change in course number | <input type="checkbox"/> 10. Change in course requisites |
| <input type="checkbox"/> 5. Change in course title | <input type="checkbox"/> 11. Change in semesters offered |
| <input type="checkbox"/> 6. Change in course credit/type | <input type="checkbox"/> 12. Transfer from one department to another |

PROPOSED: Subject Abbreviation <u>ECE</u> Course Number <u>60420</u> Long Title <u>Radio Frequency Integrated Circuits</u> Short Title <u>Radio Frequency Integrated Cir</u> <small>Abbreviated title will be entered by the Office of the Registrar if omitted. (30 CHARACTERS ONLY)</small>		EXISTING: Subject Abbreviation _____ Course Number _____		TERMS OFFERED Check All That Apply: <input checked="" type="checkbox"/> Fall <input type="checkbox"/> Spring <input type="checkbox"/> Summer
CAMPUS(ES) INVOLVED <input type="checkbox"/> Calumet <input type="checkbox"/> N. Central <input type="checkbox"/> Cont Ed <input type="checkbox"/> Tech Statewide <input type="checkbox"/> Ft. Wayne <input checked="" type="checkbox"/> W. Lafayette <input type="checkbox"/> Indianapolis				

CREDIT TYPE 1. Fixed Credit: Cr. Hrs. <u>3</u> 2. Variable Credit Range: Minimum Cr. Hrs. _____ (Check One) To <input type="checkbox"/> Or <input type="checkbox"/> Maximum Cr. Hrs. _____ 3. Equivalent Credit: Yes <input type="checkbox"/> No <input checked="" type="checkbox"/> 4. Thesis Credit: Yes <input type="checkbox"/> No <input checked="" type="checkbox"/>	COURSE ATTRIBUTES: Check All That Apply 1. Pass/Not Pass Only <input type="checkbox"/> 2. Satisfactory/Unsatisfactory Only <input type="checkbox"/> 3. Repeatable <input type="checkbox"/> Maximum Repeatable Credit: _____ 4. Credit by Examination <input type="checkbox"/> 5. Fees <input type="checkbox"/> Coop <input type="checkbox"/> Lab <input type="checkbox"/> Rate Request <input type="checkbox"/> Include comment to explain fee _____	6. Registration Approval Type Department <input type="checkbox"/> Instructor <input type="checkbox"/> 7. Variable Title <input type="checkbox"/> 8. Honors <input type="checkbox"/> 9. Full Time Privilege <input type="checkbox"/> 10. Off Campus Experience <input type="checkbox"/>
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Schedule Type	Minutes Per Mtg	Meetings Per Week	Weeks Offered	% of Credit Allocated	Cross-Listed Courses
Lecture	3	50	16	100	
Recitation					
Presentation					
Laboratory					
Lab Prep					
Studio					
Distance					
Clinic					
Experiential					
Research					
Ind. Study					
Pract/Observ					

COURSE DESCRIPTION (INCLUDE REQUISITES/RESTRICTIONS):
 This course aims at analysis and design of CMOS integrated radio frequency (RF), microwave and mm-wave circuits. Various modules of an RF transceiver are discussed including, low noise amplifiers, mixers, oscillators, frequency synthesizers and power amplifiers. A term project on design of an RF to mm-wave module is also required.

***COURSE LEARNING OUTCOMES:**
 o Knowledge of various wireless standards, modulation techniques and CMOS technology (a)
 o Knowledge of matching network, transmission line effects, Scattering parameters (a)
 o Analysis and Design of CMOS low noise amplifiers from RF to mm-wave frequencies (a, c)

Calumet Department Head _____ Date _____	Calumet School Dean _____ Date _____	Calumet Director of Graduate Studies _____ Date _____
Fort Wayne Department Head _____ Date _____	Fort Wayne School Dean _____ Date _____	Fort Wayne Director of Graduate Studies _____ Date _____
Indianapolis Department Head _____ Date _____	Indianapolis School Dean _____ Date _____	IUPUI Associate Dean for Graduate Education _____ Date _____
North Central Department Head _____ Date _____	North Central School Dean _____ Date _____	North Central Director of Graduate Studies _____ Date _____
West Lafayette Department Head <u>Mitchell R. Mellish 11/4/15</u> _____ Date _____	West Lafayette College/School Dean _____ Date _____	Date Approved by Graduate Council _____ Date _____
Graduate Area Committee Convener _____ Date _____	Graduate Dean _____ Date _____	Graduate Council Secretary _____ Date _____
		West Lafayette Registrar _____ Date _____

**Supporting Document to the Form 40G
for a New Graduate Course**

To: Purdue University Graduate Council

From: Faculty Member: Saeed Mohammadi

Department: Electrical and Computer Engineering

Campus: West Lafayette

Date:

Subject: Proposal for New Graduate Course

Contact for information if questions arise: Name: Matt Golden
Phone: 494-3374
Email: goldenm@purdue.edu
Address: EE Building, Room 135

Course Subject Abbreviation and Number: ECE 60420

Course Title: Radio Frequency Integrated Circuits

Course Description:

This course aims at analysis and design of CMOS integrated radio frequency (RF), microwave and mm-wave circuits. Various modules of an RF transceiver are discussed including, low noise amplifiers, mixers, oscillators, frequency synthesizers and power amplifiers. A term project on design of an RF to mm-wave module is also required.

Semesters Offered:

For the benefit of graduate student plan of study development, how frequently will this prototype be offered? Which semesters?
Fall Odd Years

A. Justification for the Course:

With the proliferation of cell phones and other wireless mobile devices, and the ever-increasing performance requirement, analysis, design and low-cost implementation of radio frequency, microwave and mm-wave integrated circuits

have become important topics for graduate studies in electrical engineering. Almost every respected graduate program in devices and circuits offers such a course at an advanced level. Students who may be interested in this course should have a background in applied electromagnetic, analog and digital circuits and solid-state devices. The appropriate level for this course is therefore a 600 level. The targeted audience is 2nd and 3rd year graduate students in vlsi and circuit, microelectronic and nanotechnology and field and optics who have completed the pre-requisites and are planning to widen their knowledge base in this area. The course goes through many topics in a very fast pace to introduce students to all the important topics. Students choose one of the topics for their term project to go deeper and gain a much better understanding of their topic. For their term project, students must do an in-depth literature search, analysis and design and simulation and optimization of their topic.

- Anticipated enrollment
 - Undergraduate 0
 - Graduate 20-25

B. Learning Outcomes and Method of Evaluation or Assessment:

ECE Graduate Learning Outcomes:

- a. Knowledge and Scholarship (thesis/non-thesis)
 - b. Communication (thesis/non-thesis)
 - c. Critical Thinking (thesis/non-thesis)
 - d. Ethical and Responsible Research (thesis) or Professional and Ethical Responsibility (non-thesis)
- List Learning Objectives for this course and map each Learning Objective to one or more of the ECE Learning Outcomes (a-d, listed above):
 - Knowledge of various wireless standards, modulation techniques and CMOS technology (a)
 - Knowledge of matching network, transmission line effects, Scattering parameters (a)
 - Analysis and Design of CMOS low noise amplifiers from RF to mm-wave frequencies (a, c)
 - Analysis and Design of CMOS mixers from RF to mm-wave frequencies (a, c)

- Analysis and Design of CMOS oscillators and frequency synthesizers from RF to mm-wave frequencies (a, c)
- Analysis and Design of CMOS power amplifiers from RF to mm-wave frequencies (a, c)
- Completing a term project focusing on analysis, design, simulation and optimization of one of the RF modules or an entire transceiver (a, b, c, d)
- Methods of Instruction
 - Lecture
- Will/can this course be offered via Distance Learning?
 - The course will not be offered via Distance Learning though it could be if necessary.
- Grading Criteria

Grading criteria (select from checklist); include a statement describing the criteria that will be used to assess students and how the final grade will be determined. Add and delete rows as needed.

- Two midterm exams
- A term project focusing on design and simulation
- Homework including simulation homework
- attendance/participation
- ▶ Describe the criteria that will be used to assess students and how the final grade will be determined:

Students will be assessed based on successful completion of their term project as well as demonstrating ability to perform analysis and design of RF circuits measured through homework and exams.

C. Prerequisite(s):

List prerequisites and/or experiences/background required. If no prerequisites are indicated, provide an explanation for their absence. Add bullets as needed.

- Graduate Standing or Consent of Instructor

- Prerequisite by Topic: Solid-state devices, Electromagnetics and analog and digital circuits.

D. Course Instructor(s):

Provide the name, rank, and department/program affiliation of the instructor(s). Is the instructor currently a member of the Graduate Faculty? (If the answer is no, indicate when it is expected that a request will be submitted.) Add rows as needed.

Name	Rank	Dept.	Graduate Faculty or expected date
Saeed Mohammadi	Associate Professor	ECEN	Yes

E. Course Outline:

Provide an outline of topics to be covered and indicate the relative amount of time or emphasis devoted to each topic. If laboratory or field experiences are used to supplement a lecture course, explain the value of the experience(s) to enhance the quality of the course and student learning. For special topics courses, include a sample outline of a course that would be offered under the proposed course. **(This information must be listed and may be copied from syllabus).**

Weeks	Principal Topics
Week 1:	RF front-end architectures and modulation techniques
Week 2:	Active/passive devices and CMOS technology
Week 3:	Distributed elements, S-parameters and Smith Chart
Week 4:	Understanding Noise in RF systems
Week 5:	Linearity of RF devices and circuits
Week 6:	Narrowband and wideband amplifier designs
Week 7:	Low Noise Amplifiers
Week 8	Review and Midterm 1
Week 9:	RF Mixers
Week 10-11:	RF Power amplifiers

Week 12-13:	Oscillators, frequency synthesizers and Phase Noise
Week 14:	Review and Midterm 2 (Nov. 24 th)
Week 15:	Project presentations
Week 16:	Course summary and conclusion

F. Reading List (including course text):

A primary reading list or bibliography should be limited to material the students will be required to read in order to successfully complete the course. It should not be a compilation of general reference material.

A secondary reading list or bibliography should include material students may use as background information.

- Primary Reading List
 - The Design of CMOS Radio Frequency Integrated Circuit (Tom Lee), 2nd edition, Cambridge Press
 - Required Research Papers on various topics

- Secondary Reading List
 - Analysis and Design of Analog Integrated Circuits (Gray, Hurst, Lewis, Meyer) 5th edition, Wiley
 - Microwave Engineering (Pozar), 2nd edition, Wiley
 - Multi-GHz frequency Synthesis & Divisions (Rategh, Lee), Kluwer Academic
 - Architecture for RF Frequency Synthesizer (Vaucher), Kluwer Academic
 - RF Power Amplifiers for Wireless Communications (Cripps) 2nd edition, Artech House

G. Library Resources

Describe any library resources that are currently available or the resources needed to support this proposed course.

H. Course Syllabus

(While not a necessary component of this supporting document, an example of a course syllabus is available, for information, by clicking on the link below, which goes to the *Graduate School's Policies and Procedures Manual for Administering Graduate Student Program*.

See Appendix K.

[http://www.purdue.edu/gradschool/faculty/documents/Graduate School Policies and Procedures Manual.pdf](http://www.purdue.edu/gradschool/faculty/documents/Graduate_School_Policies_and_Procedures_Manual.pdf)