Office of the Registrar FORM 40 REV. 11/09

## PURDUE UNIVERSITY REQUEST FOR ADDITION, EXPIRATION, OR REVISION OF AN UNDERGRADUATE COURSE (10000-40000 LEVEL)

Print Form

EFD 27-10

PARTMENT School of Electrical and Compute	er Engineering (CED 07.40)	EFFECTIVE RESCION Follows	21	20110
		EFFECTIVE SESSION Fall 2010		
INSTRUCTIONS: Please check the items below		s request.		
New course with supporting doc			attributes (department	head signature only)
2. Add existing course offered at a	notner campus	8. Change in instructi		
3. Expiration of a course 4. Change in course number		9. Change in course	-	
5. Change in course title		— —	•	
6. Change in course credit/type		Gridinge in contect	ers offered (departmer department to another	nt head signature only)
PROPOSED:	EXISTING:	L. Transier nom one		AS OFFERED
Subject Abbreviation		50m	1	All That Apply:
Subject Abbreviation	Subject Abbreviation	I ECE	_ Summer	Fall Spring
Course Number	Course Number	20200	CAMPU	S(ES) INVOLVED
Long Title Linear Circuit Analysis II			Calumet Cont Ed	N. Central
Short Title Linear Circuit Analysis II		T-777-	Ft. Wayne	Tech Statewide
·,	d by the Office of the Registrar if o	mitted (20 CHARACTERS ONLY)	- Indianapolis	[X]VV. Lalayette
CREDIT TYPE	a by the office of the regional has	<del></del>		
1.Fixed Credit: Cr. Hrs.	Pass/Not Pass Only	COURSE ATTRIBUTES: Check	.,,	
2.Variable Credit Range:	Satisfactory/Unsatisfactory On		ion Approval Type artment	Instructor
Minimum Cr. Hrs Check One) To Or	Repeatable	7. Variable Title	<u></u>	instructor
Maximum Cr. Hrs.	Maximum Repeatable Credit:	8. Honors	,	H
3. Equivalent Credit: Yes No	4. Credit by Examination	9. Full Time	Privilege	H H
	5. Special Fees		pus Experience	T I
ScheduleType Minutes Meetings Pe Per Mtg Week	r Weeks % of Credit Offered Allocated			Cross-Listed Courses
Lecture	Olleled Allocated			
acitation	-			
sentation				
Lab Prep				
Studio			'	
Distance				
Clinic Experiential			ì	
Research				
Ind. Study	·			
Pract/Observ		*		
COURSE DESCRIPTION (INCLUDE REQUISITE				
Prerequisites: ECE 20100 Minimum Grade of C concurrently])	and (MA 26200 [may be taken cor	ncurrently] or MA 26600 [may be take	n concurrently] or MA	36600 [may be taken
Concernation (1)				
*COURSE LEARNING OUTCOMES:				
See attachment.				
Calumet Department Head Date	Calumet School Dean	Date		
Fort Wayne Department Head Date	Fort Wayne School Dean	Date		
ndianapolis Department Head Date	Indianapolis School Dean	Date		
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th Central Department Head Date	North Central Chancellor	Date	Washing	LOAN ALM!
My Lay 3/31/10	MULLIN T. If A.	n SPYporo	DINNI PR	ON ROLL BALL
Vest Valavette Denadment Head / Date 1	Mact I staudte College/Connei Dece	, n.c.	Transfer Division No. 1	I VIVI \ / Data



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## PURDUE UNIVERSITY REQUEST FOR ADDITION, EXPIRATION, OR REVISION OF AN UNDERGRADUATE COURSE (10000-40000 LEVEL)



EFD 27-10

DEPARTMENT School of Electrical and Computer	Engineering (EFD 27-10) EFF	ECTIVE SESSI	ON Fall 2010		
NSTRUCTIONS: Please check the items below		equest.			
1. New course with supporting docu  2. Add existing course offered at an  3. Expiration of a course  4. Change in course number  5. Change in course title  6. Change in course credit/type  PROPOSED:  Subject Abbreviation  Course Number	ments other campus  EXISTING: Subject Abbreviation	7. 8. 9. 10.	Change in instruction Change in course de Change in course rec	al hours scription quisites soffered (department to anothent Check Summer	nt head signature only)  nt head signature only)  r  MS OFFERED  ( All That Apply:    Fall
Long Title Linear Circuit Analysis II				Cont Ed	Tech Statewide
Short Title Linear Circuit Analysis II				Ft. Wayne Indianapolis	W. Lafayette
Abbreviated title will be entered	by the Office of the Registrar if omit				
CREDIT TYPE  1.Fixed Credit: Cr. Hrs.  2.Variable Credit Range: Minimum Cr. Hrs (Check One) To Or Maximum Cr. Hrs.  3.Equivalent Credit: Yes No	Pass/Not Pass Only     Satisfactory/Unsatisfactory Only     Repeatable     Maximum Repeatable Credit:     Credit by Examination     Special Fees	COURSE AT	TRIBUTES: Check All 6. Registration Depar 7. Variable Title 8. Honors 9. Full Time Pi 10. Off Campi	n Approval Type tment	Instructor
ScheduleType Minutes Meetings Pe Per Mtg Week	r Weeks % of Credit Offered Allocated				Cross-Listed Courses
Lecture Recitation Sentation Soratory Lab Prep Studio Distance Clinic Experientiat Research Ind. Study					
Pract/Observ  COURSE DESCRIPTION (INCLUDE REQUISITE Prerequisites: ECE 20100 Minimum Grade of Concurrently))	S/RESTRICTIONS): and (MA 26200 [may be taken cond	currently] or MA	26600 [may be taker	n concurrentlyj or M	A 36600 [may be taken
*COURSE LEARNING OUTCOMES: See attachment.					
Calumet Department Head Date	Calumet School Dean		Date		
Fort Wayne Department Head Date	Fort Wayne School Dean		Date		
Indianapolis Department Head Date	Indianapolis School Dean		Date		
North Central Department Head Date  3/31/6    Verst Karayette Department Head Date	North Central Chancetor West Larayette College/School Dean	n 5/	Date Date West	Lafayette Registrar	Date

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**TO:** The Faculty of the College of Engineering

**FROM:** The Faculty of the School of Electrical and Computer Engineering

RE: Change to Existing Undergraduate Course: ECE 20200, Linear Circuit Analysis

II, change in requisites.

The faculty of the School of Electrical and Computer Engineering has approved the following changes to an existing course. This action is now submitted to the Engineering Faculty with a recommendation for approval.

From: ECE 20200 Linear Circuit Analysis II

Sem. Fall, Spring, Summer; Cr. 3; Lecture 3.

Prerequisites: ECE 20100 and (MA 26200 [may be taken concurrently] or MA 26600 [may be taken concurrently] or MA 36600 [may be taken concurrently])
Restrictions: Must be enrolled in one of the following: School of Electrical &

Computer Engineering, School of Interdisciplinary Engineering

**Description:** Continuation of ECE 20100. Use of Laplace Transform techniques to analyze linear circuits with and without initial conditions. Characterization of circuits based upon impedance, admittance, and transfer function parameters. Determination of frequency response via analysis of poles and zeros in the complex plane. Relationship between the transfer function and the impulse response of a circuit. Use of continuous time convolution to determine time domain responses. Properties and practical uses of resonant circuits and transformers. Input - output characterization of

a circuit as a two-port. Low and high-pass filter design.

To: ECE 20200 Linear Circuit Analysis II

Sem. Fall, Spring, Summer; Cr. 3; Lecture 3.

**Prerequisites:** ECE 20100 Minimum Grade of C and (MA 26200 [may be taken concurrently] or MA 26600 [may be taken concurrently] or MA 36600 [may be taken concurrently])

**Restrictions:** Must be enrolled in one of the following: School of Electrical & Computer Engineering, School of Interdisciplinary Engineering

**Description:** Continuation of ECE 20100. Use of Laplace Transform techniques to analyze linear circuits with and without initial conditions. Characterization of circuits based upon impedance, admittance, and transfer function parameters. Determination of frequency response via analysis of poles and zeros in the complex plane.

Relationship between the transfer function and the impulse response of a circuit. Use of continuous time convolution to determine time domain responses. Properties and practical uses of resonant circuits and transformers. Input - output characterization of a circuit as a two-port. Low and high-pass filter design.

Reason:

This course is part of the Core Curriculum for the BSEE and BSCmpE degrees. Subsets of Core Curriculum courses serve as prerequisites for most upper division ECE electives. In addition, a degree requirement for all ECE students is to achieve a GPA in all major-area (ECE) courses of at least a 2.0. Therefore, in order to ensure that ECE students are as well prepared as possible for upper division ECE courses, as well as to facilitate their achievement of the minimum major-area GPA of 2.0, a minimum grade requirement in the key ECE prerequisite course is being proposed.

on behalf of V Balakrishnan, Interim Head School of Electrical and Computer Engineering

APPROVED FOR THE FACULTY
OF THE SCHOOLS OF ENGINEERING
BY THE ENGINEERING
CURRICULUM COMMITTEE

ECC Minutes

Date

Chairman ECC

R.Cipra

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## School of Electrical and Computer Engineering (EFD 27-10)

## **Course Learning Outcomes:**

- i. an ability to compute impedances and admittances of components and circuits.
- ii. an ability to compute responses of linear circuits with and without initial conditions via one-sided Laplace transform techniques.
- iii. an ability to compute responses to linear circuits using transfer function and convolution techniques.
- iv. an ability to analyze and compute responses of linear circuits containing mutually coupled inductors and ideal transformers in the s-domain.
- v. an ability to analyze basic two port circuits using the various types of two port parameters and be able to construct such parameters from a given circuit.
- vi. an ability to analyze and design basic LP, BP, HP and resonant circuits in the s-domain.

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