#### Office of the Registrar FORM 40G REV. 9/06

### **PURDUE UNIVERSITY**

REQUEST FOR ADDITION, EXPIRATION, OR REVISION OF A GRADUATE COURSE (500-600 LEVEL) Print Form

1-07

PEPARTMENT ECE	EFFECTIVE SE	SSION Fall 2008 9	
INSTRUCTIONS: Please check the items below	which describe the purpose of this request.		
2. Add existing course offered at all 3. Expiration of a course 4. Change in course number 5. Change in course title 6. Change in course credit/type PROPOSED:  Subject Abbreviation  Course Number  Long Title Programming Parallel Machines  Short Title Prog Parallel Machines	EXISTING: Subject Abbreviation ECE Course Number 563	8. Chan  9. Chan  10. Chan  11. Chan  12. Trans	ge in course attributes ge in instructional hours ge in course description ge in course requisites ge in semesters offered To Change sfer from one department to another  TERMS OFFERED Check All That Apply: Summer Fall Spring  CAMPUS(ES) INVOLVED Calumet Cont Ed Ft. Wayne Indianapolis  Reference N. Central Tech Statewide XW. Lafayette
CREDIT TYPE	COLIBEE	ATTRIBUTES, Observa	All That Analy
1.Fixed Credit: Cr. Hrs.  2.Variable Credit Range: Minimum Cr. Hrs (Check One) To Or Maximum Cr. Hrs.  3.Equivalent Credit: Yes No No Meetings Per Mtg  4.Thesis Credit: Yes No No Meetings Per Mtg  Lecture Recitation Presentation Laboratory Lab Prep Studio Distance Clinic Experiential Research Ind. Study Pract/Observ	1. Pass/Not Pass Only 2. Satisfactory/Unsatisfactory Only 3. Repeatable Maximum Repeatable Credit: 4. Credit by Examination 5. Designator Required 6. Special Fees  T Weeks % of Credit Delivery Method (Asyn. Or Syn.)	Depa 8. Variable <sup>-</sup> 9. Remedial 10. Honors 11. Full Time	rion Approval Type rtment Instructor  Title  Privilege ous Experience Audio, Internet,
COURSE DESCRIPTION (INCLUDE REQUISITE	S):		
See attachment.			
Calumet Department Head Date	Calumet School Dean		et Undergrad Curriculum Committee Date
Fort Wayne Department Head Date  Indianapolis Department Head Date	Fort Wayne School Dean  Indianapolis School Dean	Date Fort W	/ayne Chancellor Date
North Central Department Head Date    Mark	North Central Chancellor  West Latavette College School Dean  Full Dead Taple 3	Date Date A	Approved by Graduate Council  Approved Secretary  Approved By Graduate Council  Date
Graduate Area Committee Convener Date	Graduate Dean	Date West I	Lafayette Registrar Date

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Print Form

REQUEST FOR ADDITION, EXPIRATION, OR REVISION OF A GRADUATE COURSE (500-600 LEVEL)

`EPARTMENT ECE EFFECTIVE SESSION Fall 2008

INSTRUCTIONS: Please check the items below	w which describe the purpose of this request.		
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3. Expiration of a course		님	ange in course description
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5. Change in course title			ange in semesters offered
6. Change in course credit/type		<b>声</b>	Insfer from one department to another
PROPOSED:	EXISTING:		·
	EXISTING.		TERMS OFFERED Check All That Apply:
Subject Abbreviation	Subject Abbreviation ECE		Summer Fall X Spring
Course Number	Course Number 563		CAMPUS(ES) INVOLVED
Long Title Programming Parallel Machines	And the state of t		Calumet N. Central
			Cont Ed Tech Statewide
Short Title Prog Parallel Machines			Ft. Wayne W. Lafayette
Abbreviated title will be entere	d by the Office of the Registrar if omitted. (22 CHAR	ACTERS ONLY)	
CREDIT TYPE	COURSE	ATTRIBUTES: Check	All That Apply
1.Fixed Credit: Cr. Hrs.	1. Pass/Not Pass Only		ration Approval Type
2.Variable Credit Range: Minimum Cr. Hrs	2. Satisfactory/Unsatisfactory Only	Dep	partment Instructor
(Check One) To Or	3. Repeatable  Maximum Repeatable Credity	8. Variable	
Maximum Cr. Hrs.	Maximum Repeatable Credit: 4. Credit by Examination	9. Remedia 10. Honors	al
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COURSE DESCRIPTION (INCLUDE REQUISITE:	6).		
See attachment.	<i>3).</i>		
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Calumet Department Head Date	Calumet School Dean		
Calumet Department Head Date	Calumet School Dean	Date Calun	met Undergrad Curriculum Committee Date
Fort Wayne Department Head Date	Fort Wayne School Dean	Fort'	Wayne Chancellor Date
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Indianapolis Department Head Date	Indianapolis School Dean	Date Unde	ergrad Currigulum Committee Date
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North Central Department Head Date	North Central Chancellor	Date Date	Approved by Graduate Council
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West Lafayette Department Head Date	West Lafayette College/School Dean	Date Grad	uate Council Secretary Date
. aduate Area Committee Convener Date	Graduate Dean	Date West	Lafayette Registrar Date

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## COURSE DESCRIPTION (INCLUDE REQUISITES):

Terms Offered: Sem. 2, odd years. Prerequisite: ECE 565 or equivalent.

Description: This course presents methods and techniques for programming parallel computers, such as multicore and high-end parallel architectures. Various parallel algorithms will be presented to demonstrate different techniques for identifying parallel tasks and mapping them onto parallel machines. Realistic science/engineering applications and their characteristics will be discussed. Parallel architectures to be considered are shared-memory and distributed-memory multiprocessor systems. Programming paradigms for these machines will be compared, including directive-based (OpenMP), message passing (MPI) and thread-based (Posix threads) methods. Methodologies for analyzing and improving the performance of parallel programs will be discussed. There will be a class project in which each student parallelizes and tunes the performance of a large computational application or developes/improves a tool that helps this process. Each student will prepare one lecture for a selected topic.

TO:

The Faculty of the College of Engineering

FROM: RE:

The Faculty of the School of Electrical and Computer Engineering ECE 563 Changes in Terms Offered, Description, Text, and Content

The faculty of the School of Electrical and Computer Engineering has approved the following changes in ECE 563. This action is now submitted to the Engineering Faculty with a recommendation for approval.

From:

## ECE 563 – Programming Parallel Machines

Sem.2. Class 3, cr. 3.

Prerequisite: ECE 264, ECE 463. Authorized equivalent courses or consent of instructor may be used in satisfying course pre- and co-requisites. Departmental approval required.

Examines how to program parallel processing systems. Various parallel algorithms are presented to demonstrate different techniques for mapping tasks onto parallel machines. Parallel architectures to be considered are: SIMD (synchronous), MIMD (asynchronous), and mixed-mode (SIMD/MIMD hybrid). Machines that represent these classes to be used in the course are: the MasPar MP-1 (SIMD); nCUBE 2 (MIMD); and PASM (mixedmode). There will be three programming projects, one on each machine. The similarities and differences among the machines and their languages will be discussed.

To:

# **ECE 563 - Programming Parallel Machines**

Sem. 2, odd years. Class 3, cr. 3 Prerequisite: ECE 565 or equivalent

This course presents methods and techniques for programming parallel computers, such as multicore and high-end parallel architectures. Various parallel algorithms will be presented to demonstrate different techniques for identifying parallel tasks and mapping them onto parallel machines. Realistic science/engineering applications and their characteristics will be discussed. Parallel architectures to be considered are sharedmemory and distributed-memory multiprocessor systems. Programming paradigms for these machines will be compared, including directive-based (OpenMP), message passing (MPI) and thread-based (Posix threads) methods. Methodologies for analyzing and improving the performance of parallel programs will be discussed. There will be a class project in which each student parallelizes and tunes the performance of a large computational application or develops/improves a tool that helps this process. Each student will prepare one lecture for a selected topic.

Reason:

The course description and content have been changed to reflect the updated content of the course.

Mark J. T. Smith, Head School of Electrical & Computer Engineering

APPROVED FOR THE FACULTY OF THE SCHOOLS OF ENGINEERING BY THE ENGINEERING CURRICULUM COMMITTEE

ECC Minutes

Date 3-6-08
Chairman ECC Muhal

# **ECE 563 – Programming Parallel Machines**

Required Text: Research papers and course handouts.

Weeks	Principal Topics
1	Introduction and Motivation
1	Efficiency and Speedup Measures
1	Automatic Parallelization
1	Tuning Automatically Parallelized Programs
2	<b>Explicit Program Parallelization</b>
1	Open MP
1	MPI
1	Pthreads
2	Programming Methodologies and Tools
2	Application Studies
2	Project Discussions

**Course Outcomes:** A student who successfully fulfills the course requirements will have demonstrated:

- 1) an understanding of the basic properties of parallel computer architectures and their relationship to parallel program design [3, j, k].
- 2) an ability to analyze a program for parallelism and express this parallelism for both shared-memory and distributed-memory machines [4, a, b, c, e, k].
- 3) an understanding of parallel models and programming constructs for OpenMP, MPI, and Posix threads [3,j,k].
- 4) an understanding of performance factors of parallel programs and their relationship to application characteristics and parallel programming constructs [3, 4, b, e, k].
- 5) an ability to use parallelizing compilers to parallelize and tune the performance of application programs [3, 4, b, e, j, k].

Outcome Assessment Method: Outcomes 1, 2, 3 and 4 will be assessed all, or in part, by tests. Outcomes 1, 2, 3, 4 and 5 will be measured all, or in part, by programming assignments.

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