ECE 255 Course Outcomes
These must be achieved to pass the course.

i) The ability to identify and correctly utilize the external lead structure and basic electrical characteristics of common semiconductor devices (pn junctions, MOSFETs, and BJTs).

ii) The ability to analyze and design d.c. bias circuits.

iii) The ability to analyze and design single and multistage amplifiers at low, mid and high frequencies.

iv) The ability to utilize d.c. and a.c. models of semiconductor devices in both analysis and design.

v) The ability to use a CAD tool (e.g., SPICE) in circuit analysis and design.