Question 1 (30 points total): Given the grammar G:

\[ S \to (A) \]

\[ A \to (A) \]

\[ A \to \ast \]

\[ S \to (A) \quad 0 \]

\[ A \to (A) \]

\[ A \to \ast \quad 1 \]

\[ S \to (A) \]\n
\[ S \to (A) \quad 2 \]

\[ A \to (A) \]

\[ A \to (A) \quad 3 \]

(a) (12 points)

Complete the LR(0) CFSM in your blue book. You only need to show state 4 and labels on edges adjacent to it in your solution.

(b) (12 points) Given the parse sequence below, and your table above

i. What action occurs going from step 6 to 7

ii. What action occurs going from set 7 to 8?

iii. At 11, is the grammar accepted or in an error state?

For actions, give accept, shift or reduce, and the symbol shifted or production reduced.

<table>
<thead>
<tr>
<th>step</th>
<th>state stack symbol stack</th>
<th>step</th>
<th>state stack symbol stack</th>
<th>step</th>
<th>state stack symbol stack</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>0 \lambda</td>
<td>5</td>
<td>0 1 4 4 7 (((\ast))</td>
<td>9</td>
<td>0 1 4 5 6 ((A)</td>
</tr>
<tr>
<td>2</td>
<td>0 1 (</td>
<td>6</td>
<td>0 1 4 4 5 (((A)</td>
<td>10</td>
<td>0 1 2 (A</td>
</tr>
<tr>
<td>3</td>
<td>0 1 4 (</td>
<td>7</td>
<td>0 1 4 4 5 6 (((A)</td>
<td>11</td>
<td>0 1 3 (A</td>
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<tr>
<td>4</td>
<td>0 1 4 4 (</td>
<td>8</td>
<td>0 1 4 5 (((A)</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>
Question 2: (24 points total) Given the two loops:

S1: for (j = 0; j < n; j++) {
S2:   for (k = 0; k < n; k++) {
S3:     a[j][k] = ...;
S4:     ... = a[j-1][k+1];
S5: }
S6: }
S7: for (k = 0; k < n; k++) {
S8:   for (j = 0; j < n; j++) {
S9:     a[j][k] = ...;
S10:    ... = a[j-1][k+1];
S11: }
S12: }

and an “a” array that is too large to fit in cache laid out in memory as:

1

| a[0][0] | a[0][1] | ... | a[0][n-1] | a[1][0] | a[1][1] | ... | a[1][n-1] | ... | a[n-1][0] | a[n-1][1] | ... | a[n-1][n-1] |

and an “a” array that is too large to fit in cache laid out in memory as:

2

| a[0][0] | a[1][0] | ... | a[n-1][0] | a[0][1] | a[1][1] | ... | a[n-1][1] | ... | a[n-1][n-1] | a[0][n-1] | a[1][n-1] | ... | a[n-1][n-1] |

i. (5 points) For the left loop (the j loop is outermost) which layout is best (put the letter of the correct answer in your blue book):
   a. layout 1
   b. layout 2
   c. no difference between the two

ii. (5 points) For the right loop (the k loop is outermost) which layout is best (put the letter of the correct answer in your blue book):
   a. layout 1
   b. layout 2
   c. no difference between the two

iii. (7 points) Give the direction and type of dependence, if any, on the write of “a” in S3 and the read of “a” in S4.

iv. (7 points) Give the direction and type of dependence, if any, on the write of “a” in S9 and the read of “a” in S10.
Question 3 (18 points): Given the loop:

```c
for (i = 2; i < 4*n+2; i++) {
    a[i][i] = a[i-1] + a[i-2];
}
```

that is unrolled to form the loop

```c
for (i = 2; i < 4*n+2; i+=4) {
    a[i][i] = a[i-1] + a[i-2];
    a[i+1][i+1] = a[i] + a[i-1];
    a[i+2][i+2] = a[i+1] + a[i];
    a[i+3][i+3] = a[i+2] + a[i+1];
}
```

Write the letters of all that are true in your blue books.

a. More opportunities exist after instruction level parallelism after unrolling
b. Fewer opportunities exist after instruction level parallelism after unrolling
c. There is no affect on instruction level parallelism or the effect is unknowable
d. Data cache performance is worse after unrolling
e. Data cache performance is better after unrolling
f. There is no effect on data cache performance or the effect is unknown
g. More registers are needed by a register allocator doing local (per-basic block) register allocation
h. Fewer registers are needed by a register allocator doing local (per-basic block) register allocation
i. There is no effect on the number of registers are needed by a register allocator doing local (per-basic block) register allocation or the effect is unknowable.
j. The loop could be parallelized before unrolling, but cannot be parallelized after unrolling
k. The loop could not be parallelized before unrolling, but can be after unrolling
l. The unrolling of the loop has not effect on parallelization.
Question 4 (28 points): For this question you need to perform a live variable analysis.

i. (3 points) What goes in the GEN set?
ii. (3 points) What goes in the KILL set?
iii. (3 points) Is the analysis an ANY or ALL paths algorithm?
iv. (3 points) Is the analysis a FORWARD or BACKWARDS algorithm?
v. (16 points) Given the control flow graph of a program below, fill a table like the one below in your blue books.

```

<table>
<thead>
<tr>
<th>BLOCK</th>
<th>IN</th>
<th>OUT</th>
<th>GEN</th>
<th>KILL</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
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<td></td>
<td></td>
<td></td>
</tr>
<tr>
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<td></td>
<td></td>
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</tr>
<tr>
<td>2</td>
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</tr>
<tr>
<td>3</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>
```

Write in Exam Book Only