

Cetus-assisted checkpointing of parallel codes

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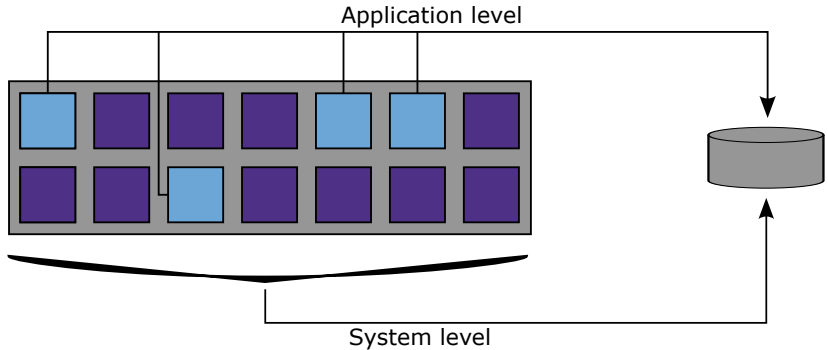
CPPC

ComPiler for Portable Checkpointing

- Portable checkpointing for SPMD applications.
- Aims to provide fully transparent operation.
- Preserves application scalability.

Why use a compiler?

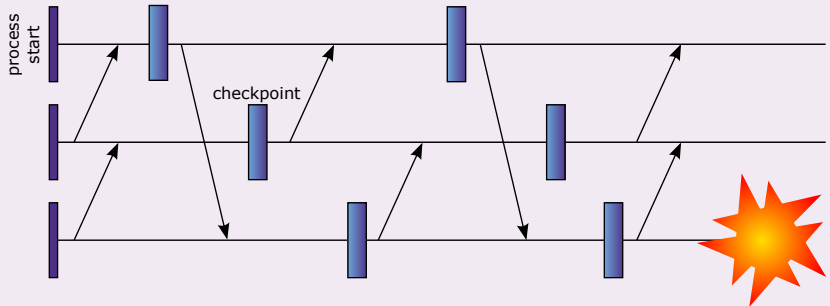
Selection of restart-relevant data



Why use a compiler?

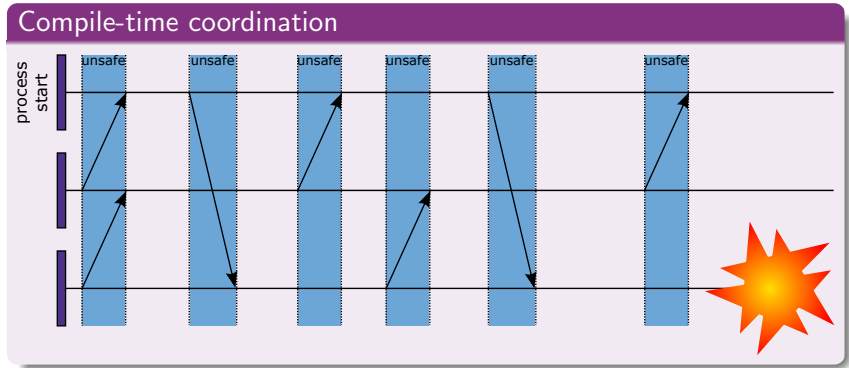
Compile-time coordination

Uncoordinated processes → restart inconsistencies



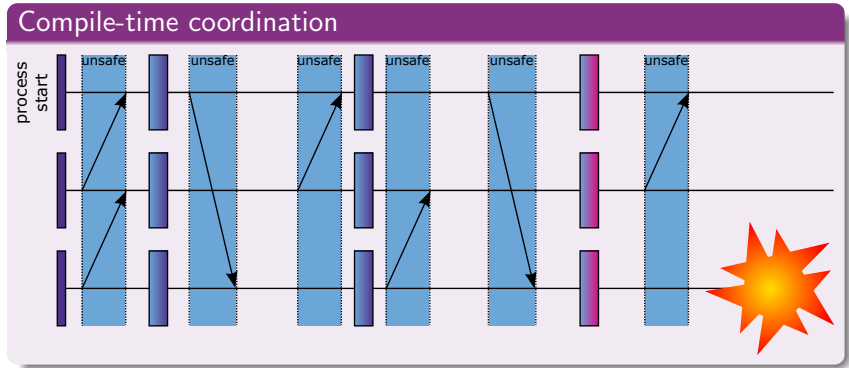
Why use a compiler?

Compile-time coordination



Why use a compiler?

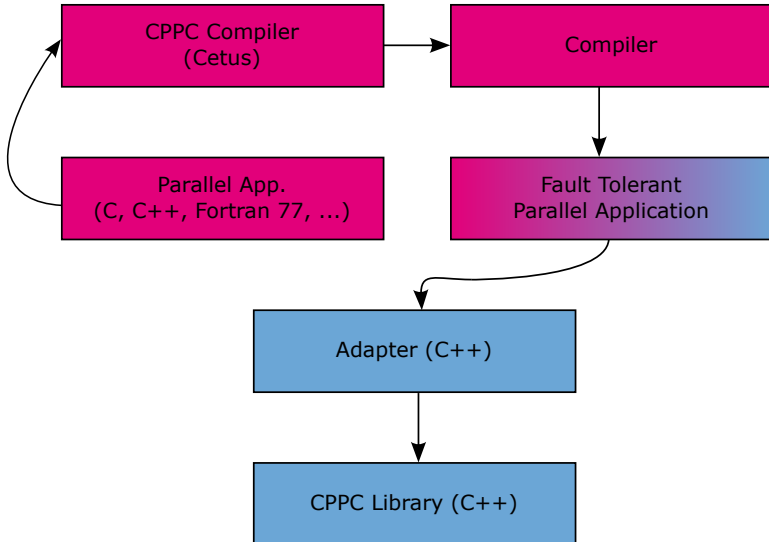
Compile-time coordination



Why Cetus?

- Well, we used SUIF before...
 - Closed-source front-ends.
 - Buggy front-ends.
 - Unmaintained front-ends.
- The Cetus License allows modification and redistribution.
- The Java implementation guarantees portability.

CPPC design



Communication analysis

Overview

- Tested for MPI, although the approach is easily extensible by design.
- Similar to a static simulation of the execution.
- Uses constant propagation and symbolic expression analysis.
- Ignores non-communication statements.

Communication analysis

Implementation

- 1 Detect variables relevant to interprocess communications:
 - Not to the communicated values, but to the communicating processes.

semantic input to the compiler

```
int MPI_Send( void * buf, int count, MPI_Datatype datatype,  
             int dest, int tag, MPI_Comm comm )
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Communication analysis

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semantic input to the compiler

```
int MPI_Send( void * buf, int count, MPI_Datatype datatype,
             int dest, int tag, MPI_Comm comm )
```

```
int dest
```

```
int tag
```

```
dest = (rank + k) % comm_size;
```

input to the compiler

Communication analysis

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semantic input to the compiler

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int MPI_Send( void * buf, int count, MPI_Datatype datatype,
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```
int dest
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```
int tag
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$$\text{dest} = (\text{rank} + k) \% \text{comm_size};$$

input to the compiler

```
int dest
```

```
int rank
```

```
int tag
```

```
int comm_size
```

```
int k
```

```
...
```

Communication analysis

Implementation

- 1 Detect variables relevant to interprocess communications:
 - Not to the communicated values, but to the communicating processes.
- 2 Assign known constant values to detected communication-relevant variables.
- 3 Analyze the code in execution order.
 - 1 Determine whether an instruction is a safe point.
 - 2 If it is a communication statement: analyze.
 - 3 If it is a communication-relevant statement: symbolic analysis.
 - 4 Else, skip to next statement.

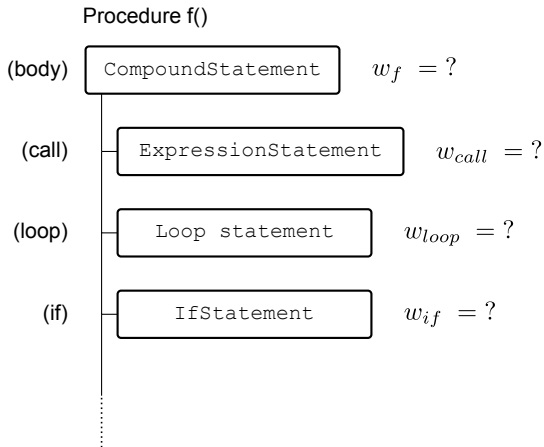
Checkpoint insertion

Overview

- Locate points in the code where checkpoints are needed in order to guarantee progress.
- Discard any code not inside loops.
- Computation time cannot be accurately predicted: use heuristics.

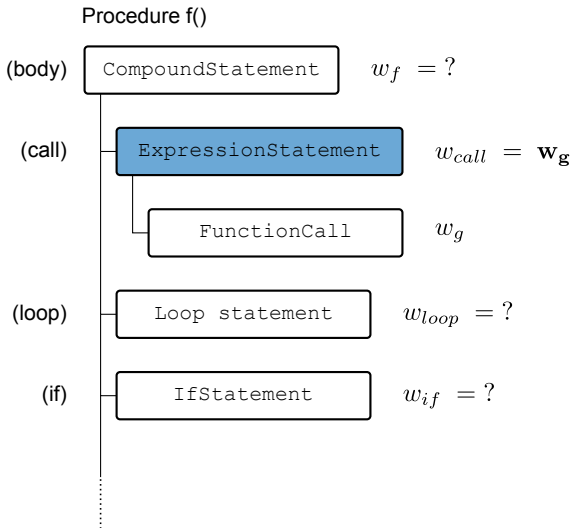
Checkpoint insertion

Cost estimation



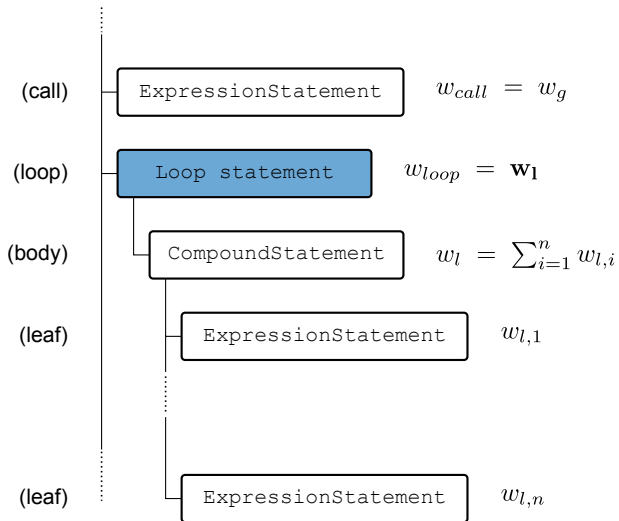
Checkpoint insertion

Cost estimation



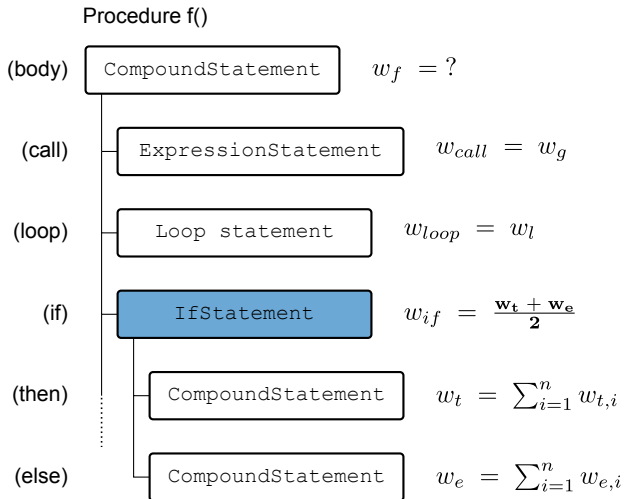
Checkpoint insertion

Cost estimation



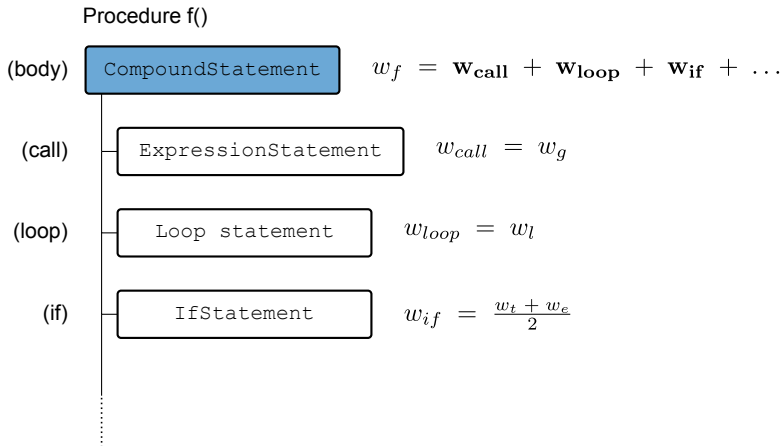
Checkpoint insertion

Cost estimation



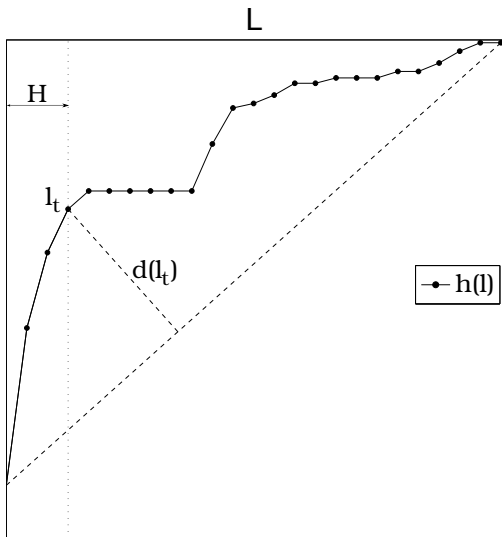
Checkpoint insertion

Cost estimation



Checkpoint insertion

Loop thresholding



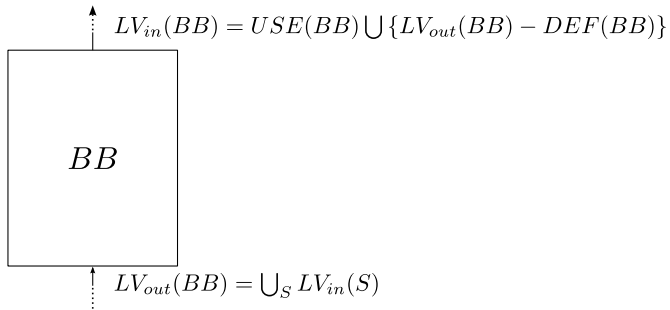
Live variable analysis

Overview

- Analyze sections of code for live variables that need to be stored into checkpoints.
- The traditional analysis proceeds from the end of the code up to the start, traversing basic blocks.
- CPPC does not use the CFG infrastructure in Cetus, but implements an execution order version:
 - Interprocedural version.
 - Some array optimizations.
- Each non compound statement has been annotated with its consumed and generated symbols.
- This information is forward-propagated taking into account the control flow.

Live variable analysis

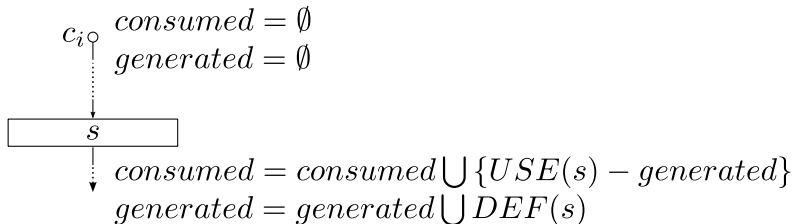
Traversing the code



$$LV_{out}(BB_{end}) = \emptyset$$

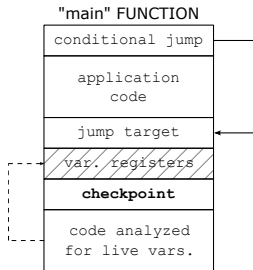
Live variable analysis

Traversing the code

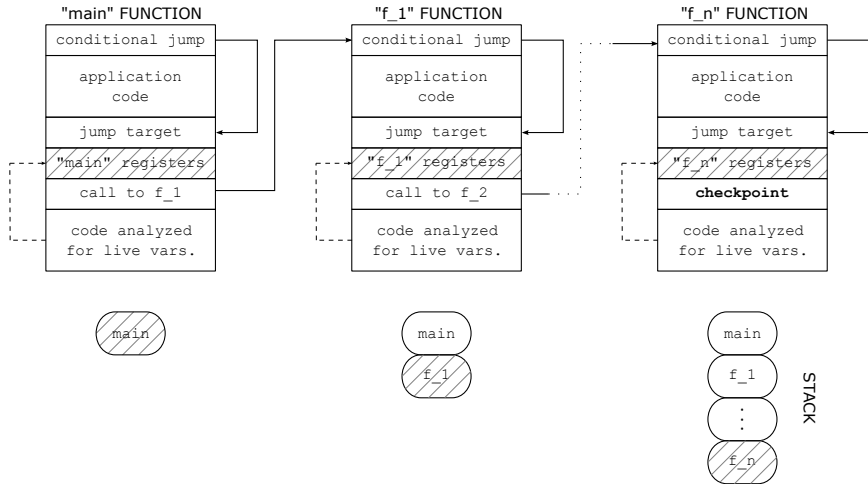


$$LV_{in}(c_i) = \mathbf{consumed}$$

Putting it all together



Putting it all together



Extending Cetus: Fortran support

- Fortran 77 front-end that generates Cetus IR from F77 codes.
- Reuse Cetus IR as much as possible.
- Extend Cetus IR where necessary, preserving interface and behavior.
- Back-end to transform Cetus IR back into F77 code.

Extending Cetus: Fortran support

IR extensions

- `cetus.hir.Declaration`: COMMON, DATA, DIMENSION, EXTERNAL, INTRINSIC, PARAMETER, SAVE.
- `cetus.hir.Literal`: DOUBLE literals.
- `cetus.hir.Specifier`: COMPLEX, DOUBLE COMPLEX, ARRAY(lbound, ubound), CHARACTER*N.
- `cetus.hir.Statement`: Computed GOTOs, FORMAT, Fortran-style DO, Implied DO.
- `cetus.hir.Expression`: expressions in FORMAT, substrings, IO calls.
- `cetus.hir.UnaryOperator`: `&&`.
- `cetus.hir.BinaryOperator`: `**`, `//`.

Perceptions on the Cetus infrastructure

Perceived strengths

- Java implementation: portability and clean design.
- Completely open architecture from head to toe.
- High level representation.
- Evolving infrastructure (e.g. new built-in analyses).

Perceived weaknesses

- Complex IR.
- Performance.

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